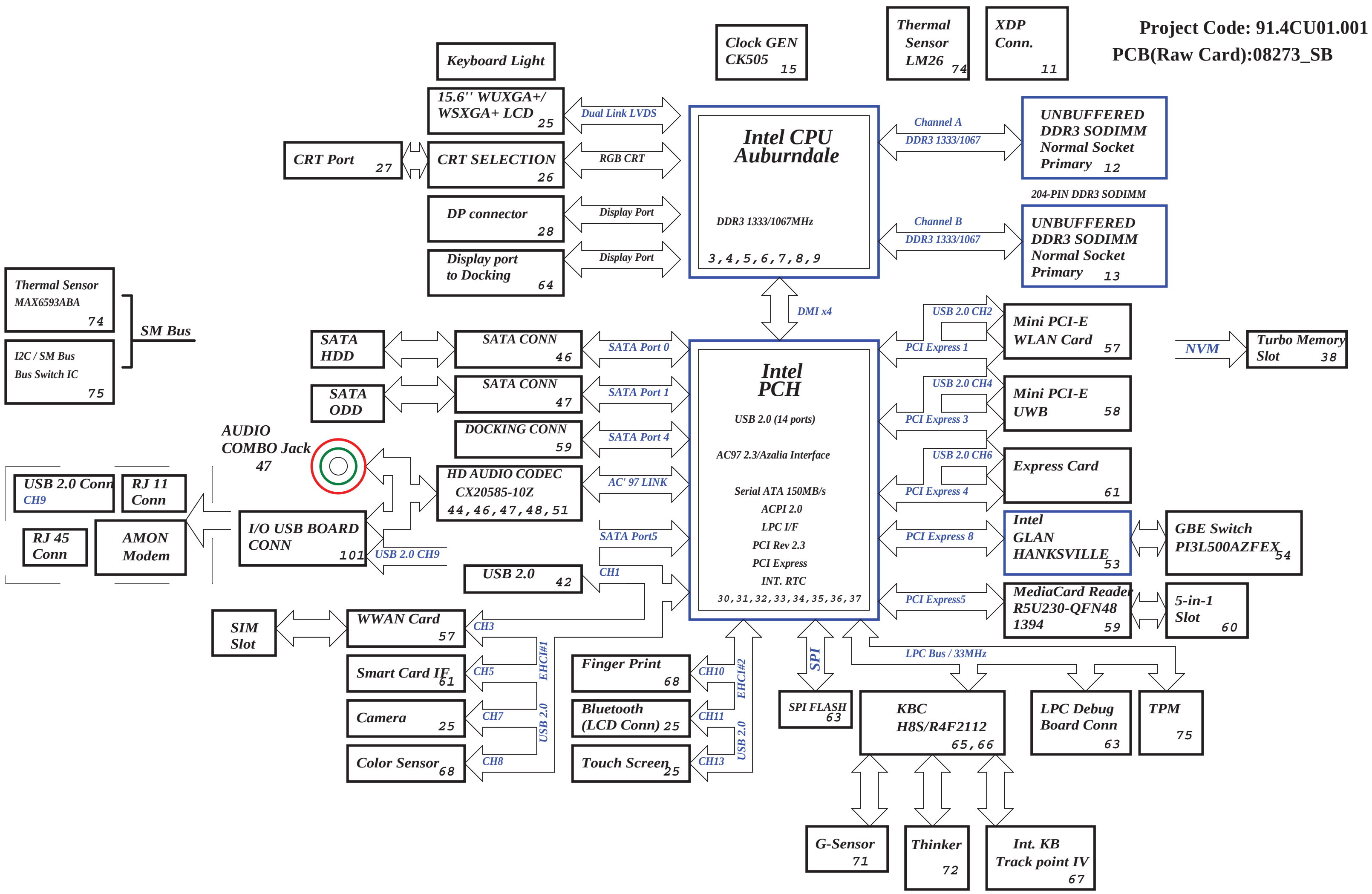
March. ' 9

Kendo-1 INT. Graphics Block Diagram

Project Code: 91.4CU01.001

PCB(Raw Card):08273_SB



PCB Layer Stackup

L1:TOP
L2:GND
L3:Signal1
L4:Signal2
L5:VCC
L6:Signal 3
L7:GND
L8:BOTTOM

Battery Charger/Selector

BQ24741RHDR 78

INPUTS	OUTPUTS
DOCK_PWR20_F	M-BAT-PWR S-BAT-PWR

System DC/DC

TPS51222RTV 82

VINT20	VCC5M VCC3M

CPU DC/DC

ADP3212MNR2G 83

VINT20	VCCCPUCORE

VCC1R05LAN/VCC1R5A

VT356 89

VCC5V_OUT	VCC1R05AMT VCC1R5A

VCC0R75B

MAX1510ETB 89

VCC1R5A	DDR3_VREF VCC0R75B

VCC1R8B

BD3551 92

VCC3M VCC5M	VCC1R8B

BOM1

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Title
Block Diagram

Size	Document Number	Rev
Custom	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 1 of 104

RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance
For the value, it can be read by the number before R. (R means resistor)
For the tolerance, it can be read from the last letter.
For the rating, we don't show on the symbol name.
For the size, R2=>0402, R3=>0603, R5=>0805,....

CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is
Capacitor type + value + rating + size + tolerance + material
SCD1U10V2MX-1
SC=> SMT Ceremic, TC=> POS cap or SP cap
D1U => 0.1uF
10V => the voltage rating is 10V
2=> 0402, 3=>0603, 5=>0805
M=>tolerance M, K, Z
X=> X7R/X5R, Y=> Y5V
-1 => symbol version, nonsense to EE characteristic

PLANAR_ID[3..0]

ICH9-M GPIOn	39	38	37	36	Planar ID Version	Planar PCB Version
PLANAR_IDn	3	2	1	0		
	0	0	0	0	Kendo-1 Pre-DV	SA
	0	0	0	1		
	0	0	1	0		
	0	0	1	1		
	0	1	0	0		
	0	1	0	1		
	0	1	1	0		
	0	1	1	1		

EC HISTORY

Stage	Date	EC No.	Page	Note

PCI TABLE

DEVICE	IDSEL	IRQ (Default)	REQ# / GNT#
MINIPCI SLOT	AD18	F, G	REQ# 3/ GNT#3
CARDBUS R5C811	AD16	SERIRQ	REQ#0 / GNT#0
USB UHCI	AD29	A, C, D	
USB 2.0 EHCI	AD29	H	
DMI-to-PCI/ AC97 Modem/ AC97 Audio	AD30	B B	
LPC Bridge IDE SATA SMBus	AD31	C C B	
PCI Express	AD28	A, B, C, D	

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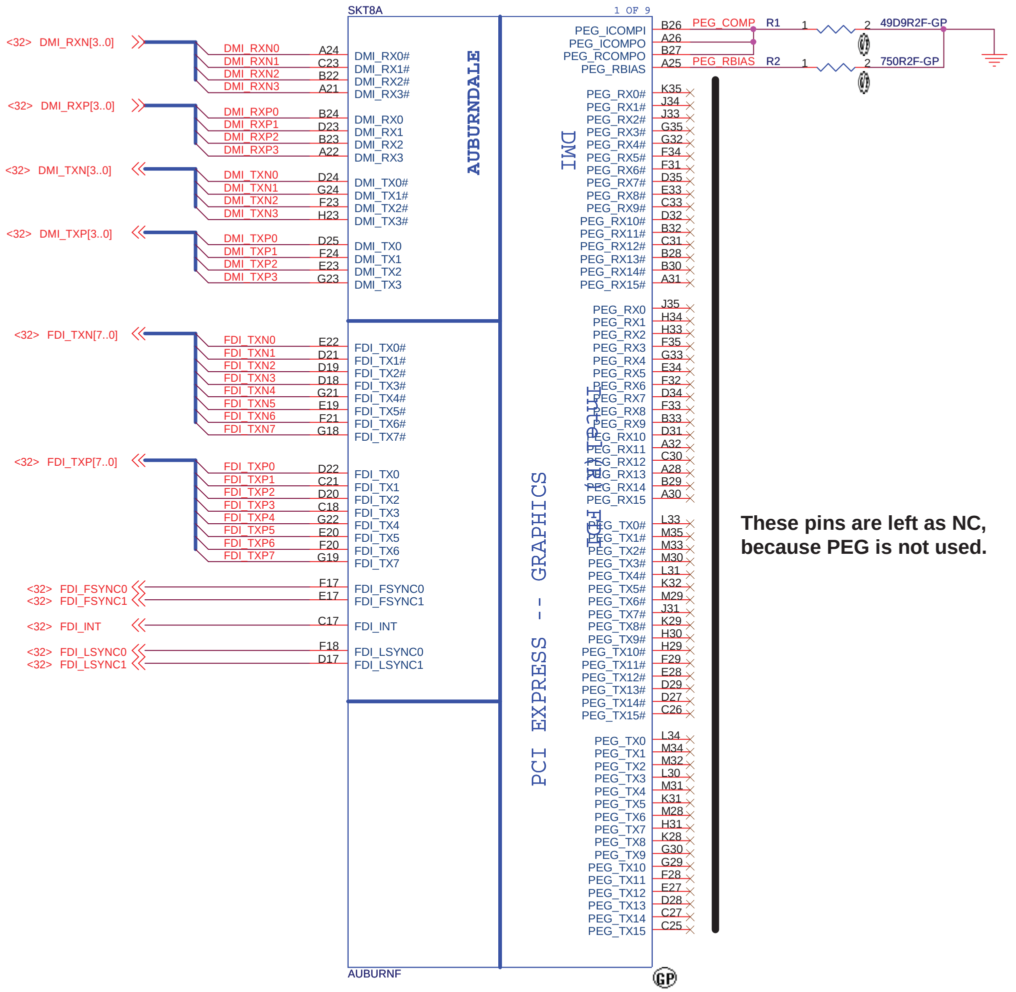
Title
EC HISTORY

Size A3
Document Number **Kendo-1-UMA**
Rev **SB**
Date: Friday, March 13, 2009
Sheet 2 of 104



These pins are left as NC,
because PEG is not used.

BOM1

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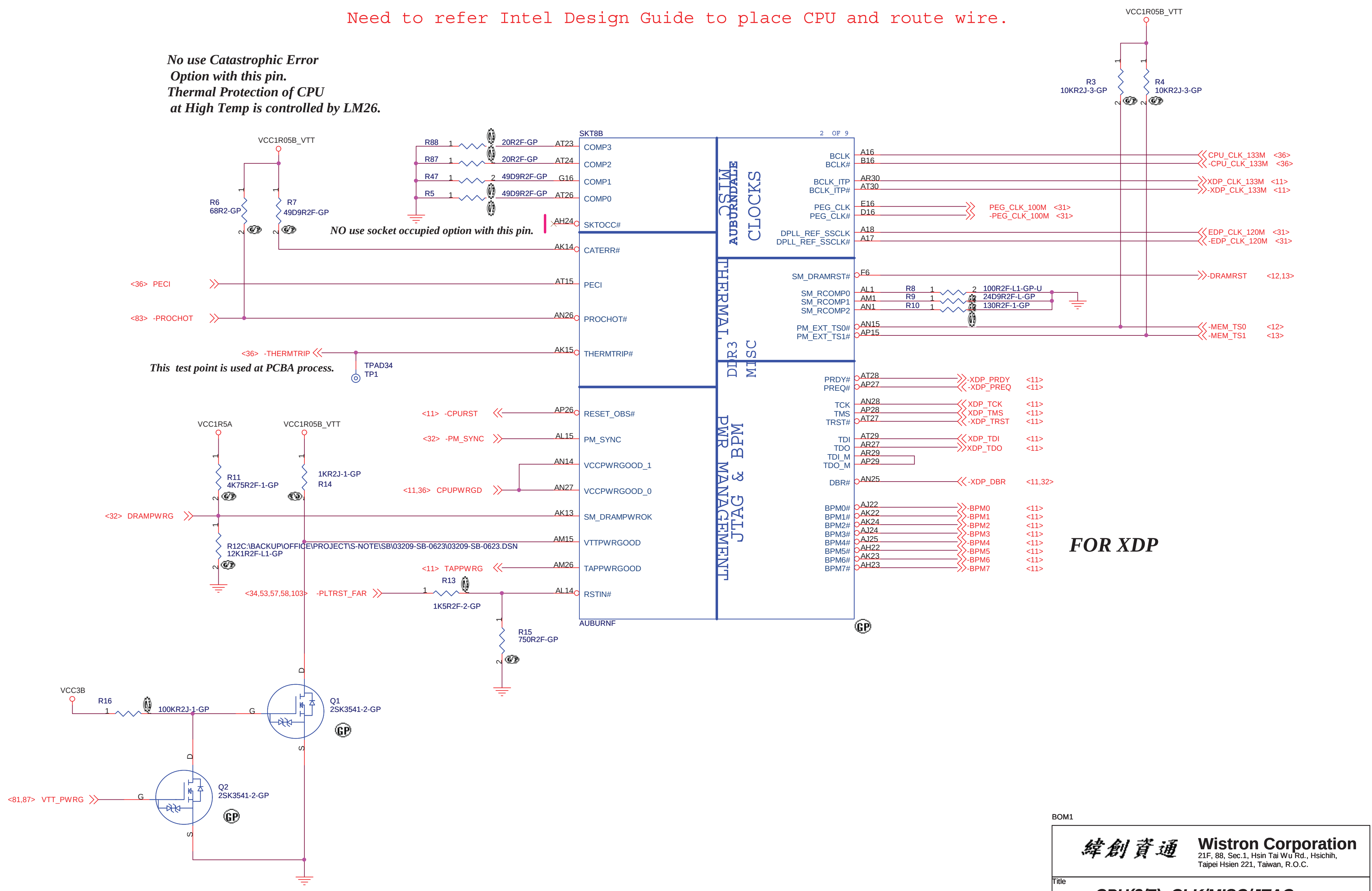
Title
CPU(1/7): DMI/PEG/FDI

Size A3
Document Number **Kendo-1-UMA**
Rev **SB**

Date: Friday, March 13, 2009 Sheet 3 of 104


Need to refer Intel Design Guide to place CPU and route wire.

No use Catastrophic Error Option with this pin. Thermal Protection of CPU at High Temp is controlled by LM26.

NO use socket occupied option with this pin.

This test point is used at PCBA process.

FOR XDP

BOM1

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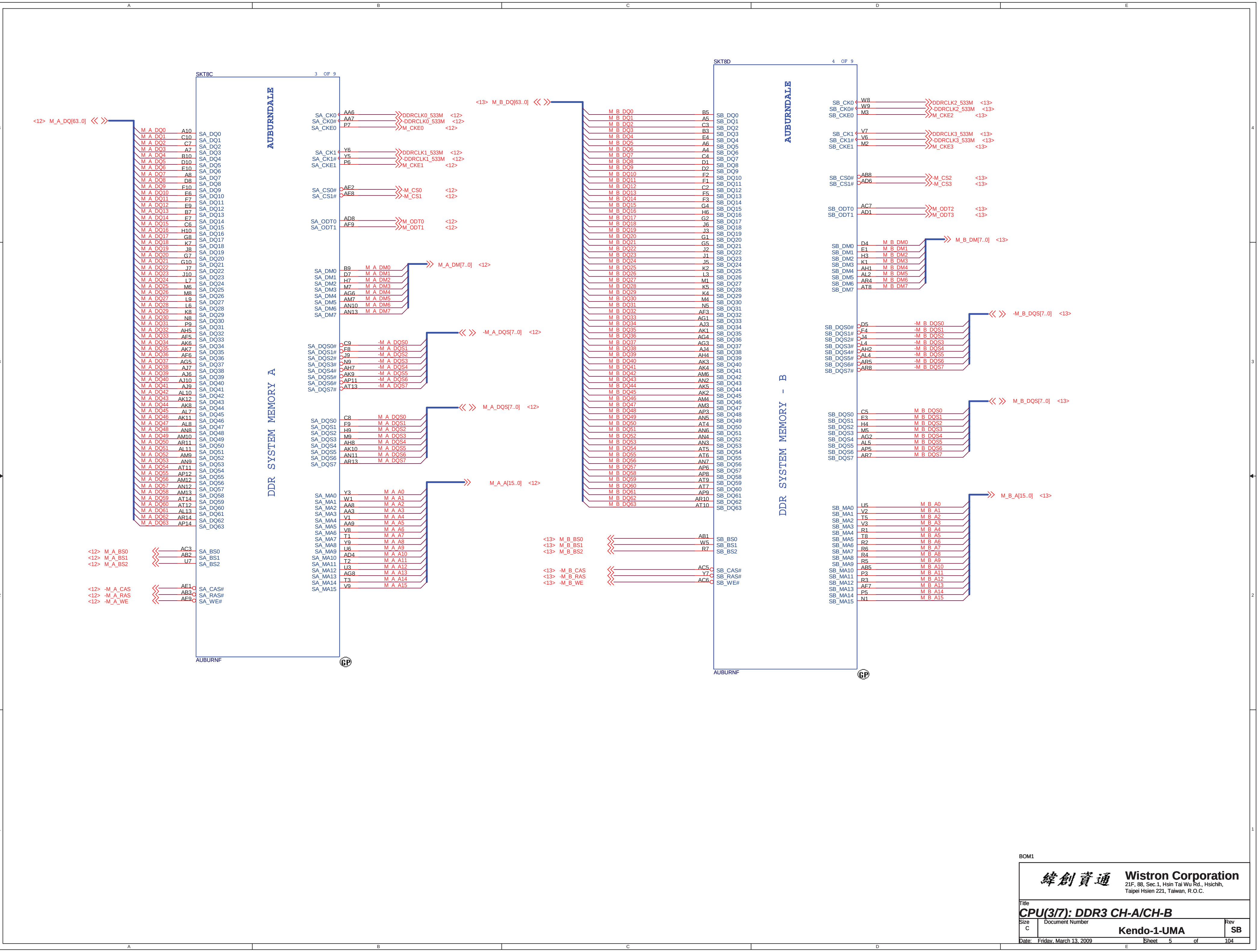
Title		
CPU(2/7): CLK/MISC/JTAG		
Size A3	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 4 of 104	



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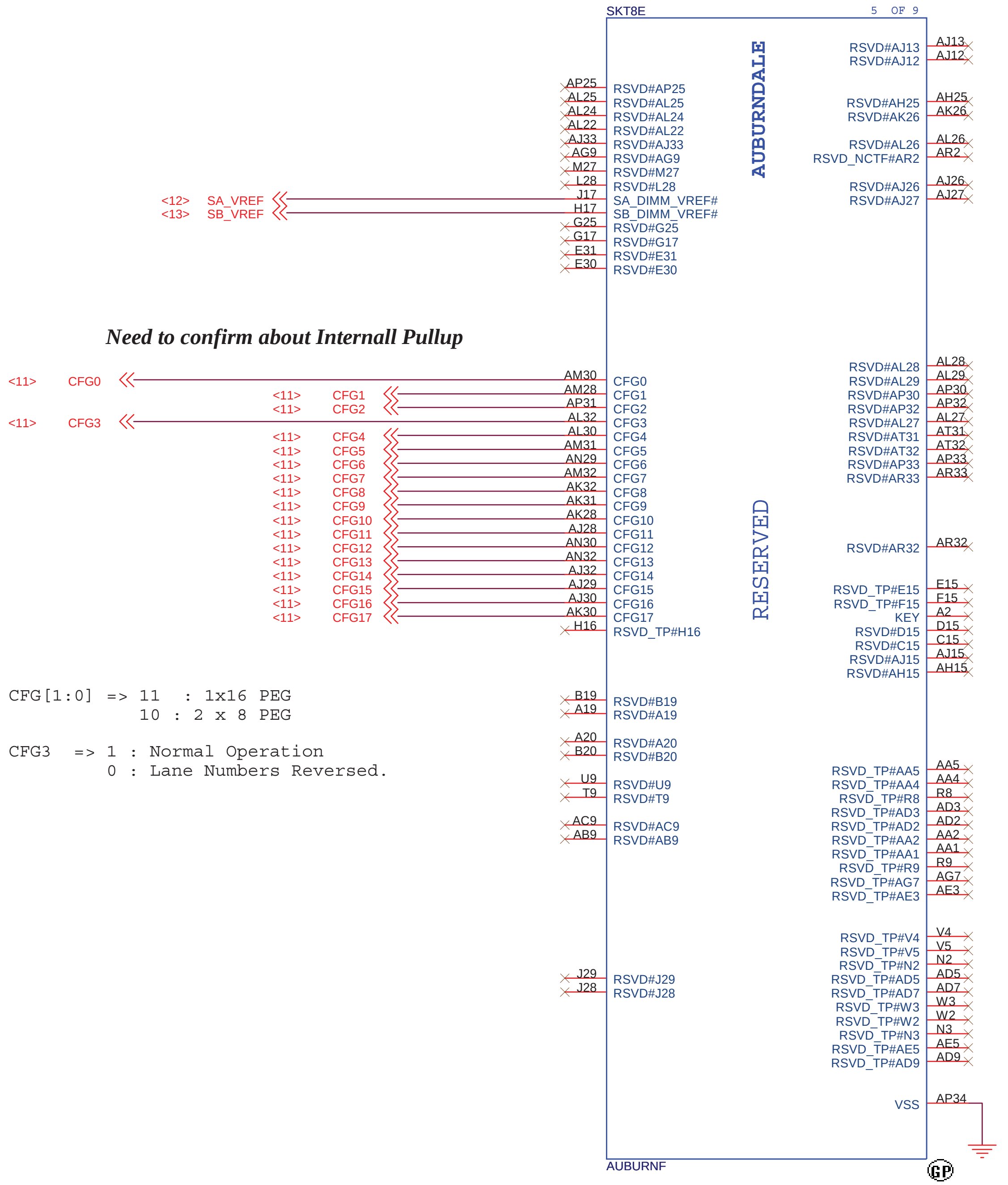
Title
CPU(3/7): DDR3 CH-A/CH-B

Size	Document Number	Rev
C	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 5 of 104



Need to confirm about Internall Pullup

```
CFG[1:0] => 11  : 1x16 PEG
            10 : 2 x 8 PEG

CFG3   => 1 : Normal Operation
          0 : Lane Numbers Reversed.
```

BOM1

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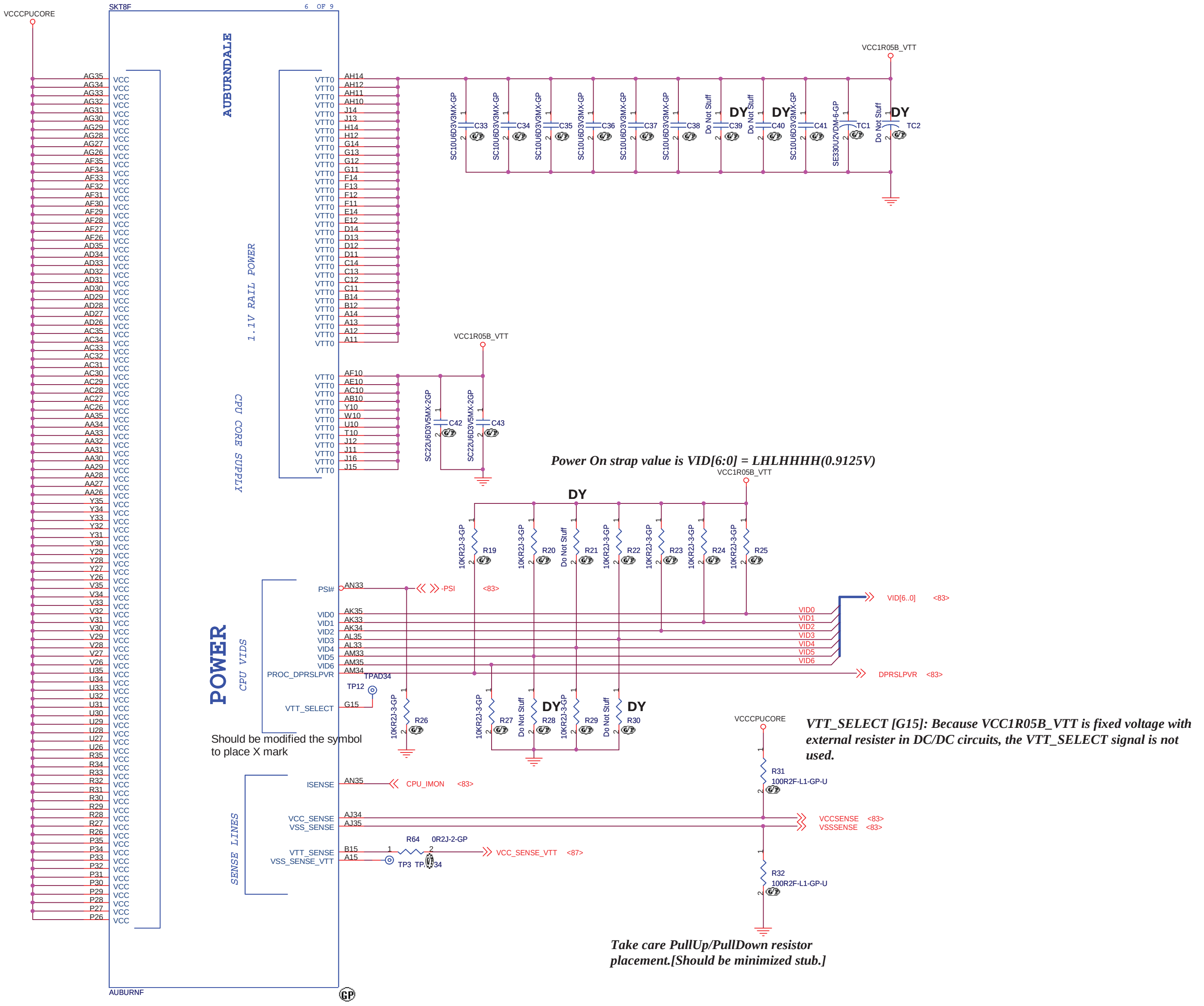
Title
CPU(4/7): CFG/RSVD

Size	Document Number	Rev
Custom	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 6 of 104


VCCCPUCORE
SKT8F
6 OF 9
AUBURNDALE
1.1V RAIL POWER
CPU CORE SUPPLY
POWER
CPU VIDS
SENSE LINES
AG35 AG34 AG33 AG32 AG31 AG30 AG29 AG28 AG27 AG26
AF35 AF34 AF33 AF32 AF31 AF30 AF29 AF28 AF27 AF26
AD35 AD34 AD33 AD32 AD31 AD30 AD29 AD28 AD27 AD26
AC35 AC34 AC33 AC32 AC31 AC30 AC29 AC28 AC27 AC26
AA35 AA34 AA33 AA32 AA31 AA30 AA29 AA28 AA27 AA26
Y35 Y34 Y33 Y32 Y31 Y30 Y29 Y28 Y27 Y26
V35 V34 V33 V32 V31 V30 V29 V28 V27 V26
U35 U34 U33 U32 U31 U30 U29 U28 U27 U26
R35 R34 R33 R32 R31 R30 R29 R28 R27 R26
P35 P34 P33 P32 P31 P30 P29 P28 P27 P26
VCC
VTT0
AH14 AH12 AH11 AH10 J14 J13 H14 H12 G14 G13 G12 G11 F14 F13 F12 F11 E14 E12 D14 D13 D12 D11 C14 C13 C12 C11 B14 B12 A14 A13 A12 A11
AF10 AE10 AC10 AB10 Y10 W10 U10 T10 J12 J11 J16 J15
VCC1R05B_VTT
C33 C34 C35 C36 C37 C38 C39 C40 C41 TC1 TC2
SC10U6D3V3MX-GP
Do Not Stuff
SE330U2VDM-6-GP
DY
C42 C43
SC22U6D3V5MX-2GP
PSI#
AN33
-PSI <83>
VID0 VID1 VID2 VID3 VID4 VID5 VID6
AK35 AK33 AK34 AL35 AL33 AM33 AM35 AM34
VID[6..0] <83>
PROC_DPRSLPVR
DPRSLPVR <83>
TPAD34
TP12
VTT_SELECT
G15
R19 R20 R21 R22 R23 R24 R25
R26 R27 R28 R29 R30
10KR2J-3-GP
Should be modified the symbol
to place X mark
ISENSE
AN35
CPU_IMON <83>
VCC_SENSE
VSS_SENSE
AJ34
AJ35
VCCSENSE <83>
VSSSENSE <83>
VTT_SENSE
VSS_SENSE_VTT
B15
A15
R64
0R2J-2-GP
TP3 TP34
VCC_SENSE_VTT <87>
R31
R32
100R2F-L1-GP-U
AUBURNF
GP

Power On strap value is VID[6:0] = LHLHHHH(0.9125V)

VTT_SELECT [G15]: Because VCC1R05B_VTT is fixed voltage with external resister in DC/DC circuits, the VTT_SELECT signal is not used.

Take care PullUp/PullDown resistor placement.[Should be minimized stub.]

BOM1

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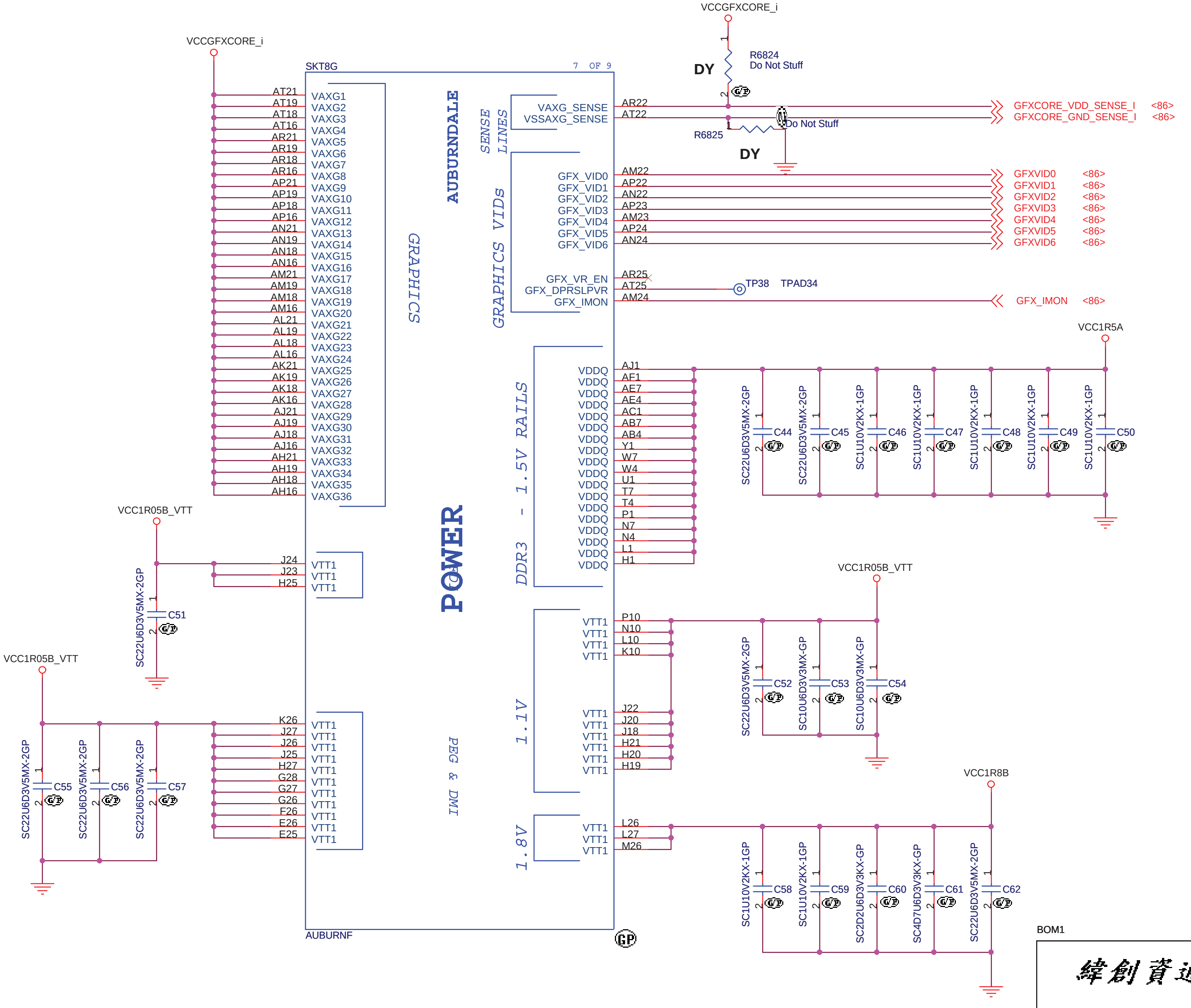
Title
CPU(5/7): VCC1

Size	Document Number	Rev
C	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 7 of 104



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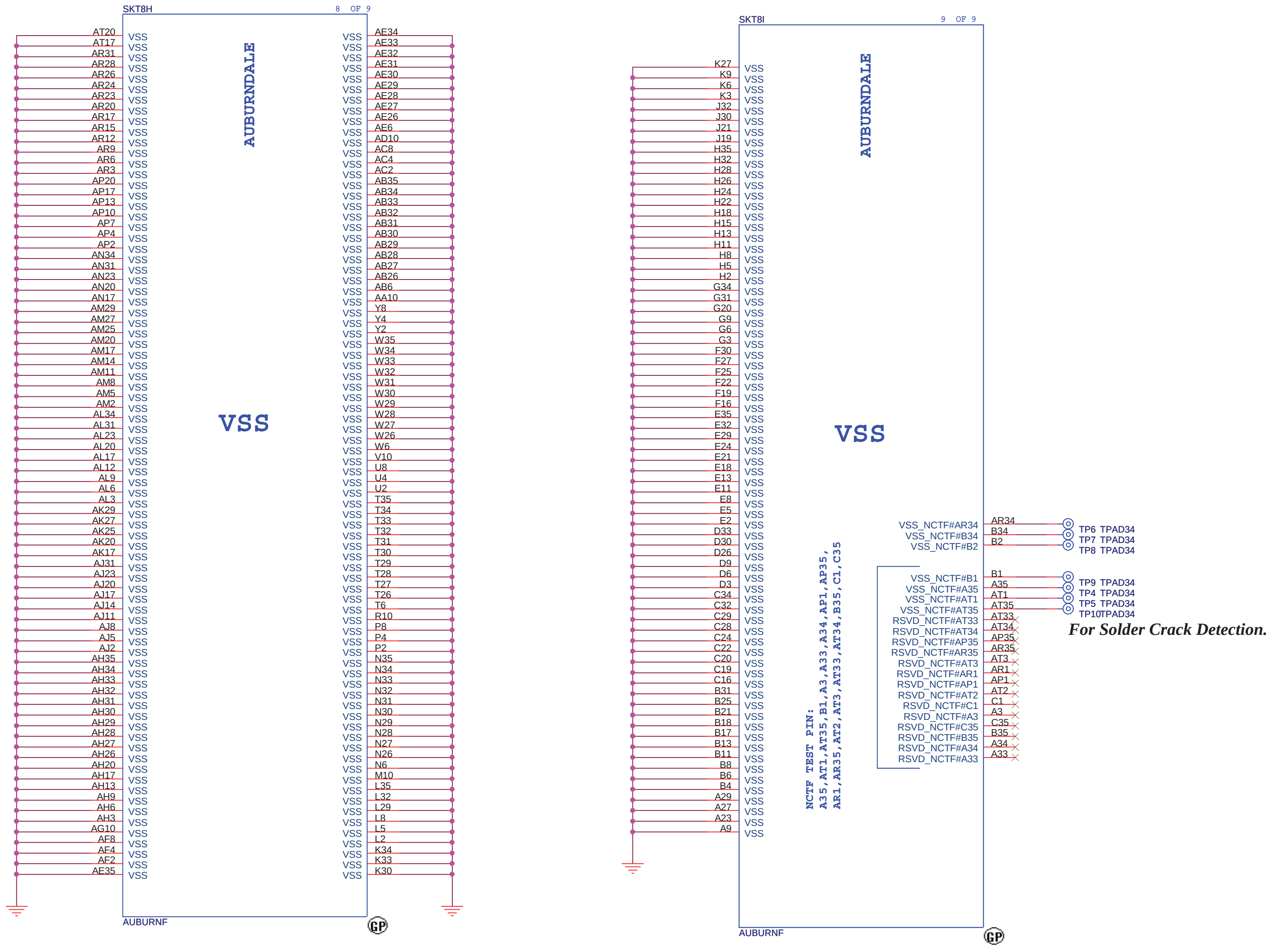
Title
CPU(6/7): VCC2

Size	Document Number	Rev
Custom	**Kendo-1-UMA**	**SB**

Date: Friday, March 13, 2009 Sheet 8 of 104


SKT8H
8 OF 9
AUBURNDALE
VSS
AUBURNF
GP
AT20 AT17 AR31 AR28 AR26 AR24 AR23 AR20 AR17 AR15 AR12 AR9 AR6 AR3 AP20 AP17 AP13 AP10 AP7 AP4 AP2 AN34 AN31 AN23 AN20 AN17 AM29 AM27 AM25 AM20 AM17 AM14 AM11 AM8 AM5 AM2 AL34 AL31 AL23 AL20 AL17 AL12 AL9 AL6 AL3 AK29 AK27 AK25 AK20 AK17 AJ31 AJ23 AJ20 AJ17 AJ14 AJ11 AJ8 AJ5 AJ2 AH35 AH34 AH33 AH32 AH31 AH30 AH29 AH28 AH27 AH26 AH20 AH17 AH13 AH9 AH6 AH3 AG10 AF8 AF4 AF2 AE35
AE34 AE33 AE32 AE31 AE30 AE29 AE28 AE27 AE26 AE6 AD10 AC8 AC4 AC2 AB35 AB34 AB33 AB32 AB31 AB30 AB29 AB28 AB27 AB26 AB6 AA10 Y8 Y4 Y2 W35 W34 W33 W32 W31 W30 W29 W28 W27 W26 W6 V10 U8 U4 U2 T35 T34 T33 T32 T31 T30 T29 T28 T27 T26 T6 R10 P8 P4 P2 N35 N34 N33 N32 N31 N30 N29 N28 N27 N26 N6 M10 L35 L32 L29 L8 L5 L2 K34 K33 K30
SKT8I
9 OF 9
AUBURNDALE
VSS
AUBURNF
GP
K27 K9 K6 K3 J32 J30 J21 J19 H35 H32 H28 H26 H24 H22 H18 H15 H13 H11 H8 H5 H2 G34 G31 G20 G9 G6 G3 F30 F27 F25 F22 F19 F16 E35 E32 E29 E24 E21 E18 E13 E11 E8 E5 E2 D33 D30 D26 D9 D6 D3 C34 C32 C29 C28 C24 C22 C20 C19 C16 B31 B25 B21 B18 B17 B13 B11 B8 B6 B4 A29 A27 A23 A9
VSS
NCTF TEST PIN:
A35,AT1,AT35,B1,A3,A33,A34,AP1,AP35,
AR1,AR35,AT2,AT3,AT33,AT34,B35,C1,C35
VSS_NCTF#AR34 AR34 TP6 TPAD34
VSS_NCTF#B34 B34 TP7 TPAD34
VSS_NCTF#B2 B2 TP8 TPAD34
VSS_NCTF#B1 B1 TP9 TPAD34
VSS_NCTF#A35 A35 TP4 TPAD34
VSS_NCTF#AT1 AT1 TP5 TPAD34
VSS_NCTF#AT35 AT35 TP10 TPAD34
RSVD_NCTF#AT33 AT33
RSVD_NCTF#AT34 AT34
RSVD_NCTF#AP35 AP35
RSVD_NCTF#AR35 AR35
RSVD_NCTF#AT3 AT3
RSVD_NCTF#AR1 AR1
RSVD_NCTF#AP1 AP1
RSVD_NCTF#AT2 AT2
RSVD_NCTF#C1 C1
RSVD_NCTF#A3 A3
RSVD_NCTF#C35 C35
RSVD_NCTF#B35 B35
RSVD_NCTF#A34 A34
RSVD_NCTF#A33 A33

For Solder Crack Detection.

BOM1

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Title		
CPU(7/7): GND		
Size A3	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 9 of 104	

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BOM1

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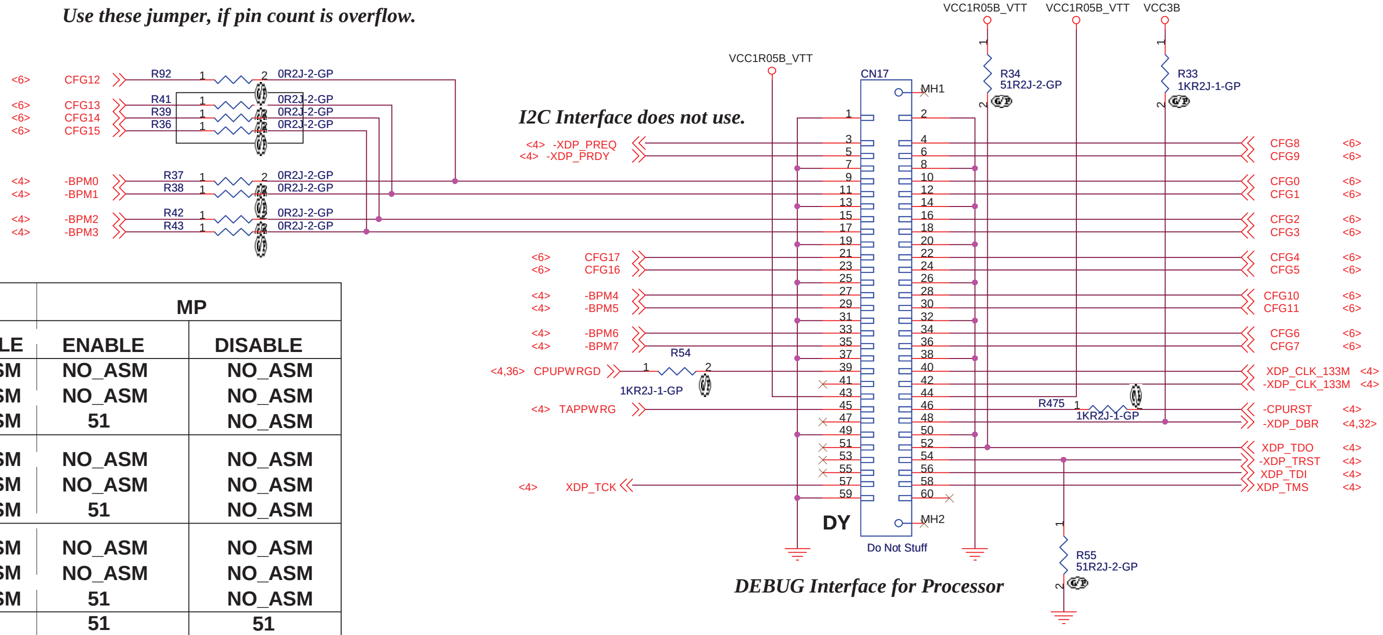
Title		
Size A2	Document Number **Kendo-1-UMA**	Rev **SB**
Date: Friday, March 13, 2009	Sheet 10 of 104	

Please see Intel debug port Guidelines 385422

XDP Present function does not use.

Use these jumper, if pin count is overflow.

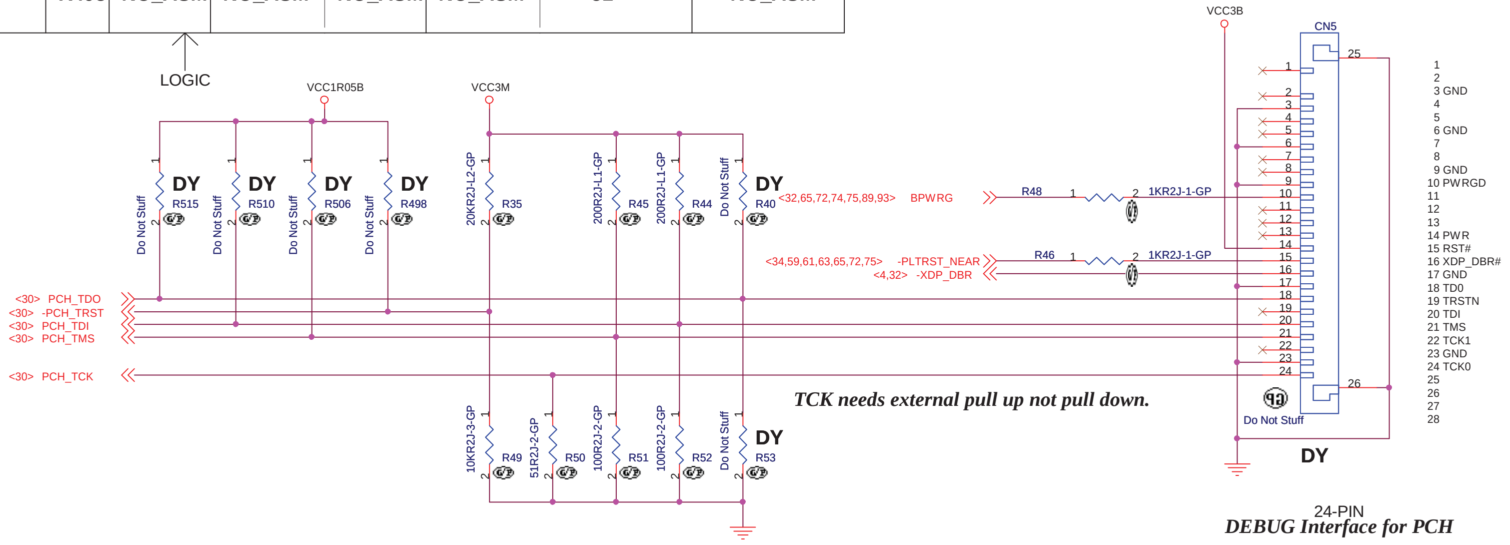


I2C Interface does not use.

DEBUG Interface for Processor

PCH PIN	REF DES	PCH ES1 JTAG		PCH ES2 JTAG		MP	
		ENABLE	DISABLE	ENABLE	DISABLE	ENABLE	DISABLE
TDO	R40	NO_ASM	NO_ASM	200	NO_ASM	NO_ASM	NO_ASM
	R53	NO_ASM	NO_ASM	100	NO_ASM	NO_ASM	NO_ASM
	R515	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TMS	R37	200	NO_ASM	200	NO_ASM	NO_ASM	NO_ASM
	R51	100	NO_ASM	100	NO_ASM	NO_ASM	NO_ASM
	R510	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TDI	R38	200	20K	200	NO_ASM	NO_ASM	NO_ASM
	R52	100	10K	100	NO_ASM	NO_ASM	NO_ASM
	R506	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM
TCK	R50	51	51	51	51	51	51
TRST#	R35	20K	NO_ASM	20K	NO_ASM	NO_ASM	NO_ASM
	R49	10K	NO_ASM	10K	NO_ASM	NO_ASM	NO_ASM
	R498	NO_ASM	NO_ASM	NO_ASM	NO_ASM	51	NO_ASM

LOGIC



TCK needs external pull up not pull down.

24-PIN
DEBUG Interface for PCH

BOM1

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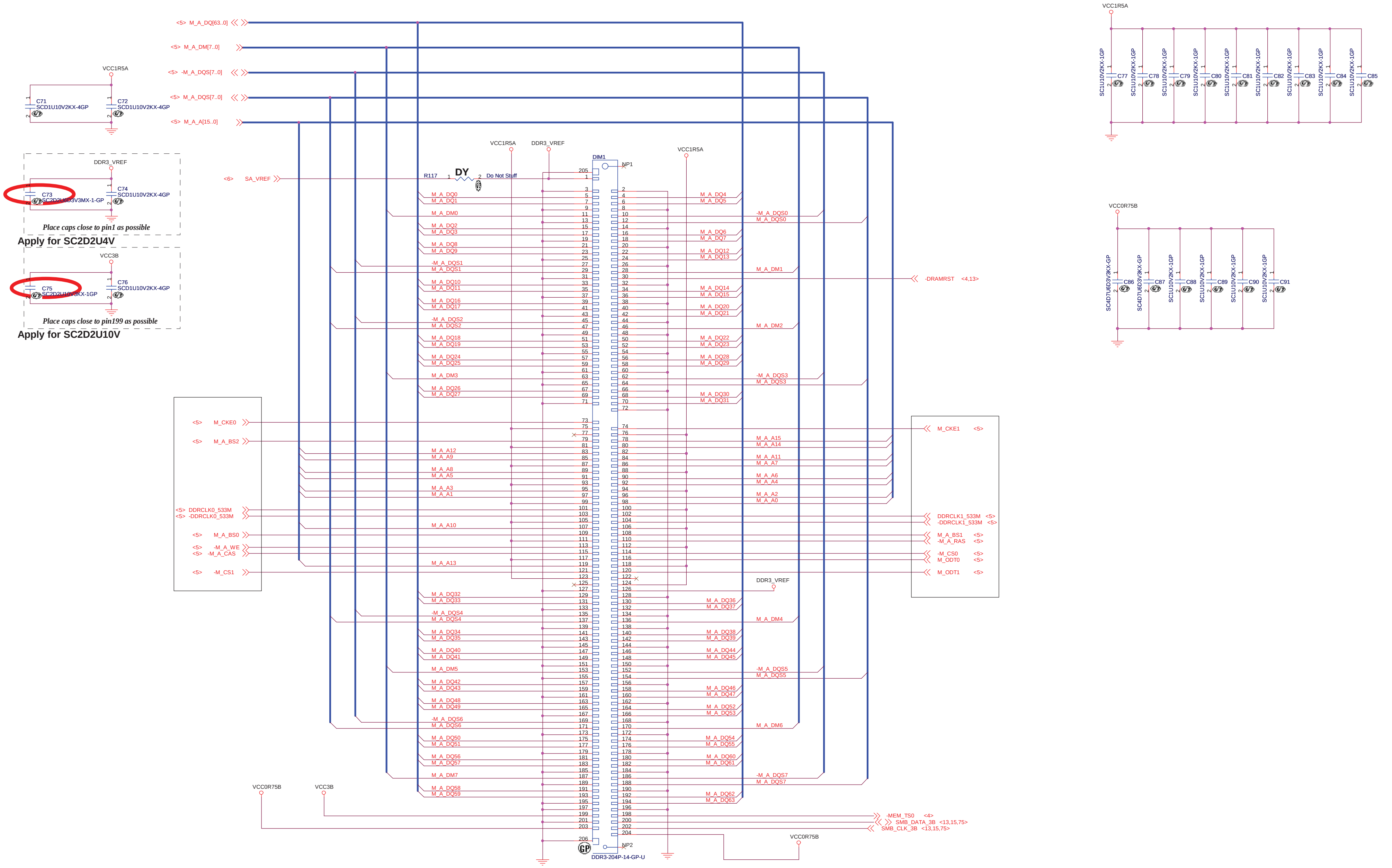
Title	XDP CONNECTOR	
Size A3	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 11 of 104	



SPD Address : 50h

This connector should be placed on far side from CPU.

BOM1

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Title
DDR3 SODIMM_CH-A_Primary

Size	Document Number	Rev
A2	Kendo-1-UMA	SB

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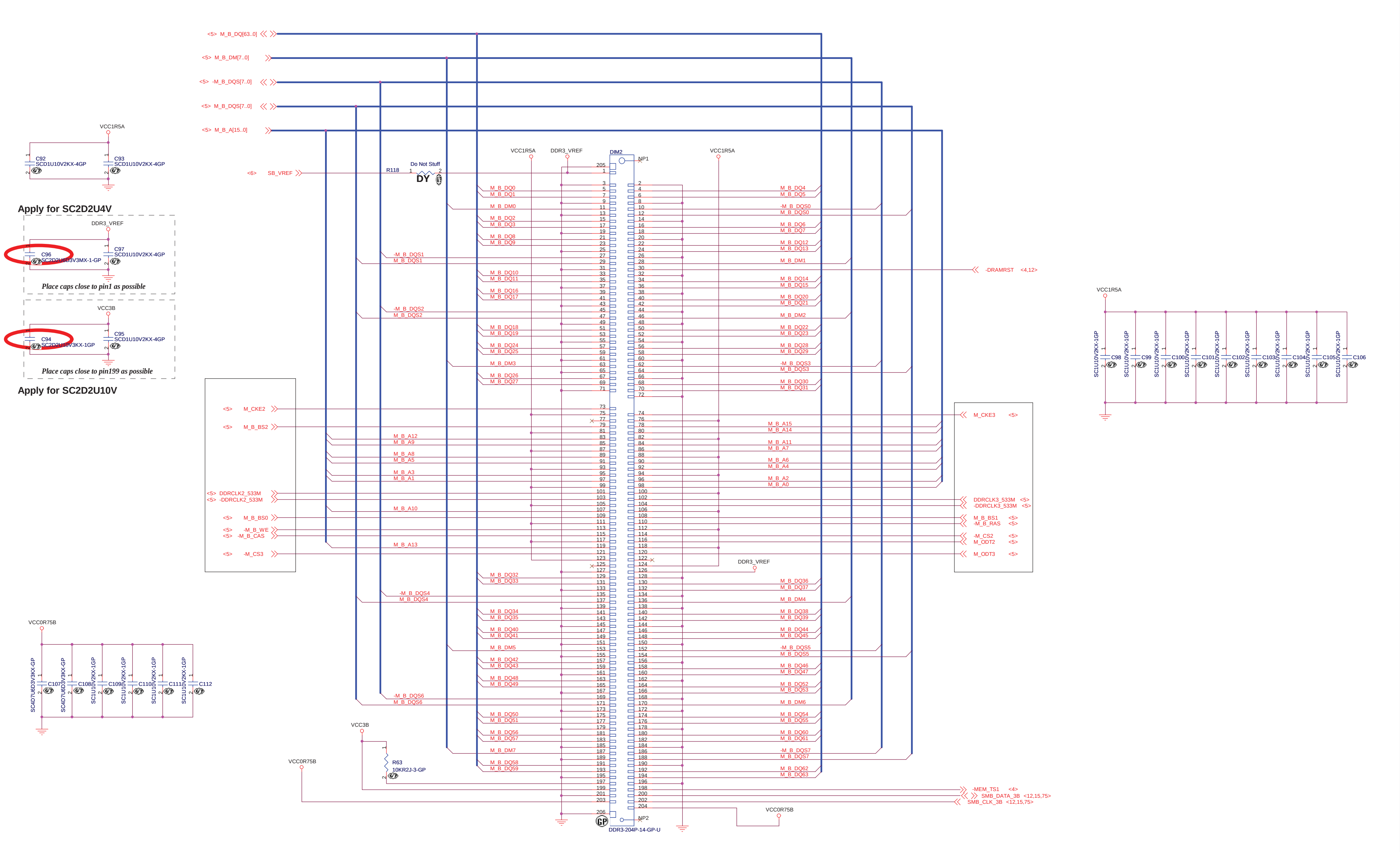

SPD Address : 51h

This connector should be placed on far side from CPU.

BOM1

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Title	**DDR3 SODIMM_CH-B_Primary**	
Size A2	Document Number **Kendo-1-UMA**	Rev SB
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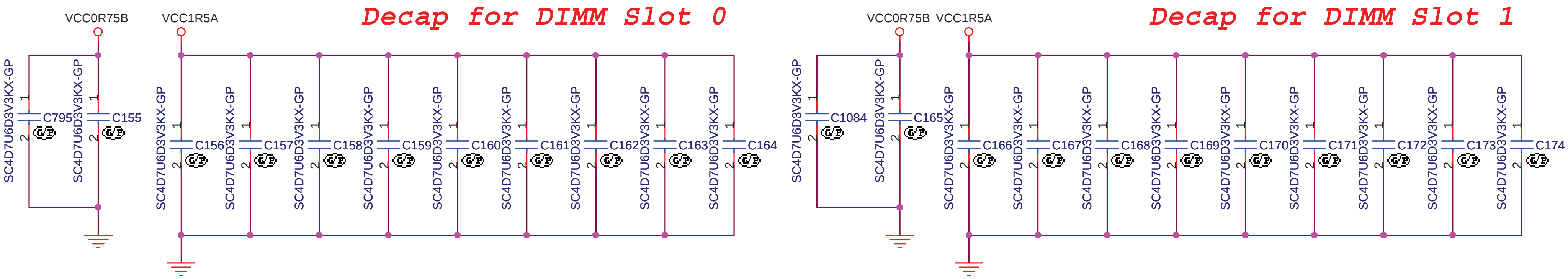

BOM1

緯創資通

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Title
DECAP For DIMMs

Size	Document Number	Rev
A4	Kendo-1-UMA	SB

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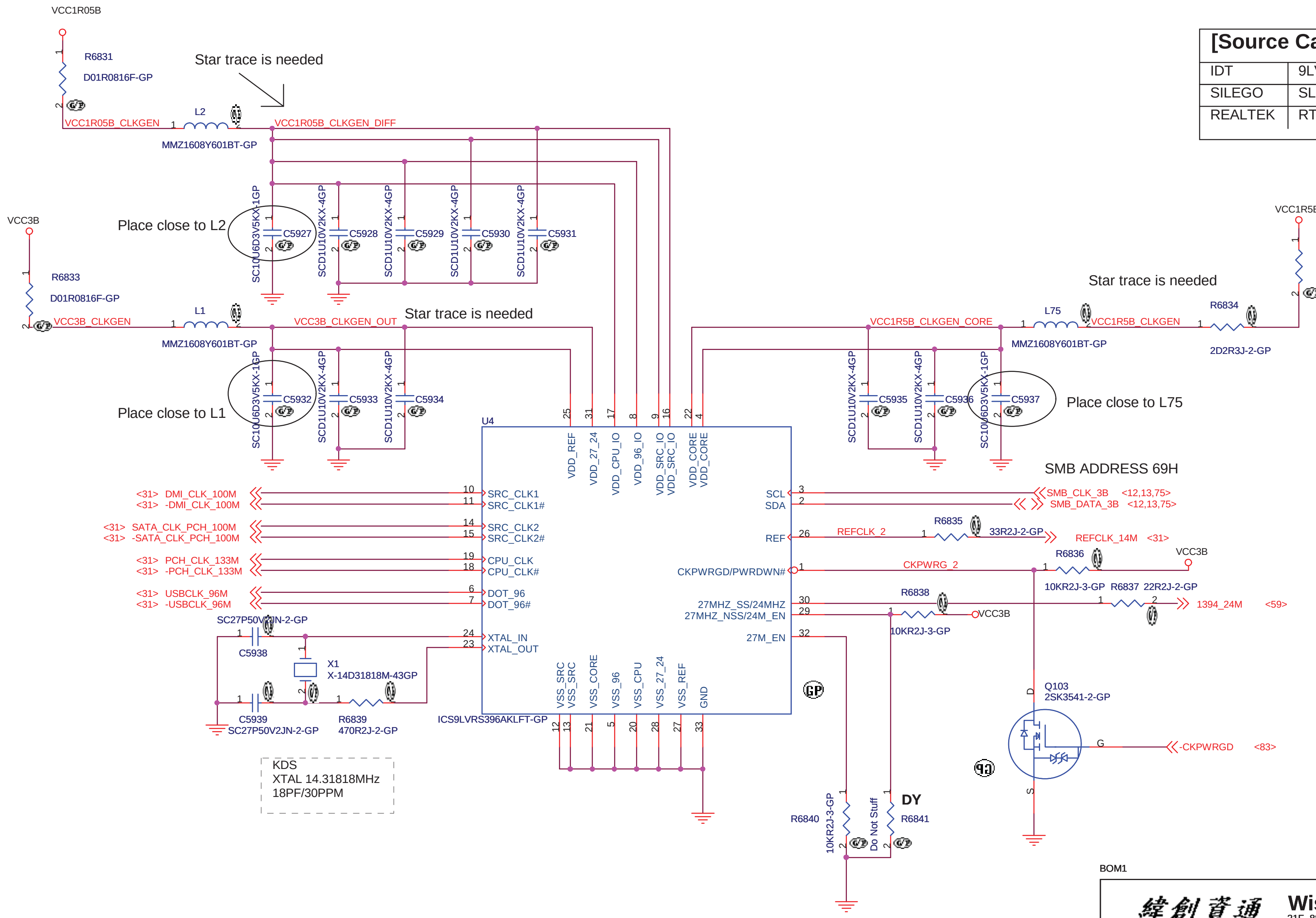
[Source Candidate]	
IDT	9LVRS396AKLF
SILEGO	SLG8LV569
REALTEK	RTM890N-634



BOM1

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Title
CLOCK GEN(CK505)

Size B
Document Number **Kendo-1-UMA**
Rev **SB**

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BOM1

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Title
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Size A2
Document Number
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Rev **SB**

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BOM1

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Title
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Size C
Document Number **Kendo-1-UMA**
Rev **SB**

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BOM1

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Title	***BLANK***	
Size C	Document Number **Kendo-1-UMA**	Rev **SB**
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Title ***BLANK***		
Size C	Document Number **Kendo-1-UMA**	Rev **SB**
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BOM1

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Size A	Document Number **Kendo-1-UMA**	Rev **SB**
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Title
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Document Number **Kendo-1-UMA**
Rev **SB**

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BOM1

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Title	***BLANK***	
Size A	Document Number **Kendo-1-UMA**	Rev **SB**
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Title
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Size C
Document Number **Kendo-1-UMA**
Rev **SB**

Date: Friday, March 13, 2009

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Title
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Size C
Document Number
Kendo-1-UMA
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Date: Friday, March 13, 2009
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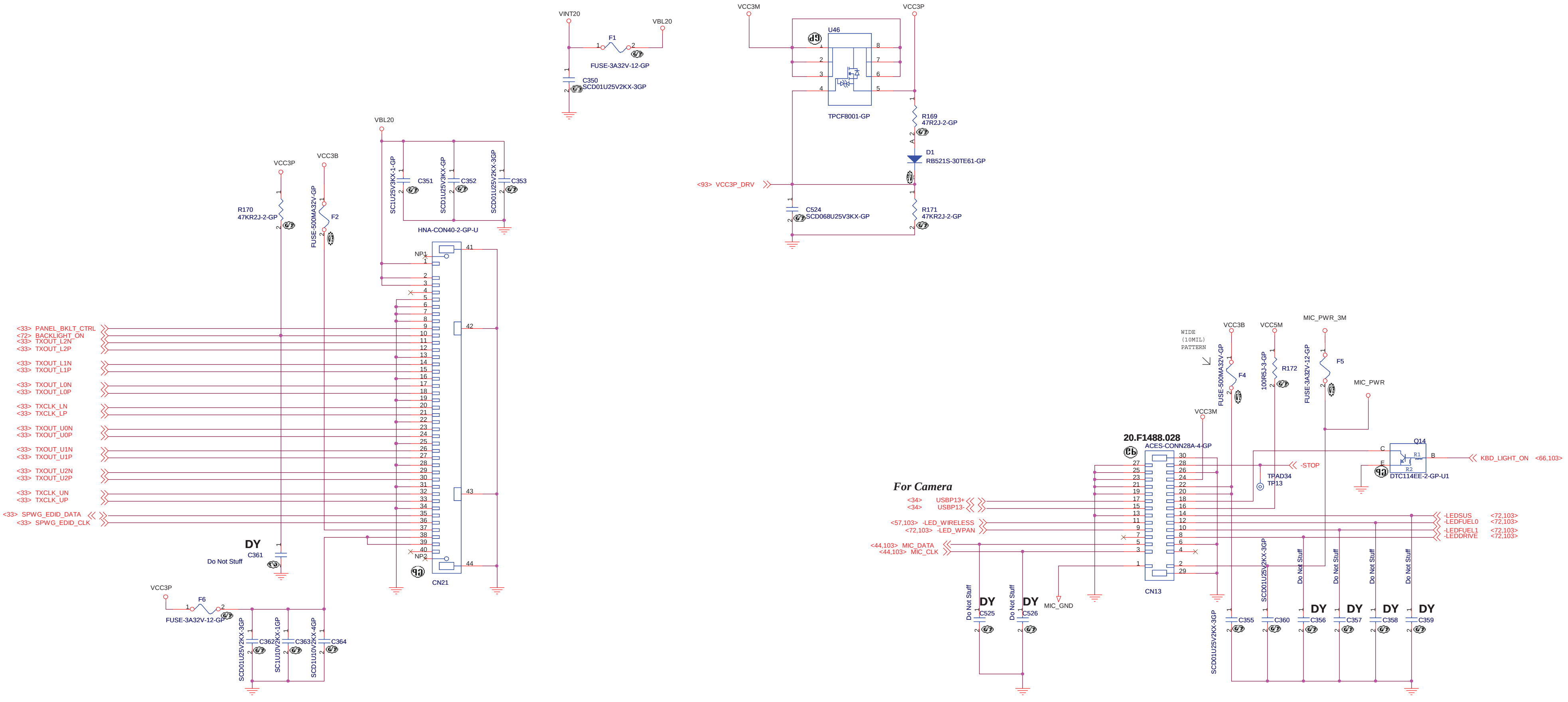
LCD / Inverter Connector



BOM1

緯創資通 **Wistron Corporation**
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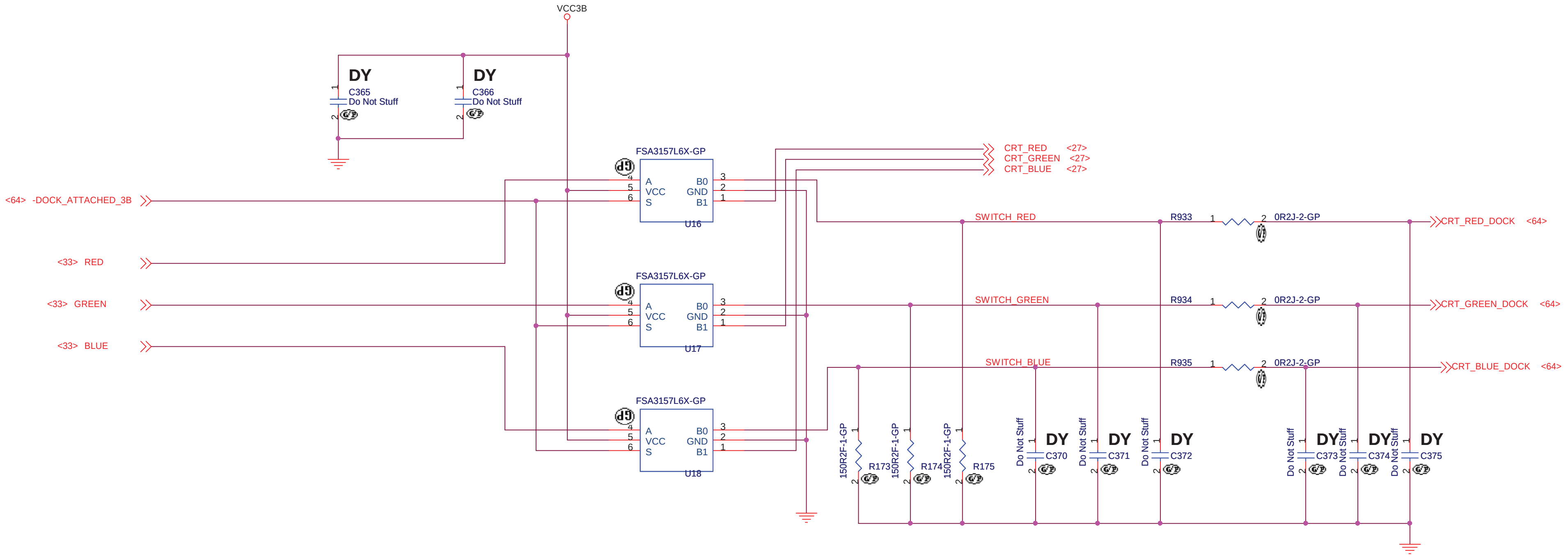
Taipei Hsien 221, Taiwan, R.O.C.

Title	*LCD CONNECTOR*		
Size A2	Document Number Kendo-1-UMA		Rev SB
Date: Friday, March 13, 2009	Sheet 25	of 104	



BOM1

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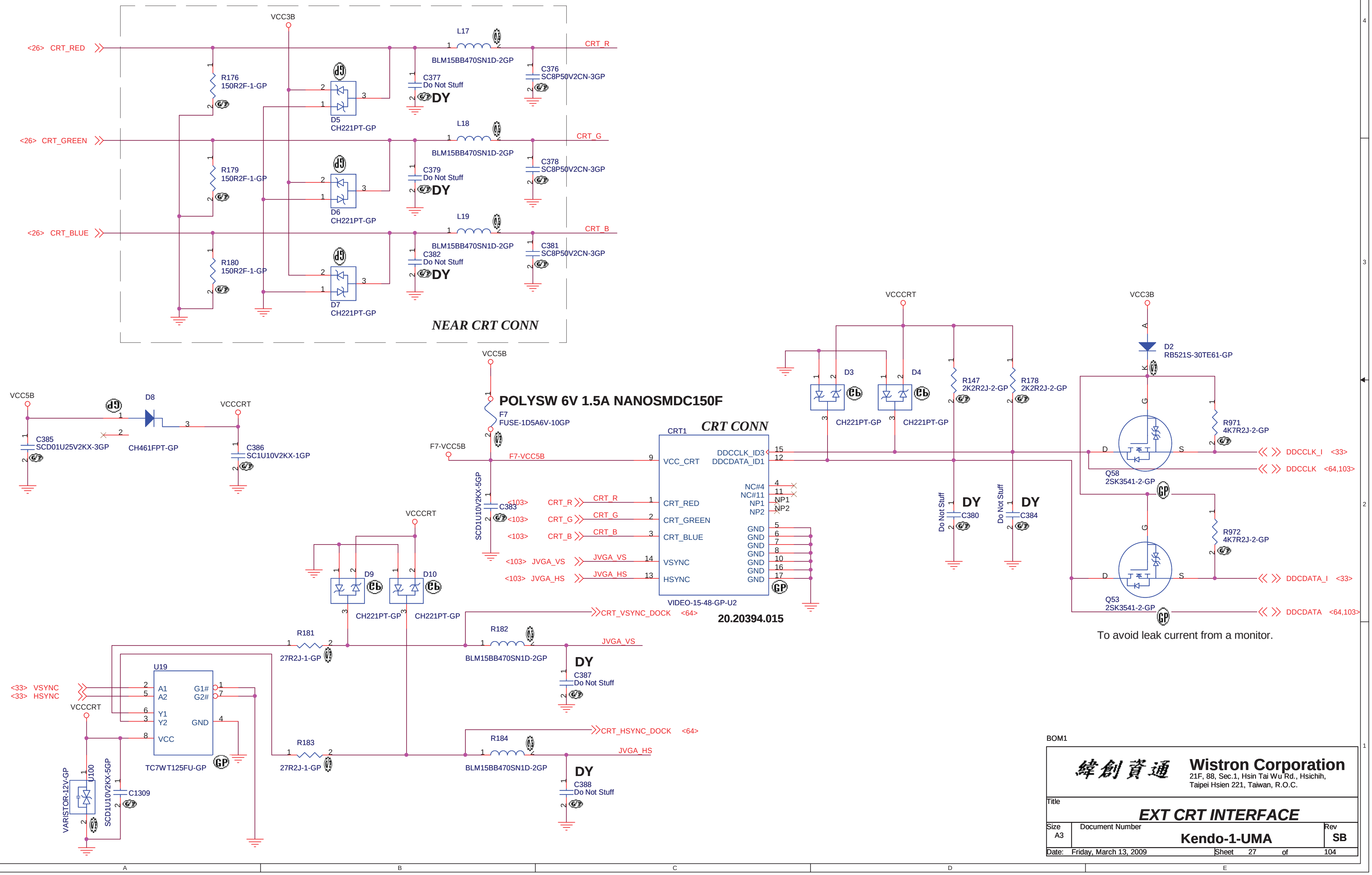
Taipei Hsien 221, Taiwan, R.O.C.

Title
RGB SWITCH

Size	Document Number	Rev
A3	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 26 of 104

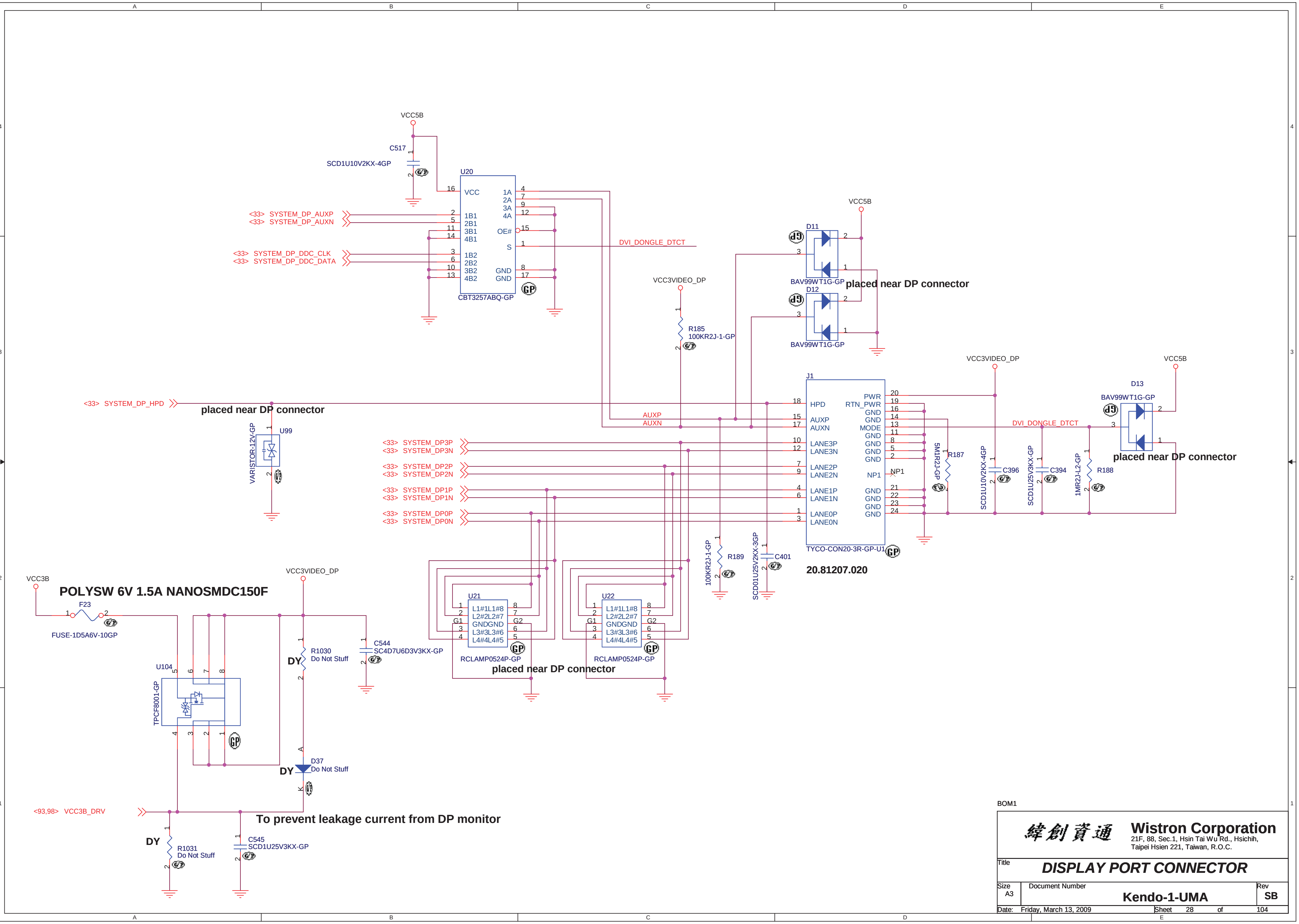


緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
EXT CRT INTERFACE

Size	Document Number	Rev
A3	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 27 of 104



緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
DISPLAY PORT CONNECTOR

Size	Document Number	Rev
A3	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 28 of 104

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BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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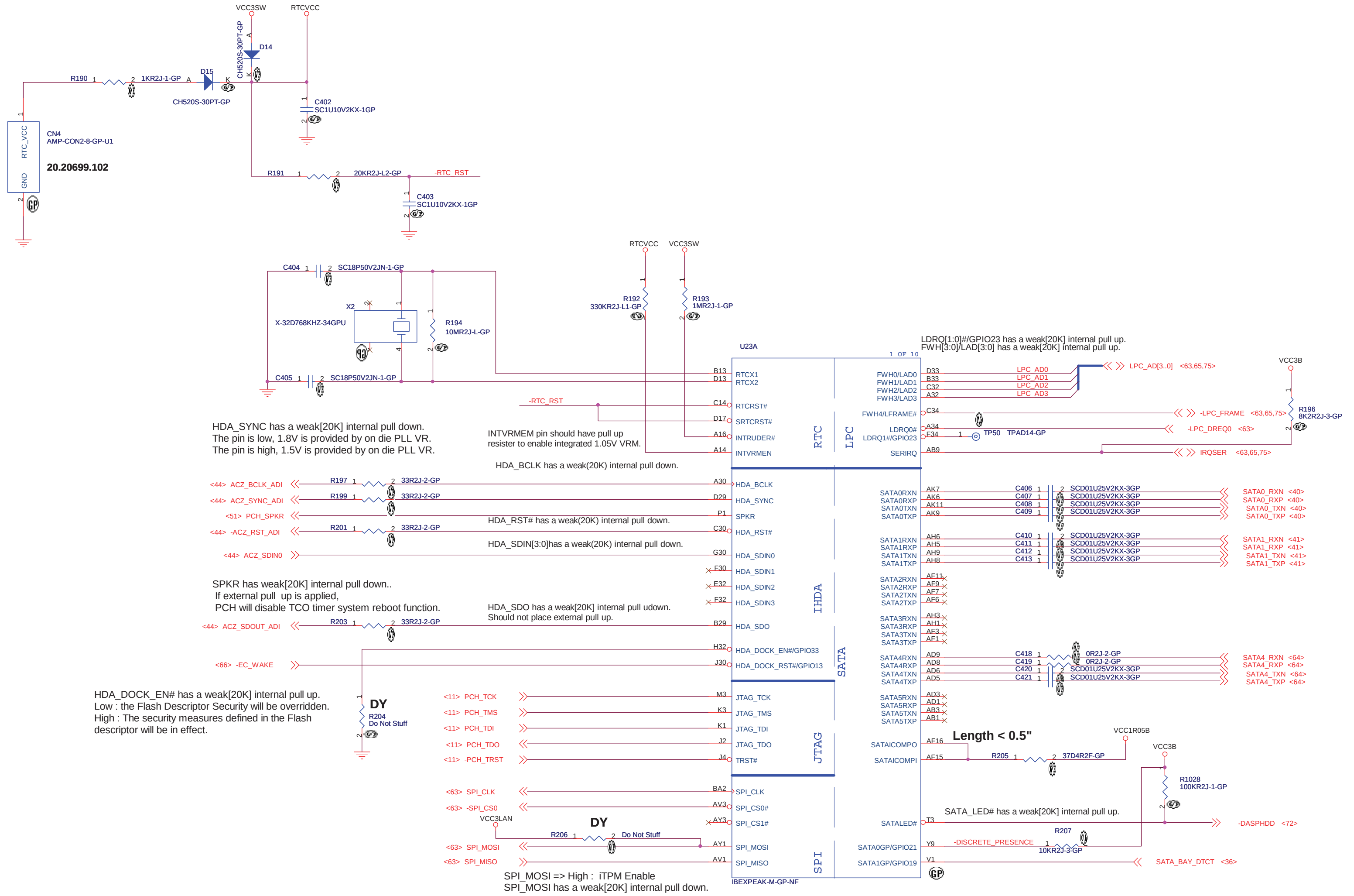
Title
BLANK

Size A
Document Number **Kendo-1-UMA**
Rev **SB**

Date: Friday, March 13, 2009



LDRQ[1:0]#/GPIO23 has a weak[20K] internal pull up.
FWH[3:0]/LAD[3:0] has a weak[20K] internal pull up.

HDA_SYNC has a weak[20K] internal pull down.
The pin is low, 1.8V is provided by on die PLL VR.
The pin is high, 1.5V is provided by on die PLL VR.

INTVRMEM pin should have pull up
resister to enable integrated 1.05V VRM.

HDA_BCLK has a weak(20K) internal pull down.

HDA_RST# has a weak(20K) internal pull down.

HDA_SDIN[3:0]has a weak(20K) internal pull down.

SPKR has weak[20K] internal pull down..
If external pull up is applied,
PCH will disable TCO timer system reboot function.

HDA_SDO has a weak[20K] internal pull udown.
Should not place external pull up.

HDA_DOCK_EN# has a weak[20K] internal pull up.
Low : the Flash Descriptor Security will be overridden.
High : The security measures defined in the Flash
descriptor will be in effect.

SATA_LED# has a weak[20K] internal pull up.

SPI_MOSI => High : iTPM Enable
SPI_MOSI has a weak[20K] internal pull down.

SATA0: To HDD Bay
SATA1: To ODD Bay
SATA2: To HDD Bay
SATA3: Reserved
SATA4: To Docking Connector
SATA5: To eSATA Combo Connector
Note: SATA4 and SATA5 support Multiplier

BOM1

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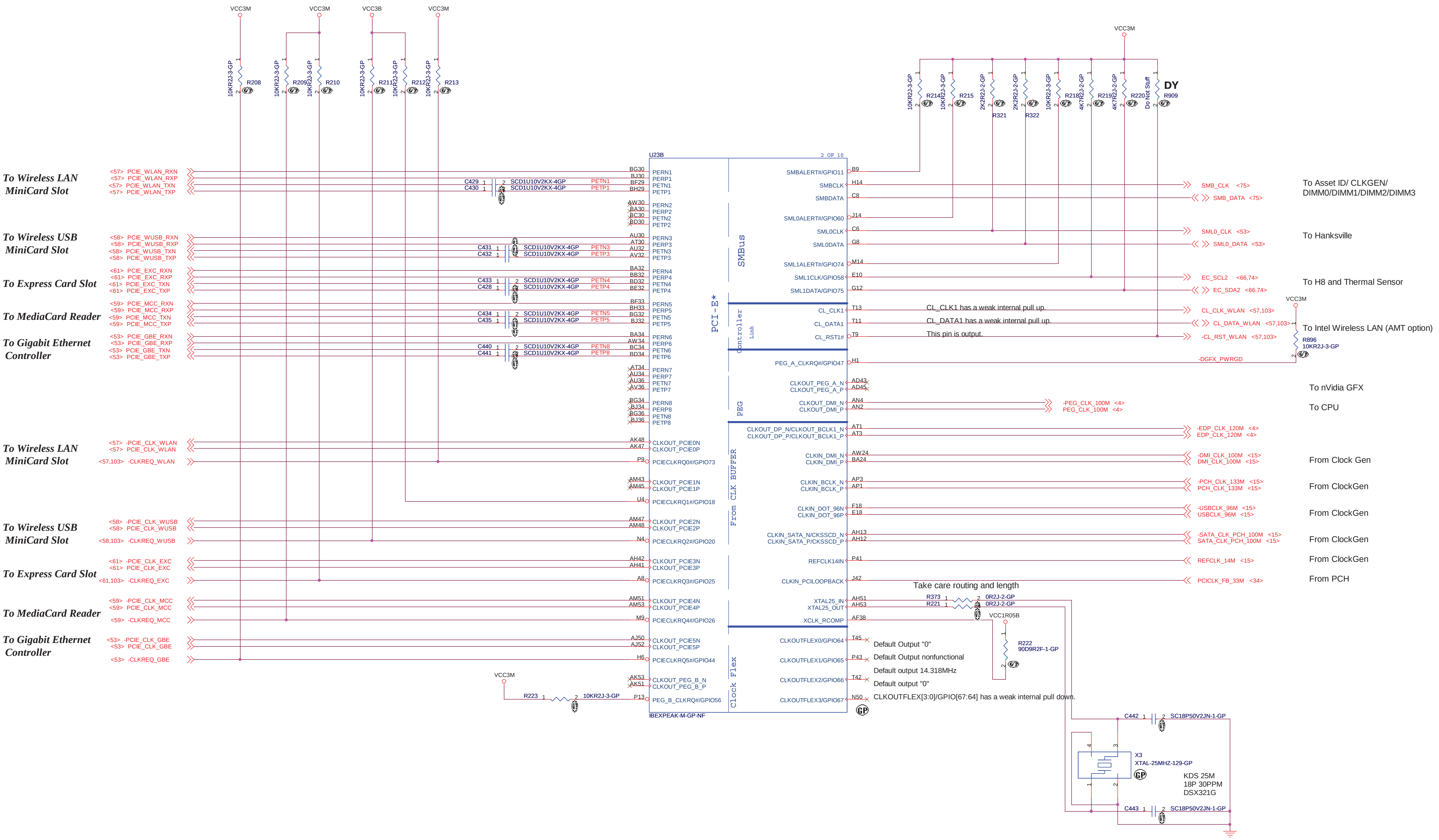
Title	**PCH (1/8):HDA/JTAG/SATA**	
Size A2	Document Number **Kendo-1-UMA**	Rev **SB**
Date: Friday, March 13, 2009	Sheet 30 of 104	

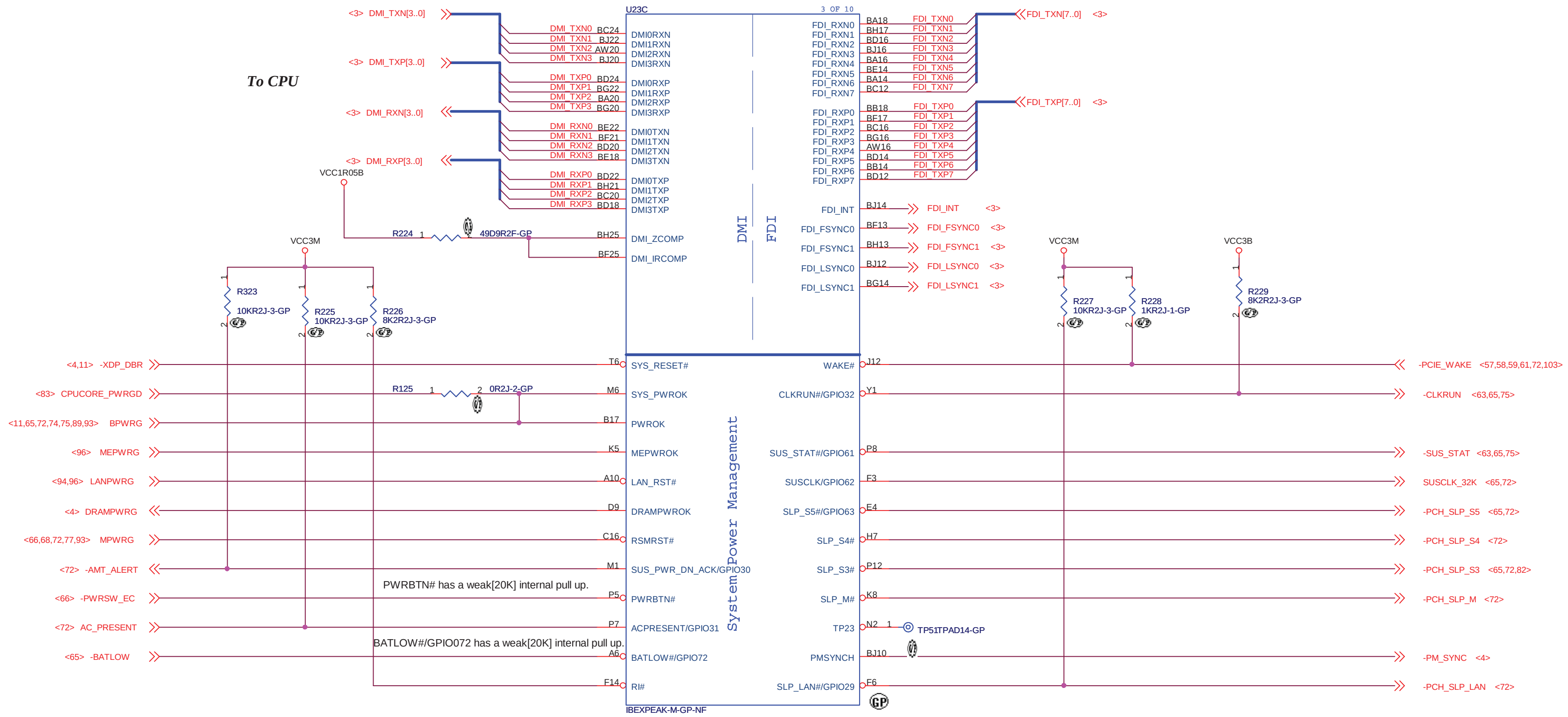


BOM1

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

緯創資通

Title		
PCH(2/8):PCIE/SMBUS/CLK		
Size: A2	Document Number: Kendo-1-UMA	Rev: SB
Date: Friday, March 13, 2009	Sheet 31 of 104	



BOM1

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

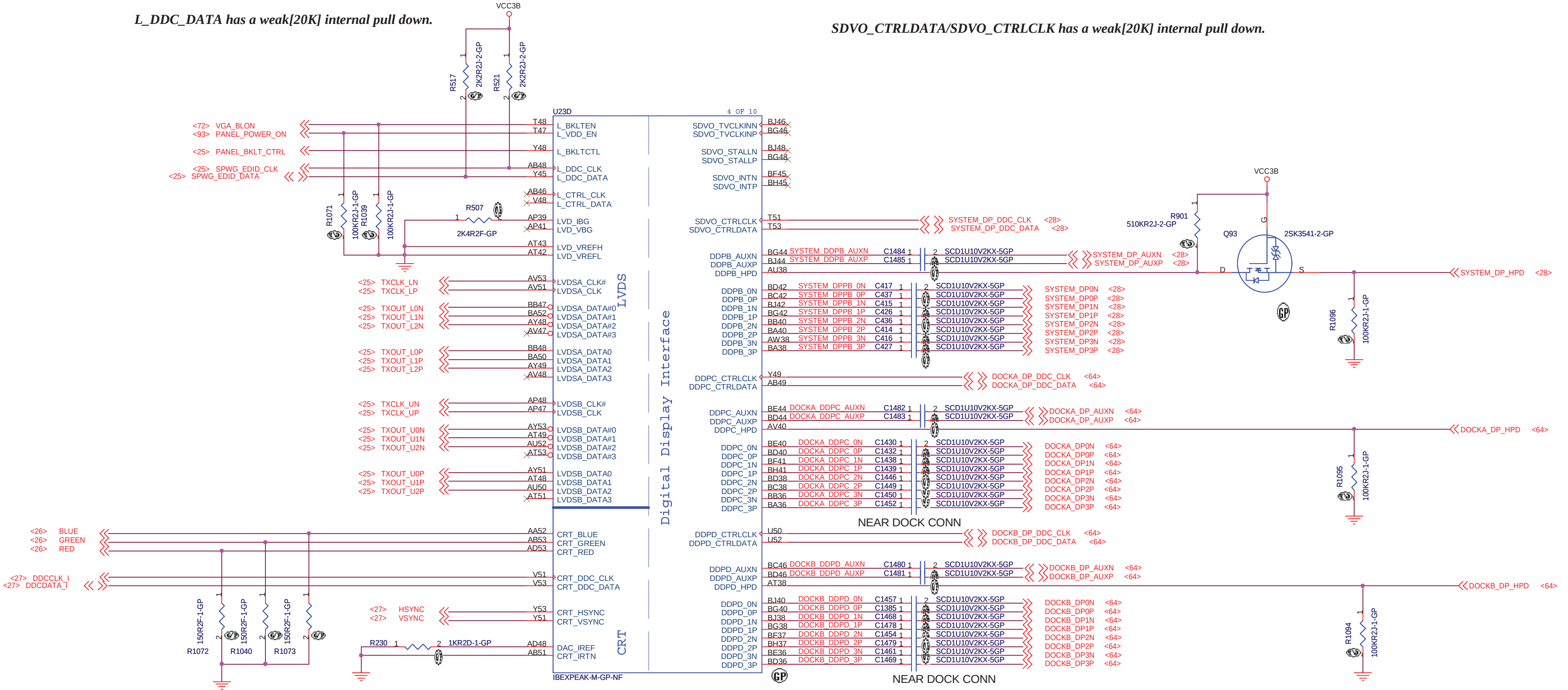
Title
PCH (3/8):DMI/FDI/PM

Size Custom
Document Number **Kendo-1-UMA**
Rev **SB**

Date: Friday, March 13, 2009 Sheet 32 of 104

L_DDC_DATA has a weak[20K] internal pull down.

SDVO_CTRLDATA/SDVO_CTRLCLK has a weak[20K] internal pull down.



DDP[D:C]_CTRLCLK has a weak[10K] internal pull down.
DDP[D:C]_CTRLDATA has a weak[20K] internal pull down.

BOM1

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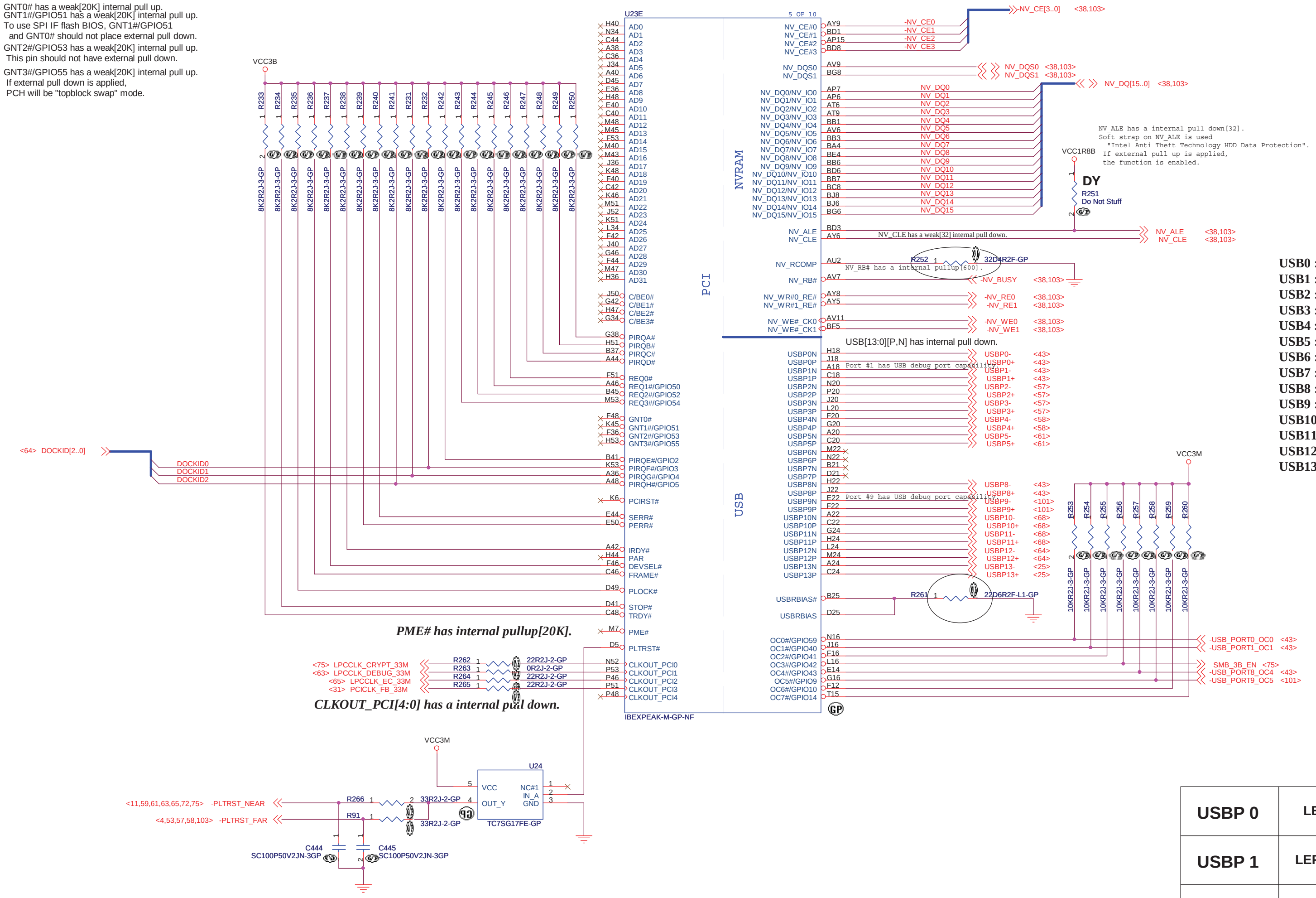
Title		
PCH (4/8):LVDS/CRT/DDI		
Size C	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 33 of 104	

Because PCI IF is not used, ADx.C/BEx and GNTx are left as NC.

GNT0# has a weak[20K] internal pull up.
GNT1#/GPIO51 has a weak[20K] internal pull up.
To use SPI IF flash BIOS, GNT1#/GPIO51
and GNT0# should not place external pull down.
GNT2#/GPIO53 has a weak[20K] internal pull up.
This pin should not have external pull down.
GNT3#/GPIO55 has a weak[20K] internal pull up.
If external pull down is applied,
PCH will be "topblock swap" mode.



NV_ALE has a internal pull down[32].
Soft strap on NV_ALE is used
"Intel Anti Theft Technology HDD Data Protection".
If external pull up is applied,
the function is enabled.

NV_CLE has a weak[32] internal pull down.

NV_RB# has a internal pullup[600].

USB[13:0][P,N] has internal pull down.

Port #1 has USB debug port capability.

Port #9 has USB debug port capability.

PME# has internal pullup[20K].

CLKOUT_PCI[4:0] has a internal pull down.

USB0 : To System USB Port
USB1 : To System onboard USB port(eSATA combo)
USB2 : To WiMAX Mini Card Slot
USB3 : To WWAN Mini Card Slot
USB4 : To WUSB Mini Card Slot
USB5 : To Express Card Slot
USB6 : Reserved
USB7 : Reserved
USB8 : To System USB Port
USB9 : To System Subcard USB port
USB10: To FPR
USB11:To Bluetooth
USB12:To Docking
USB13:To Camera

USBP 0	LEFT DUAL CONN	USB_ON1	-USB_PORT0_OC0
USBP 1	LEFT COMBO CONN	USB_ON1	-USB_PORT1_OC1
USBP 8	LEFT DUAL CONN	USB_ON1	-USB_PORT8_OC4
USBP 9	RIGHT SUB CARD	USB_ON2	-USB_PORT9_OC5

BOM1

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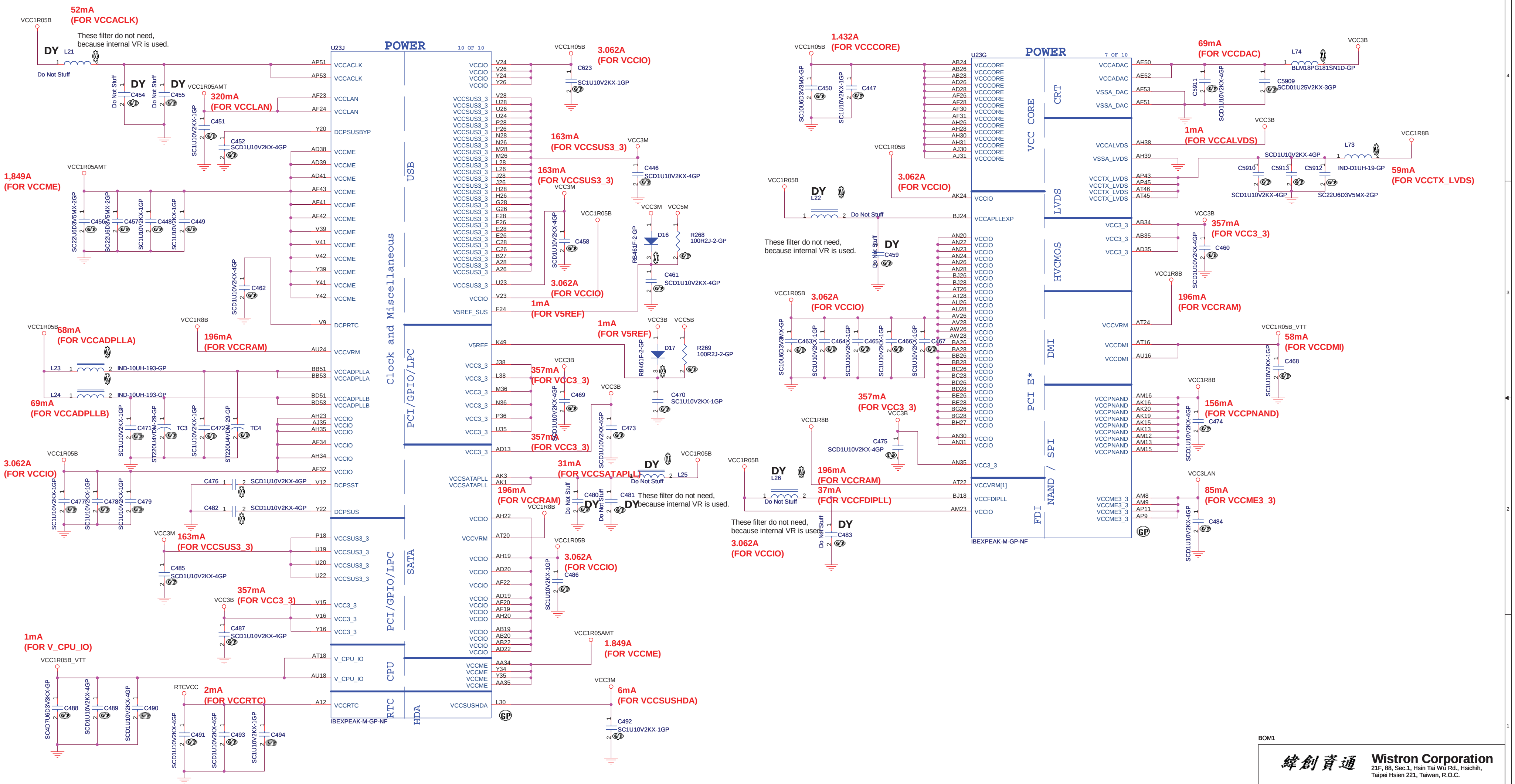
Taipei Hsien 221, Taiwan, R.O.C.

Title
PCH (5/8):PCI/USB/NVM

Size	Document Number	Rev
A2	Kendo-1-UMA	SB

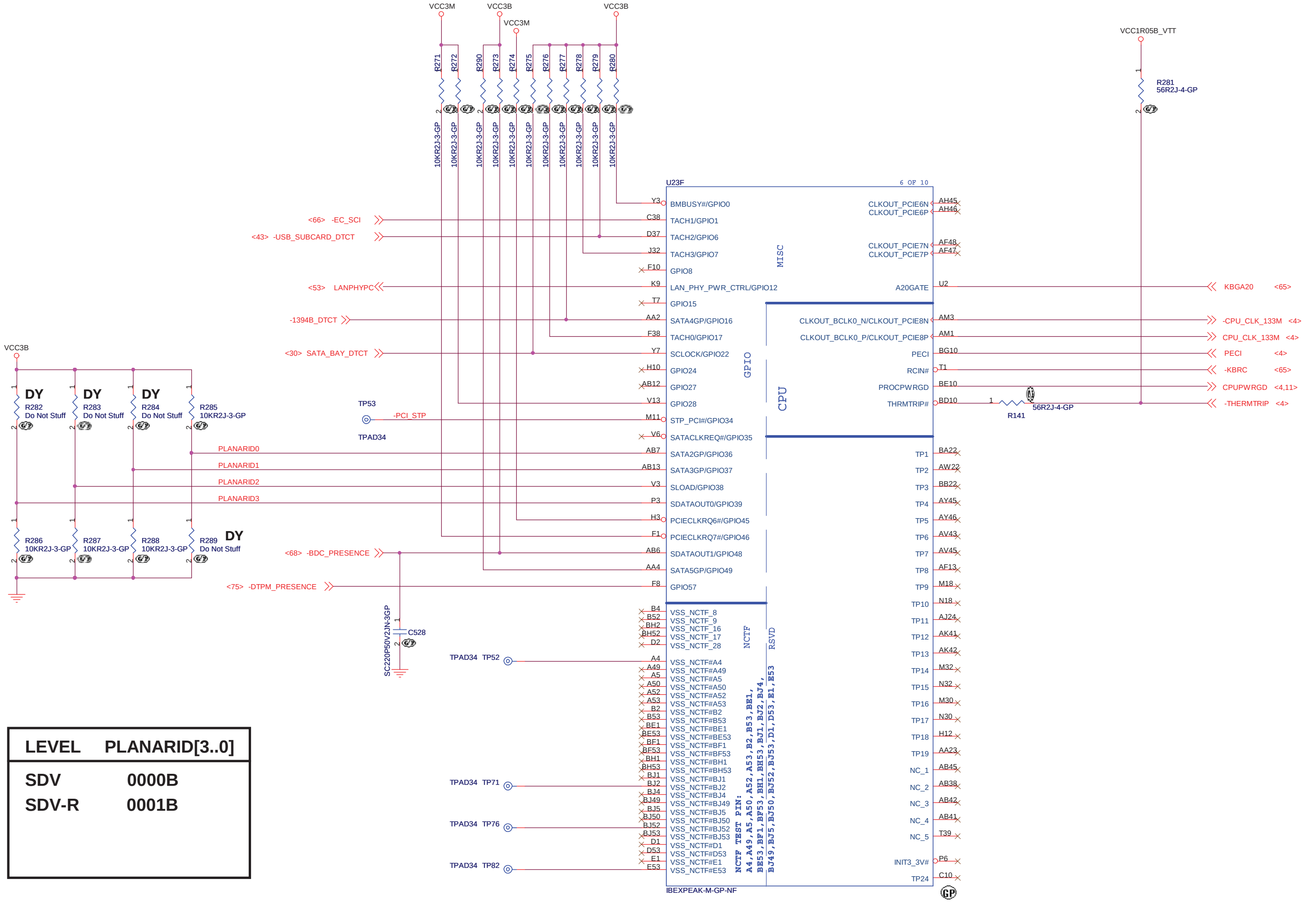
Date: Friday, March 13, 2009 Sheet 34 of 104



BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
PCH (6/8): VCC		
Size: Custom	Document Number: Kendo-1-UMA	Rev: SB
Date: Friday, March 13, 2009	Sheet 35 of 104	



LEVEL	PLANARID[3..0]
SDV	0000B
SDV-R	0001B

BOM1

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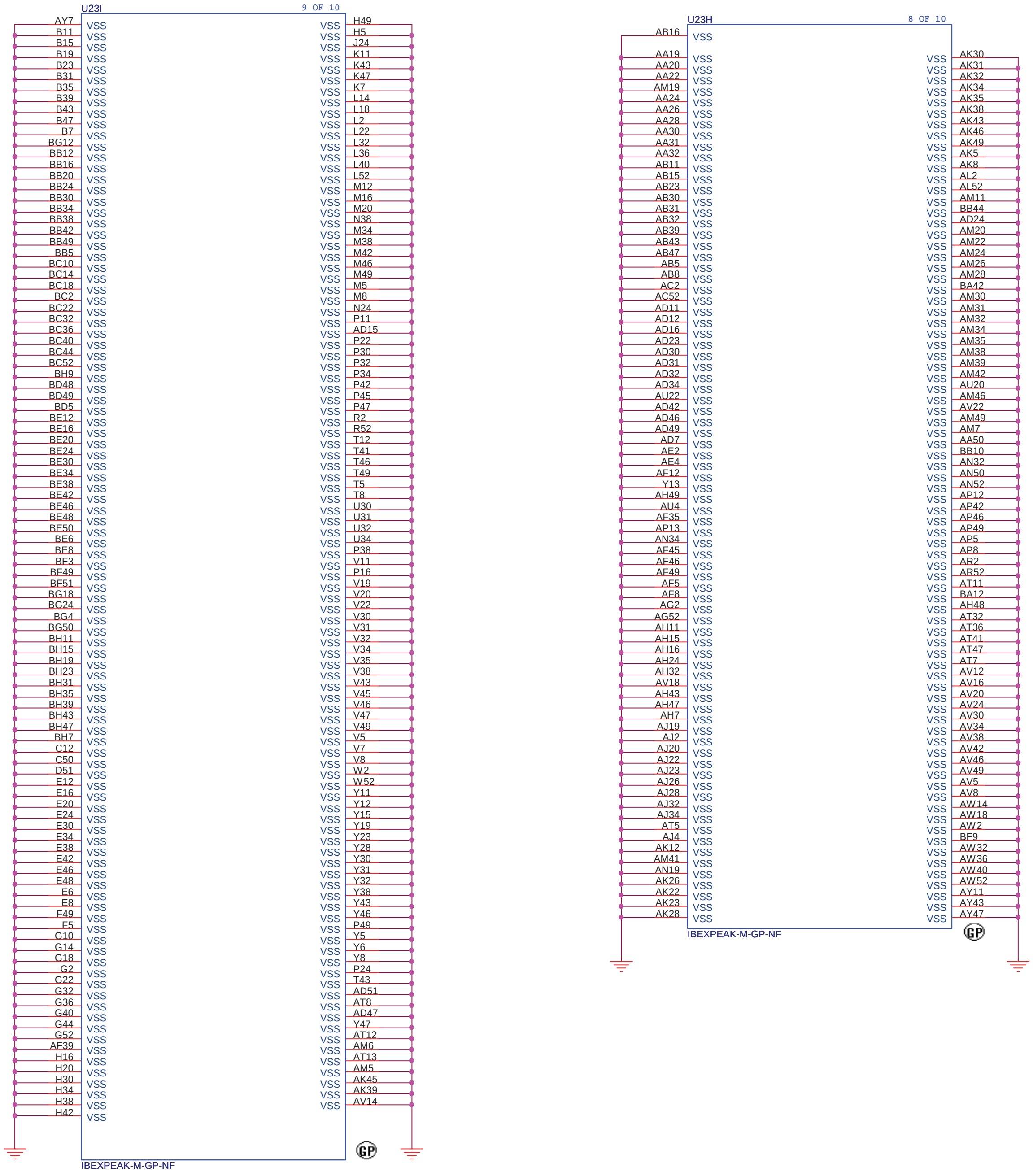
Taipei Hsien 221, Taiwan, R.O.C.

Title		
PCH (7/8):GPIO/NTCF/RSVD		
Size A2	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 36 of 104	



BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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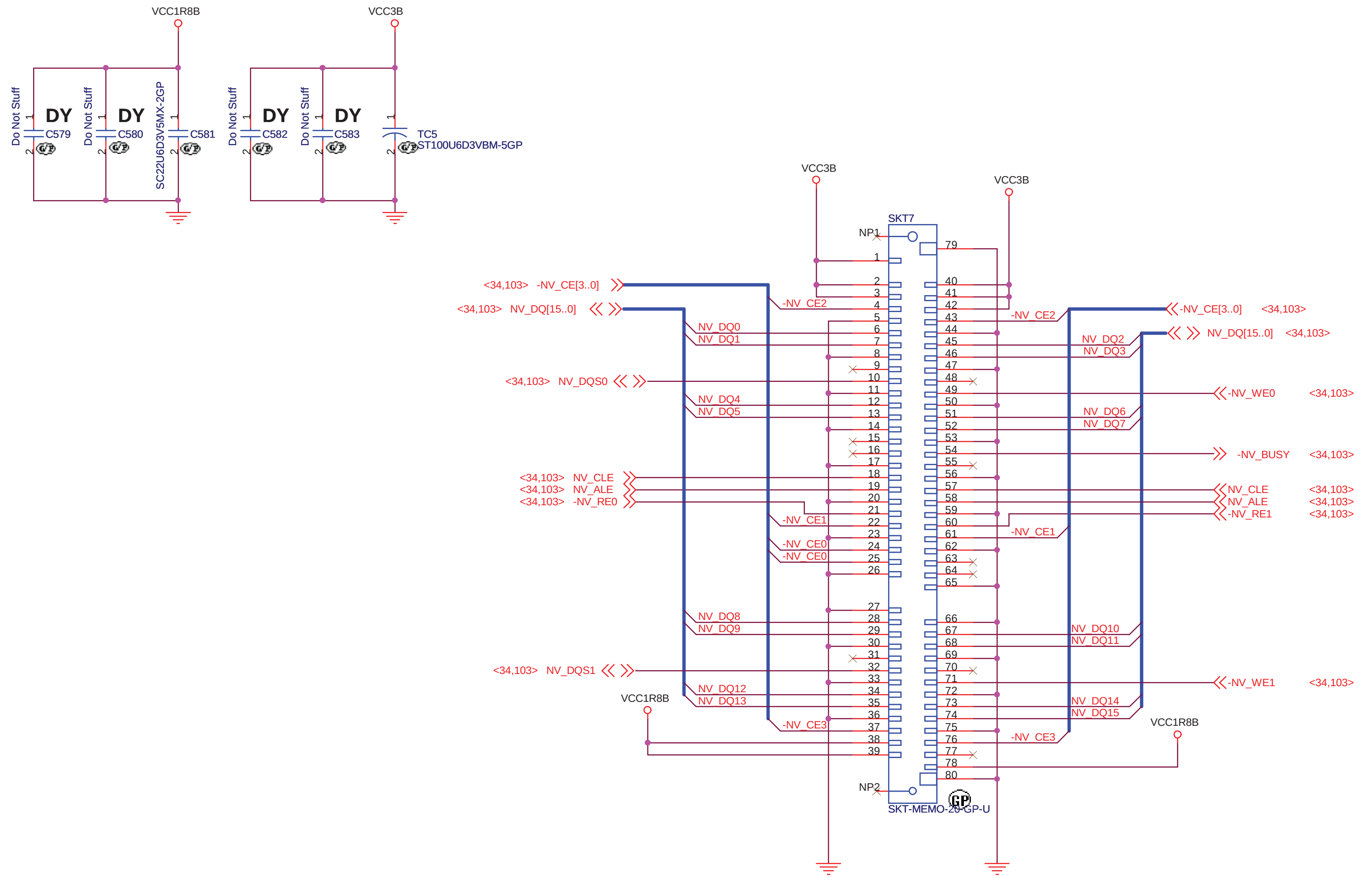
Title
PCH (8/8):GND

Size	Document Number	Rev
Custom	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 37 of 104



BOM1

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
TURBO MEMORY SLOT

Size B
Document Number **Kendo-1-UMA**
Rev **SB**

Date: Friday, March 13, 2009
Sheet 38 of 104

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BOM1

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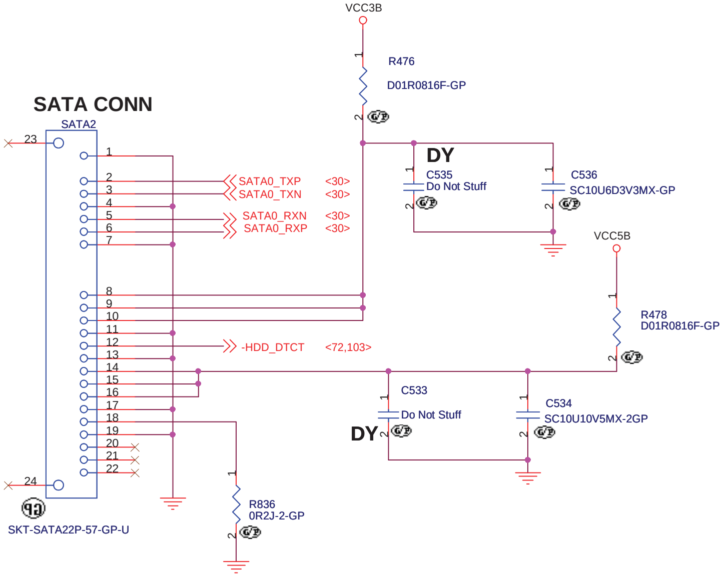
Taipei Hsien 221, Taiwan, R.O.C.

Title	***BLANK***	
Size A	Document Number **Kendo-1-UMA**	Rev **SB**
Date: Friday, March 13, 2009	Sheet 39 of 104	



BOM1

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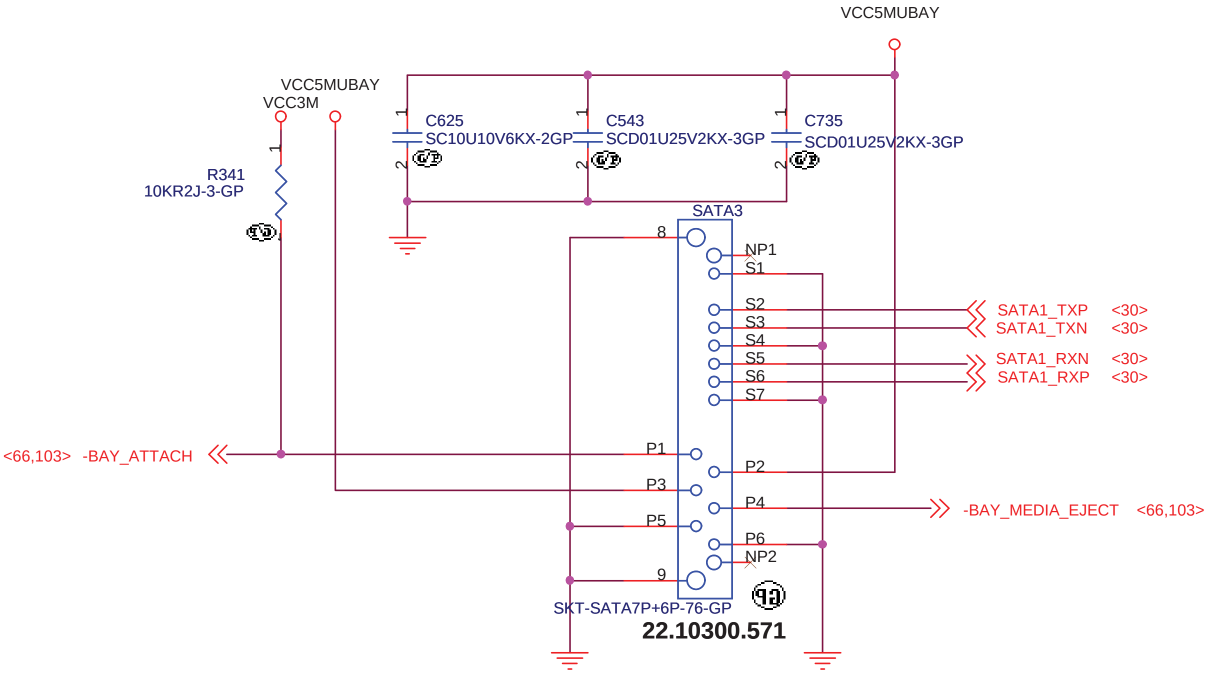
Taipei Hsien 221, Taiwan, R.O.C.

Title
SATA HDD CONN

Size	Document Number	Rev
A3	**Kendo-1-UMA**	**SB**

Date: Friday, March 13, 2009 Sheet 40 of 104



BOM1

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Taipei Hsien 221, Taiwan, R.O.C.

Title

SATA ODD BAY

Size	Document Number	Rev
A4	**Kendo-1-UMA**	**SB**

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BOM1

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Taipei Hsien 221, Taiwan, R.O.C.

Title
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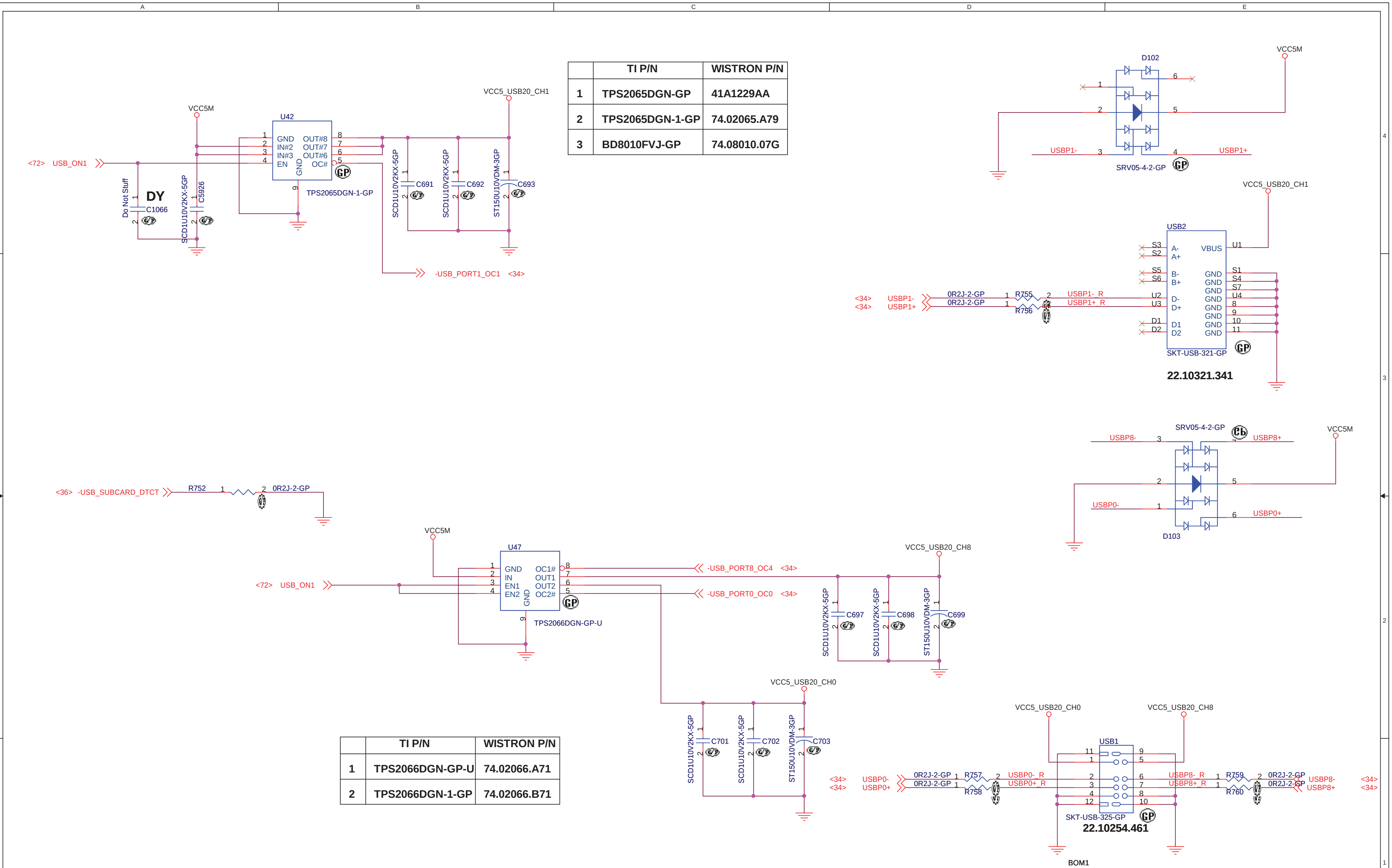
Size A	Document Number **Kendo-1-UMA**	Rev **SB**
Date: Friday, March 13, 2009	Sheet 42 of 104	



	TI P/N	WISTRON P/N
1	TPS2065DGN-GP	41A1229AA
2	TPS2065DGN-1-GP	74.02065.A79
3	BD8010FVJ-GP	74.08010.07G

	TI P/N	WISTRON P/N
1	TPS2066DGN-GP-U	74.02066.A71
2	TPS2066DGN-1-GP	74.02066.B71

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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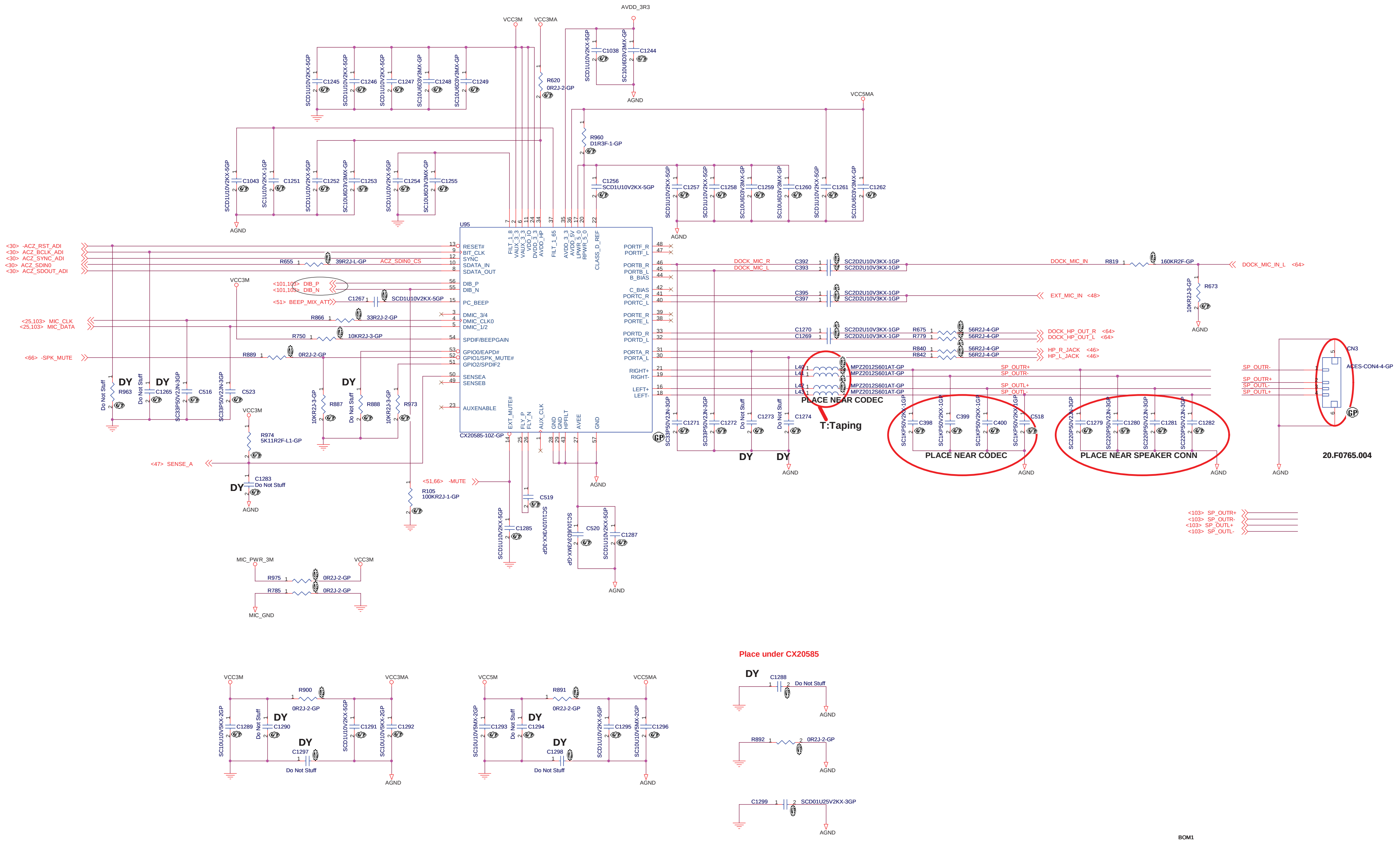
Title
USB2.0 CONN&PWR

Size	Document Number	Rev
A3	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 43 of 104



BOM1

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Title		
AUDIO CODEC CX20585		
Size A2	Document Number Kendo-1-UMA	Rev SB
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BOM1

緯創資通

Wistron Corporation

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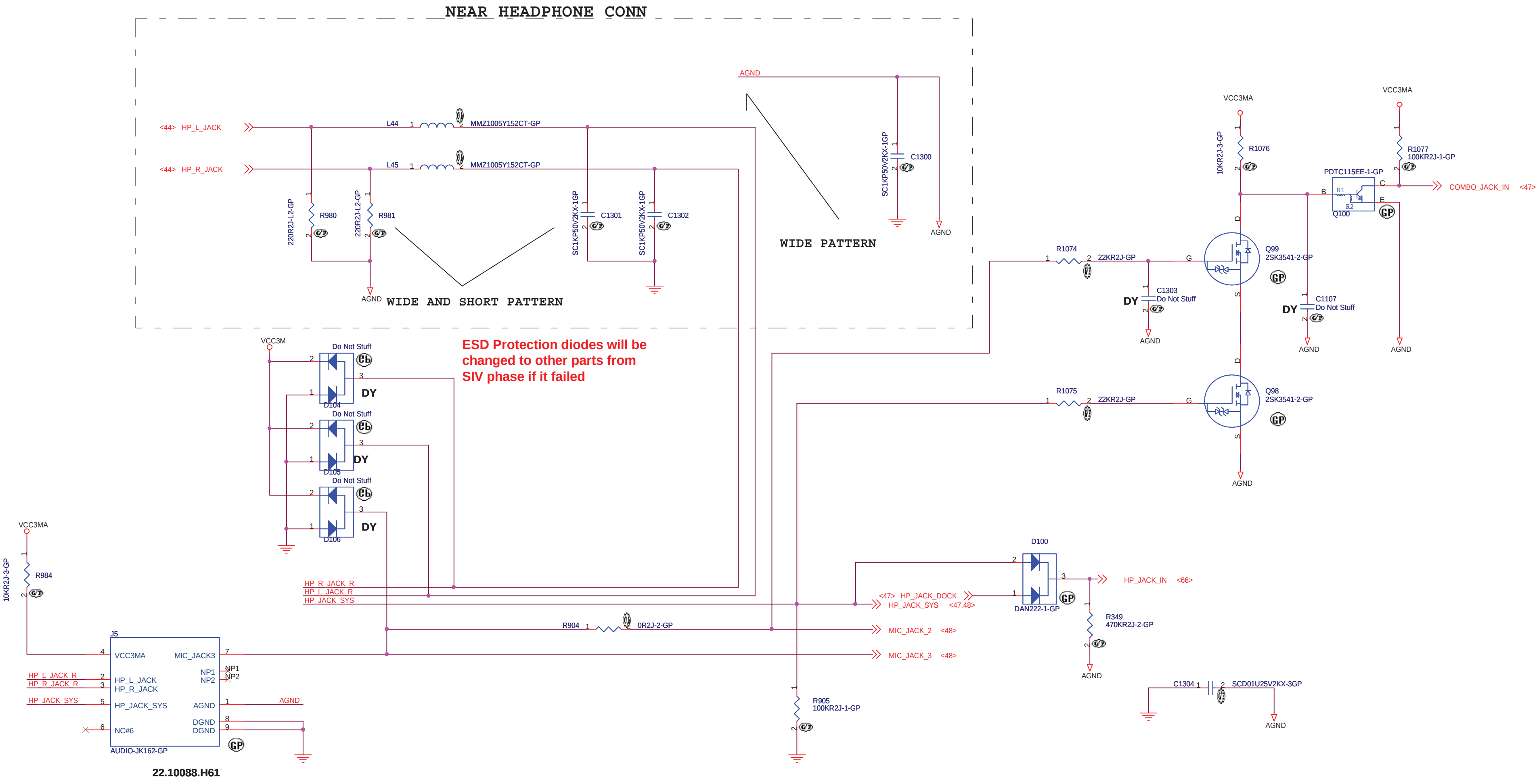
Taipei Hsien 221, Taiwan, R.O.C.

Title	***BLANK***	
Size A	Document Number **Kendo-1-UMA**	Rev **SB**
Date: Friday, March 13, 2009	Sheet 45 of 104	



ESD Protection diodes will be changed to other parts from SIV phase if it failed

22.10088.H61

BOM1

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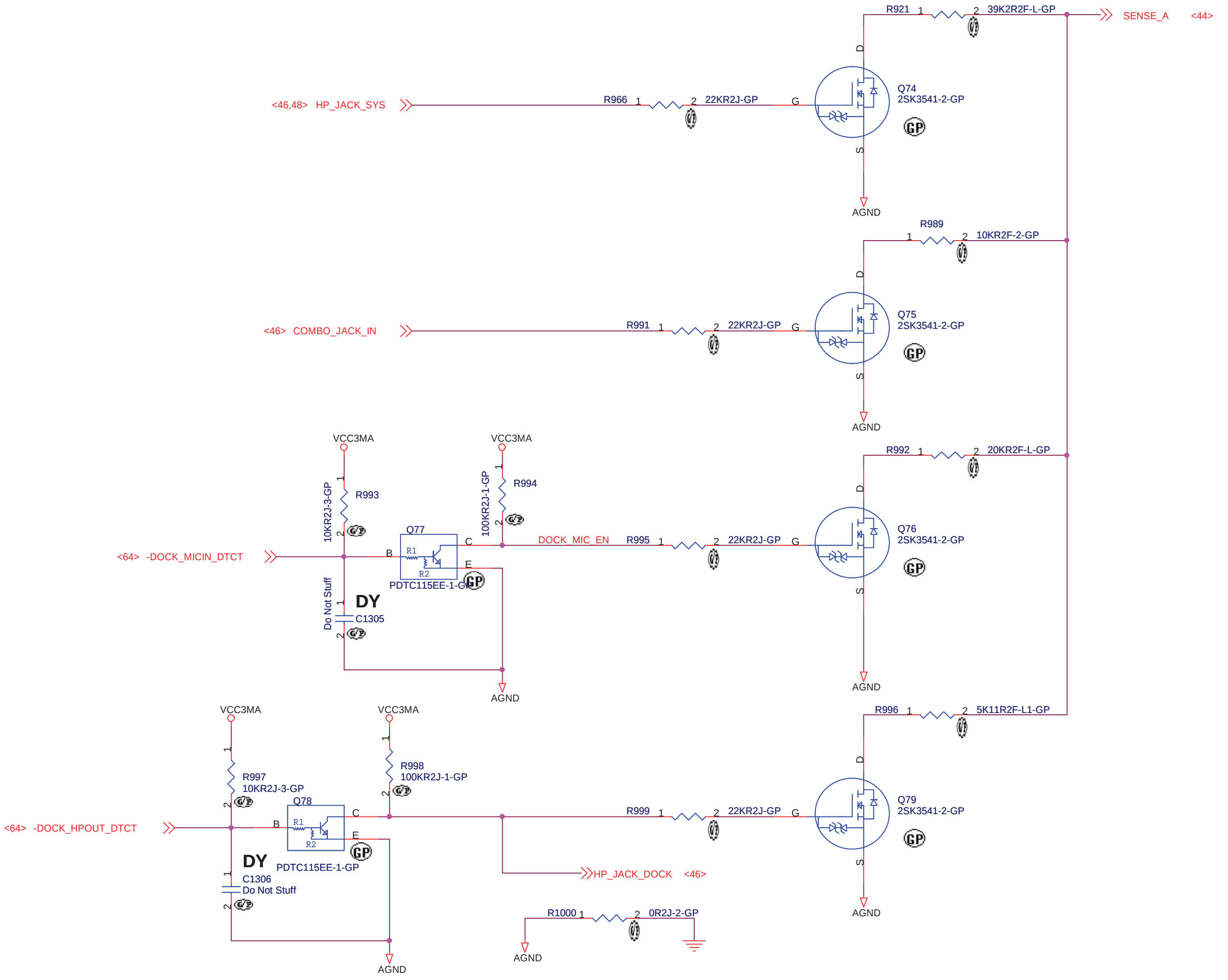
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title: **AUDIO CONNECTOR**

Size	Document Number	Rev
C	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 46 of 104



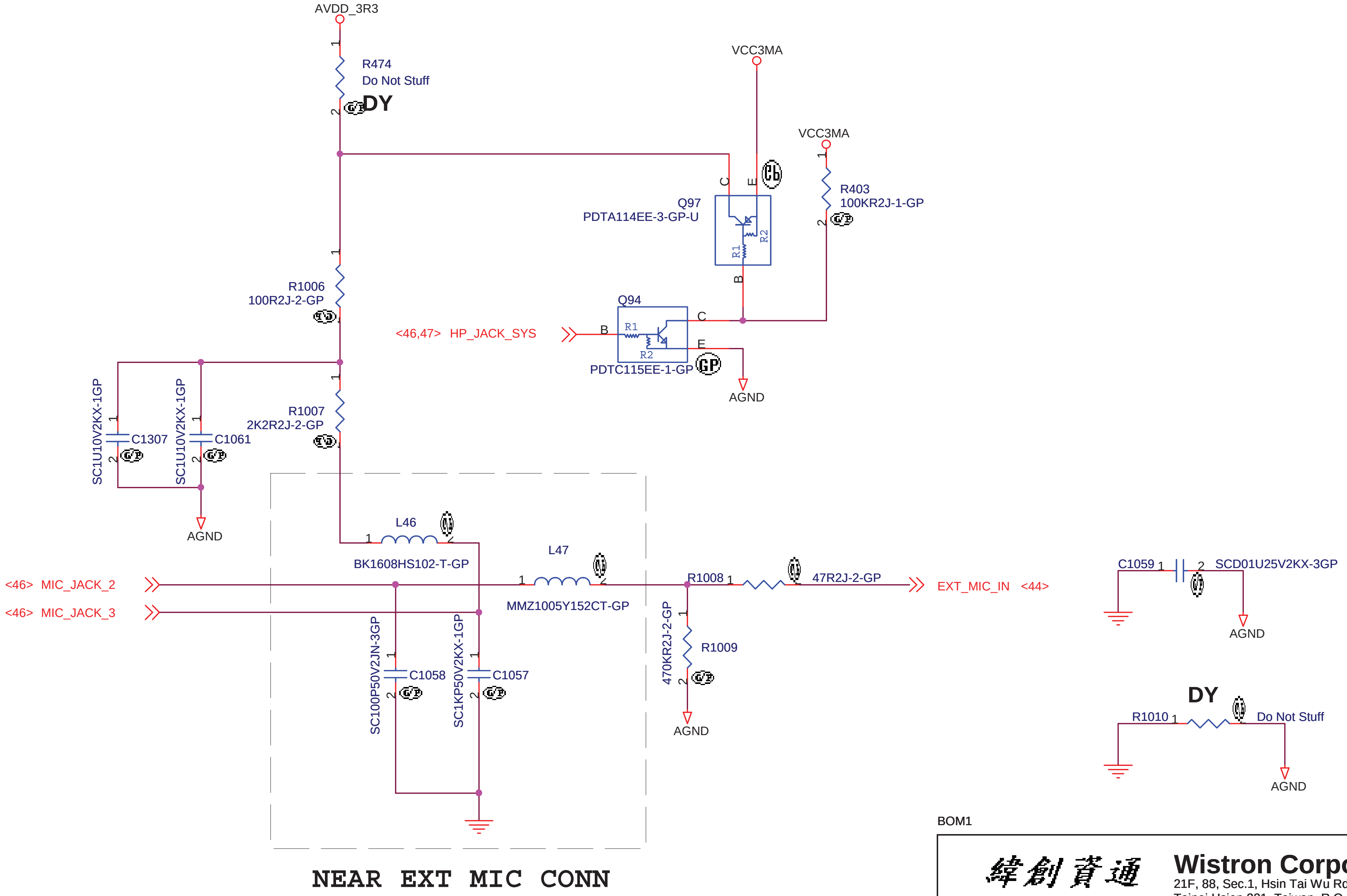
BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
AUDIO JACK SENSE

Size	Document Number	Rev
A3	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 47 of 104



緯創資通

Wistron Corporation

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Taipei Hsien 221, Taiwan, R.O.C.

Title	**AUDIO EXT MIC I/F**	
Size A4	Document Number **Kendo-1-UMA**	Rev **SB**
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BOM1

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Title
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Size A
Document Number **Kendo-1-UMA**
Rev **SB**

Date: Friday, March 13, 2009

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BOM1

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Title
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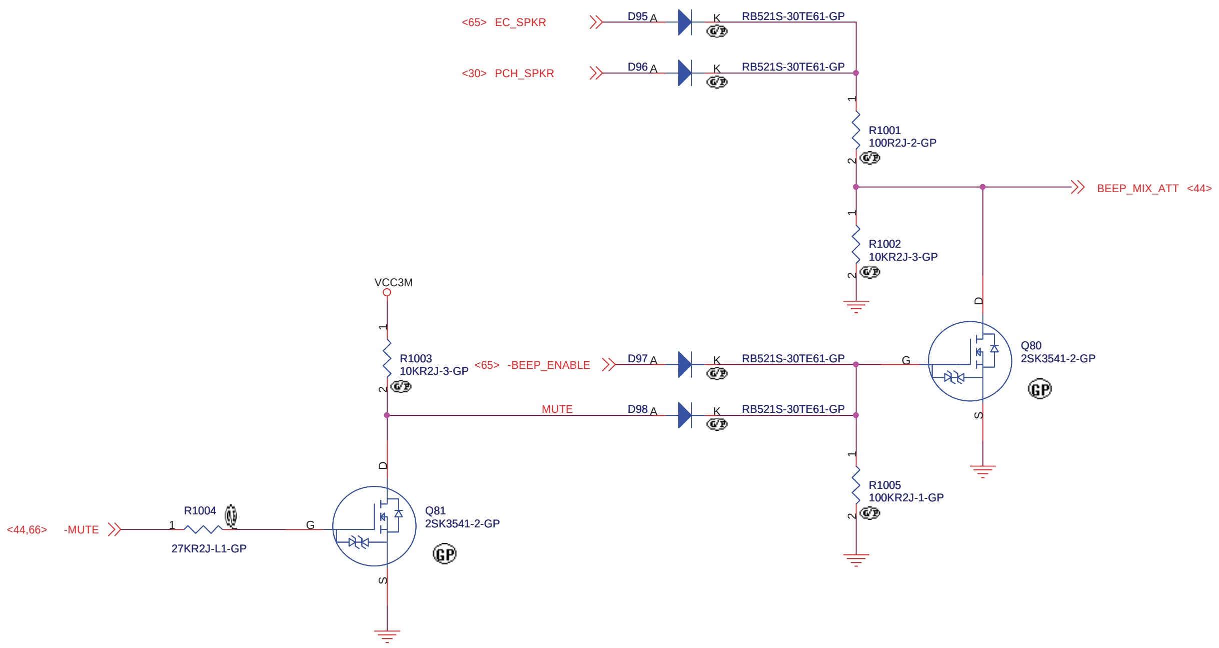
Size	Document Number	Rev
A	**Kendo-1-UMA**	**SB**

Date: Friday, March 13, 2009 Sheet 50 of 104



BOM1

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Title
BEEP CONTROL

Size	Document Number	Rev
A3	Kendo-1-UMA	SB

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BOM1

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Title
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Size A | Document Number **Kendo-1-UMA** | Rev **SB**

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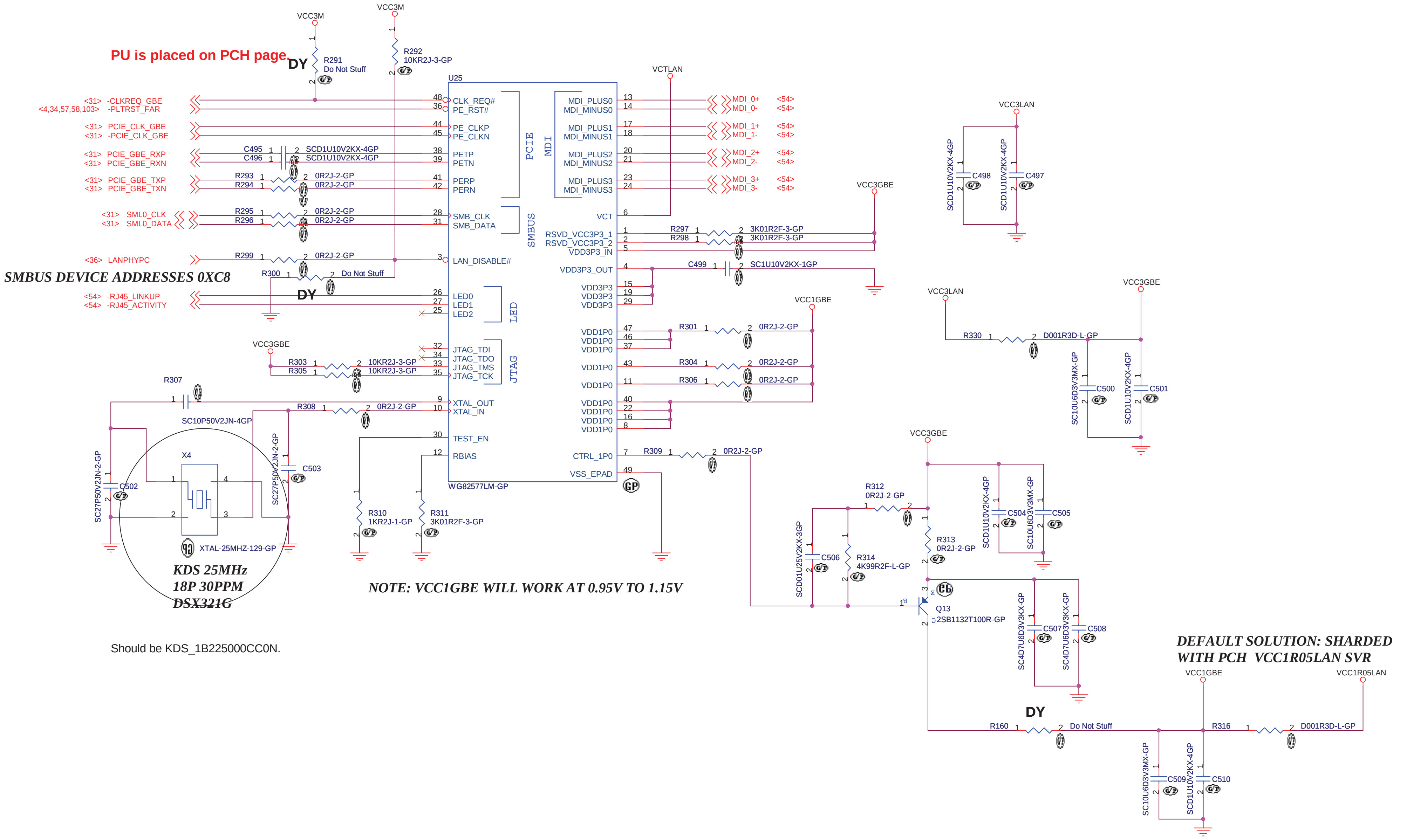

PU is placed on PCH page

SMBUS DEVICE ADDRESSES 0XC8

KDS 25MHz
18P 30PPM
DSX321G

NOTE: VCC1GBE WILL WORK AT 0.95V TO 1.15V

Should be KDS_1B225000CC0N.

DEFAULT SOLUTION: SHARDED WITH PCH VCC1R05LAN SVR

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Taipei Hsien 221, Taiwan, R.O.C.

Title
GBE HANKSVILLE

Size	Document Number	Rev
A3	Kendo-1-UMA	SB

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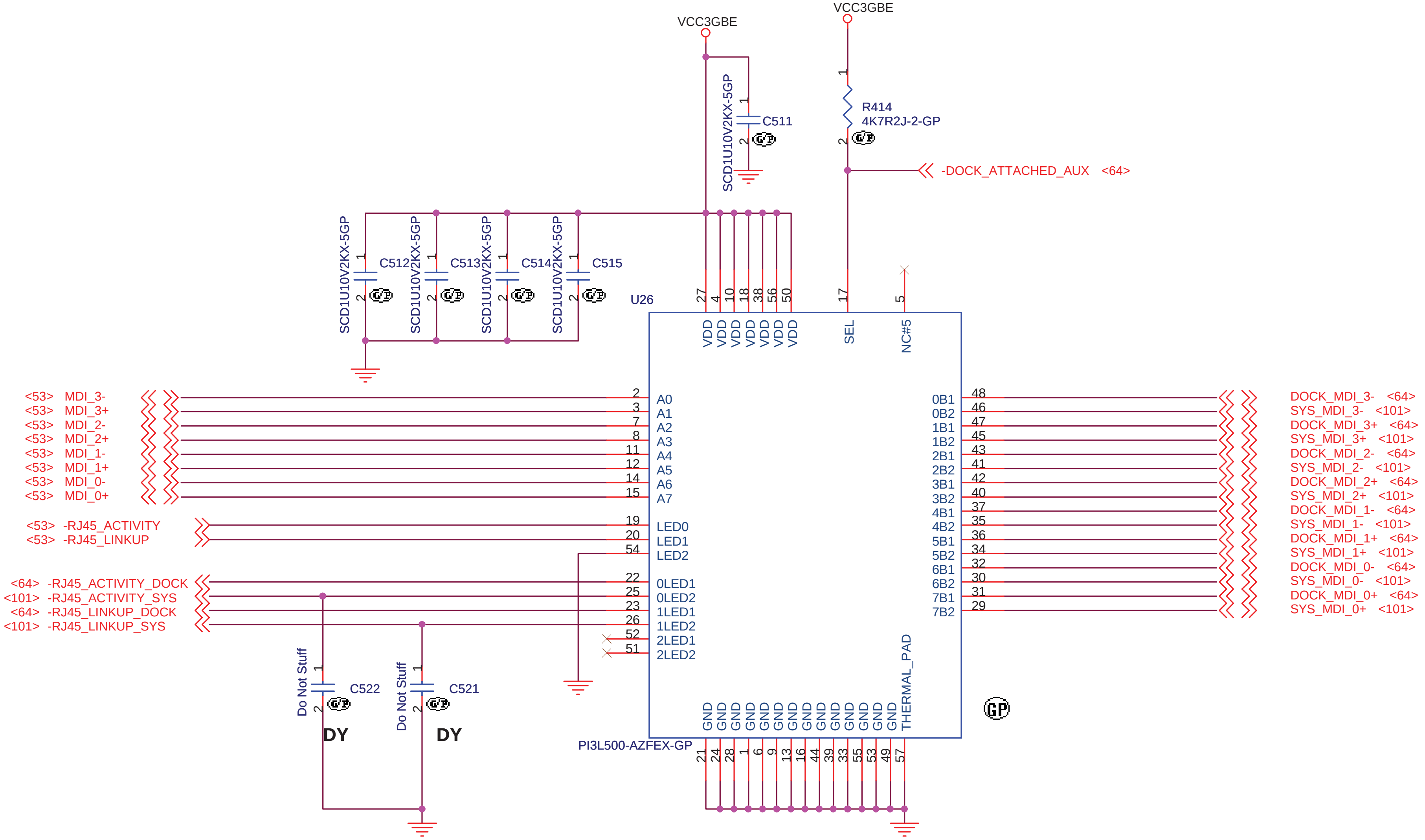

BOM1

[Source Cadidate]				
1st	Pericom	PI3L500AZFEX	73.3L500.003	41R0539AA
2nd	TI	TS3L500AERHUR	73.3L500.A0V	41R0539BA

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Title
GBE LAN SW

Size A4
Document Number **Kendo-1-UMA**
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BOM1

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
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Size A
Document Number **Kendo-1-UMA**
Rev **SB**

Date: Friday, March 13, 2009

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BOM1

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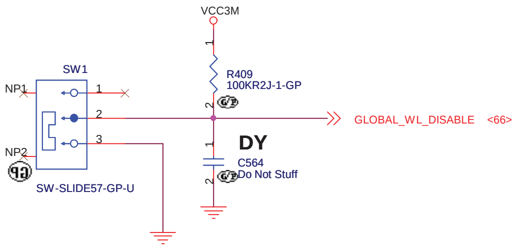
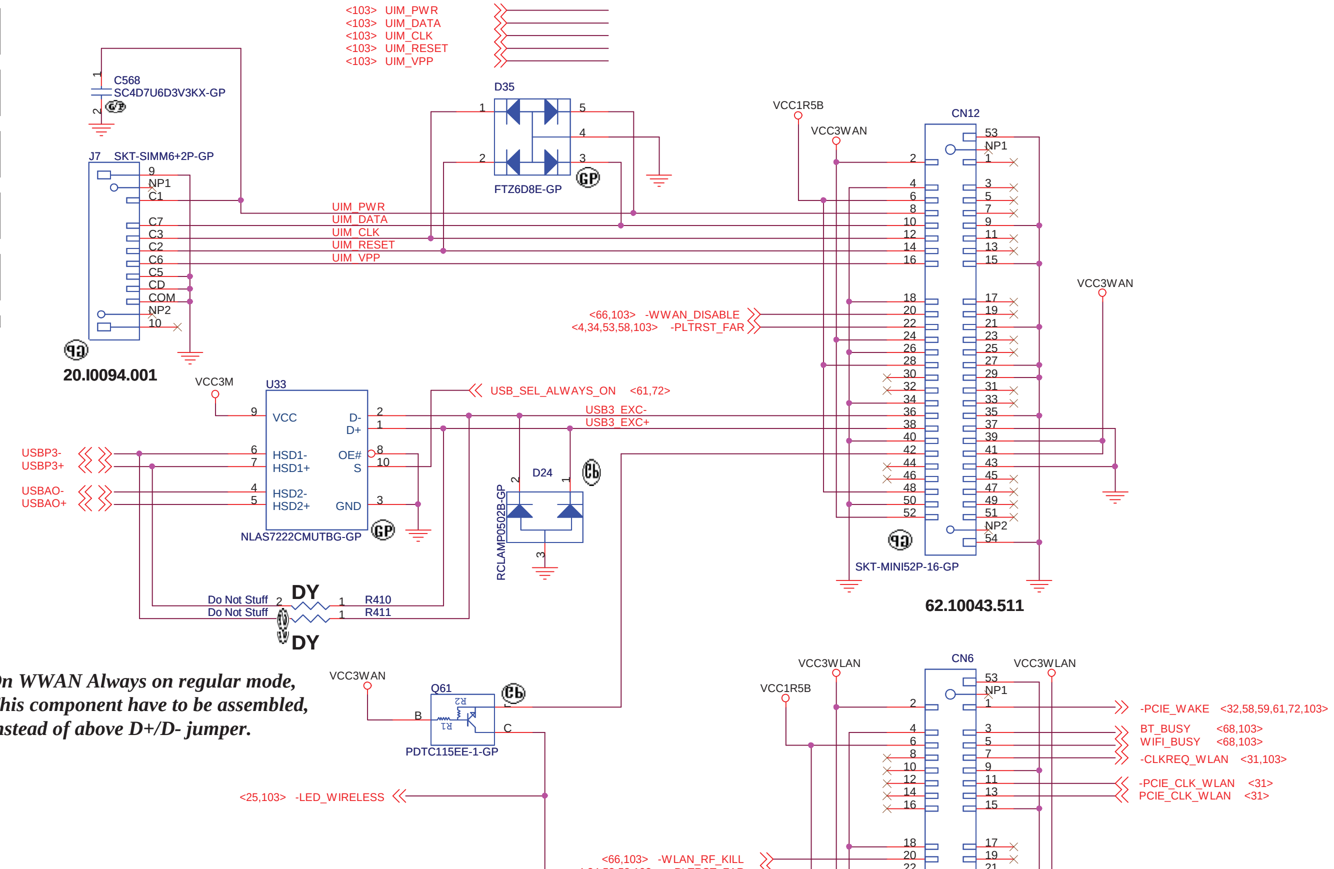
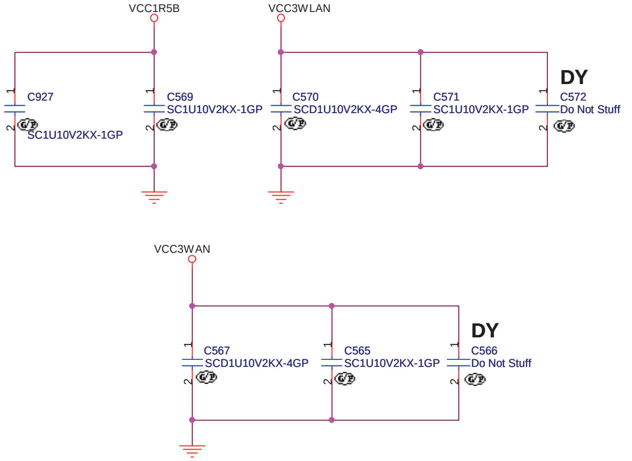
WIRELESS DISABLE SWITCH





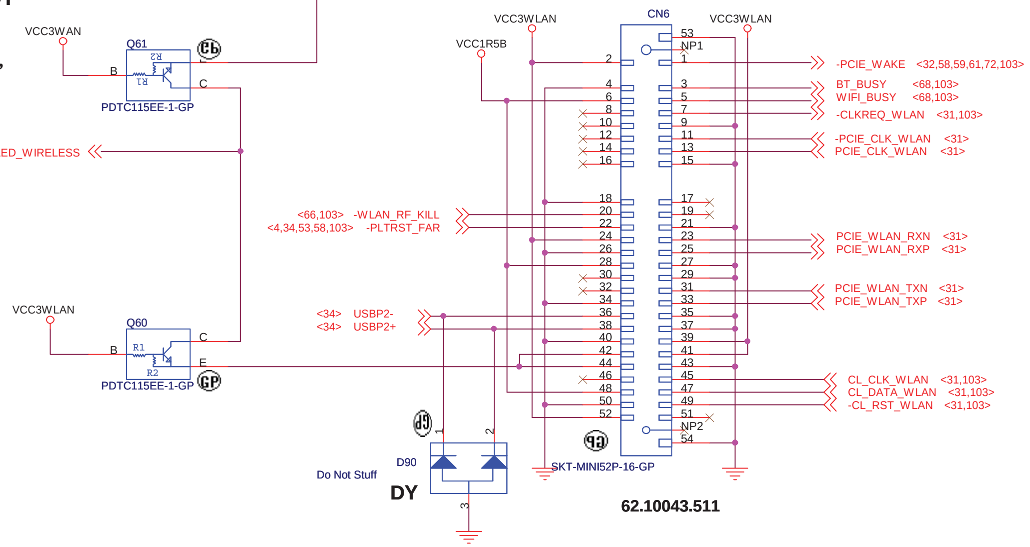
On WWAN Always on regular mode,
This component have to be assembled,
instead of above D+/D- jumper.



Always ON	YES	NO
U33 R410 R411	ASM No_ASM No_ASM	No_ASM ASM ASM

LOGIC

USB_SEL_ALWAYS_ON	USB_WWAN
L	USBP3
H	USBAO



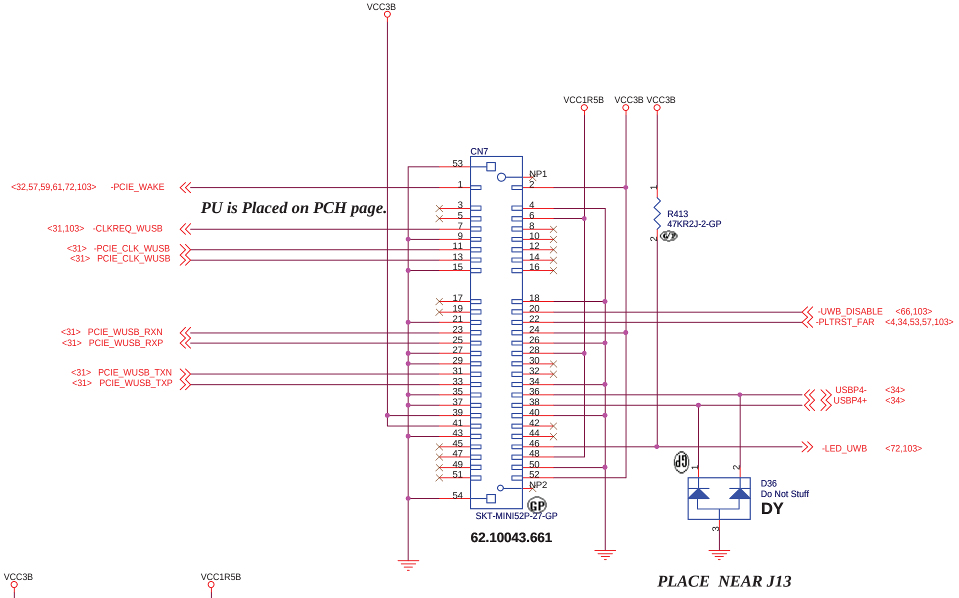
BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
	MINI CARD SLOT 1	
Size A3	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 57 of 104	

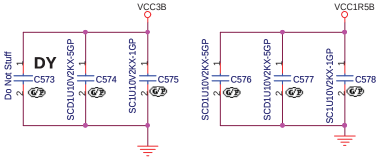
HALF MINI CARD FOR WUSB

7.0H CONNECTOR



PU is Placed on PCH page.

PLACE NEAR J13

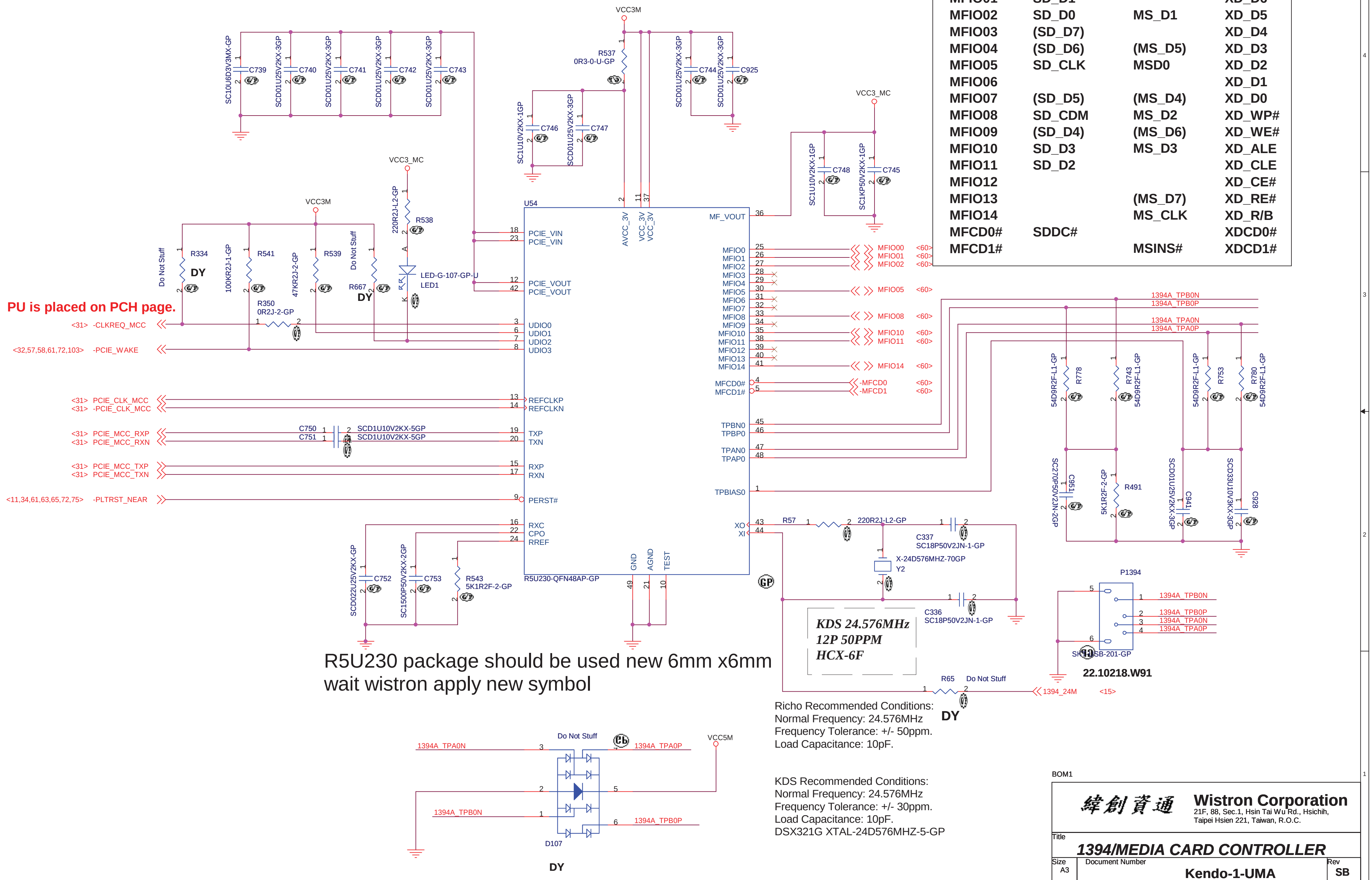


BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
	MINI CARD SLOT 2	
Size C	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 58 of 104	

MEDIA I/F	SD/MMC	MEMORYSTICK	XD
MFIO00	SDWP#	MSBS	XD_D7
MFIO01	SD_D1		XD_D6
MFIO02	SD_D0	MS_D1	XD_D5
MFIO03	(SD_D7)		XD_D4
MFIO04	(SD_D6)	(MS_D5)	XD_D3
MFIO05	SD_CLK	MSD0	XD_D2
MFIO06			XD_D1
MFIO07	(SD_D5)	(MS_D4)	XD_D0
MFIO08	SD_CDM	MS_D2	XD_WP#
MFIO09	(SD_D4)	(MS_D6)	XD_WE#
MFIO10	SD_D3	MS_D3	XD_ALE
MFIO11	SD_D2		XD_CLE
MFIO12			XD_CE#
MFIO13		(MS_D7)	XD_RE#
MFIO14		MS_CLK	XD_R/B
MFCD0#	SDDC#		XDCD0#
MFCD1#		MSINS#	XDCD1#



PU is placed on PCH page.

R5U230 package should be used new 6mm x6mm
wait wistron apply new symbol

KDS 24.576MHz
12P 50PPM
HCX-6F

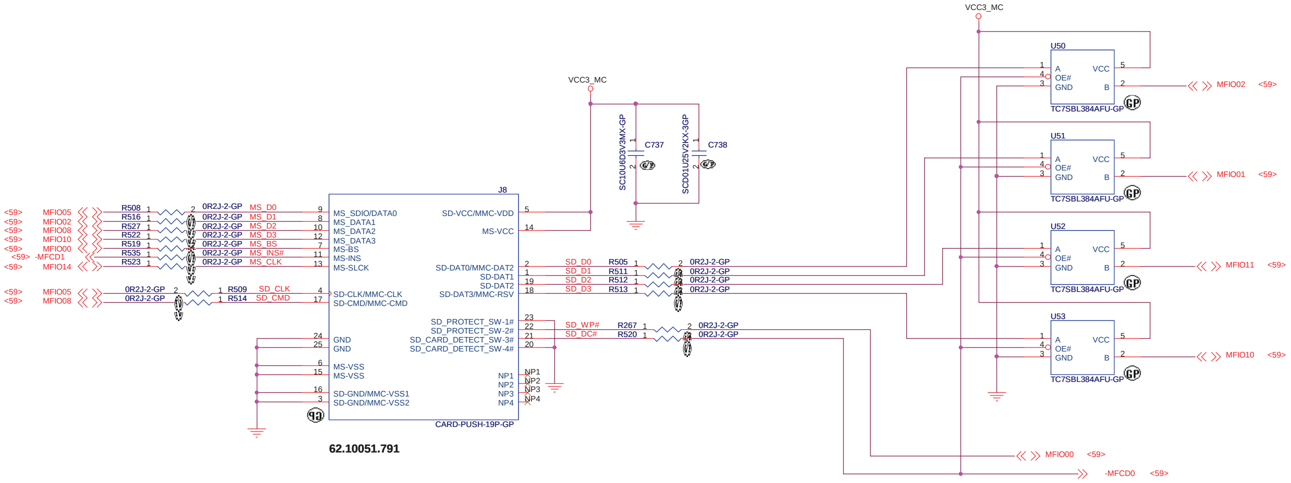
Richo Recommended Conditions:
Normal Frequency: 24.576MHz
Frequency Tolerance: +/- 50ppm.
Load Capacitance: 10pF.

KDS Recommended Conditions:
Normal Frequency: 24.576MHz
Frequency Tolerance: +/- 30ppm.
Load Capacitance: 10pF.
DSX321G XTAL-24D576MHZ-5-GP

BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

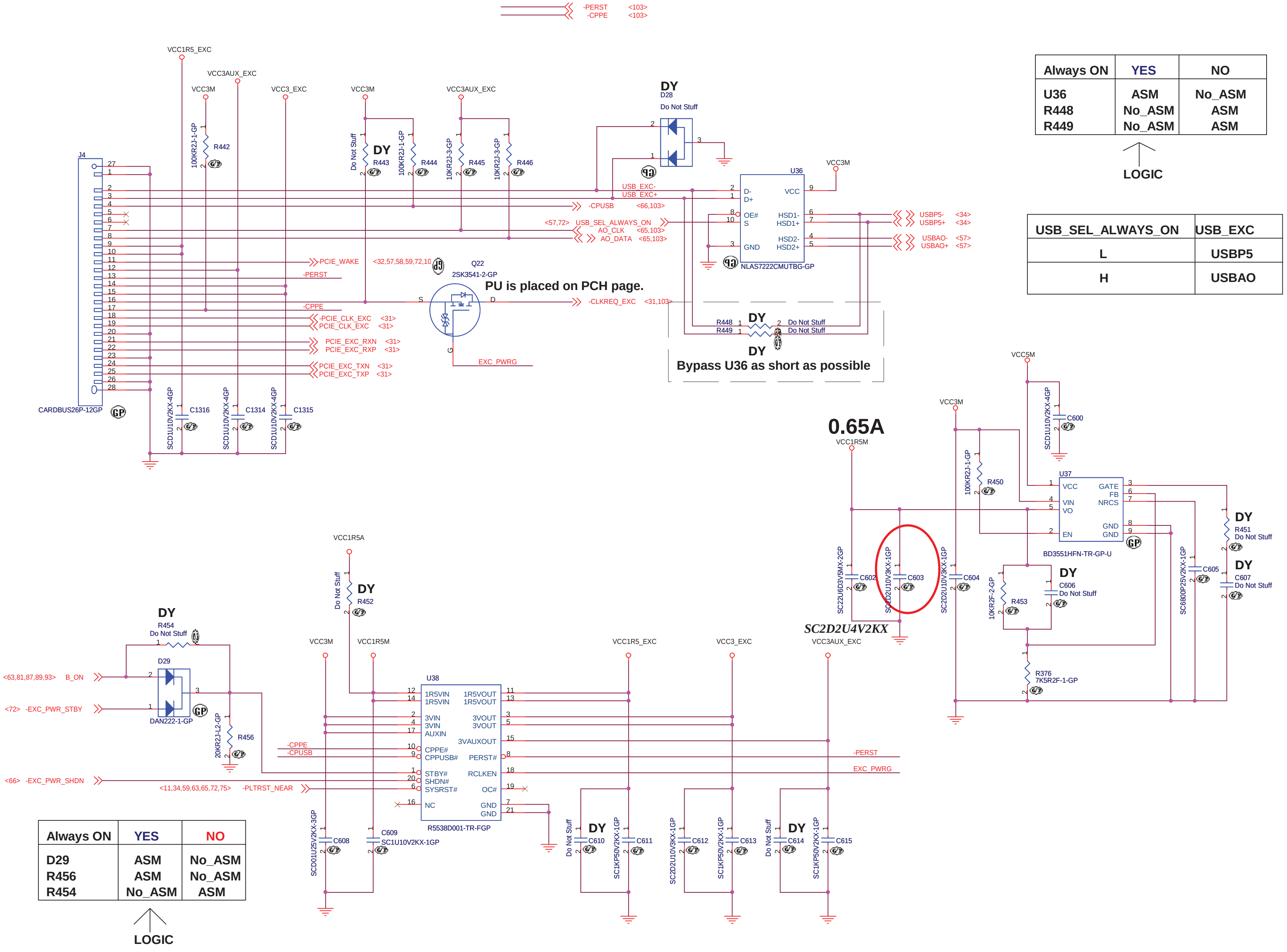
Title		
1394/MEDIA CARD CONTROLLER		
Size A3	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 59 of 104	



BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
MEDIA CARD INTERFACE		
Size A2	Document Number **Kendo-1-UMA**	Rev **SB**
Date: Friday, March 13, 2009	Sheet 60 of 104	



Always ON	YES	NO
U36	ASM	No_ASM
R448	No_ASM	ASM
R449	No_ASM	ASM

LOGIC

USB_SEL_ALWAYS_ON	USB_EXC
L	USBP5
H	USBAO

Always ON	YES	NO
D29	ASM	No_ASM
R456	ASM	No_ASM
R454	No_ASM	ASM

LOGIC

BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
Express Card/SMART CARD IF

Size	Document Number	Rev
C	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 61 of 104

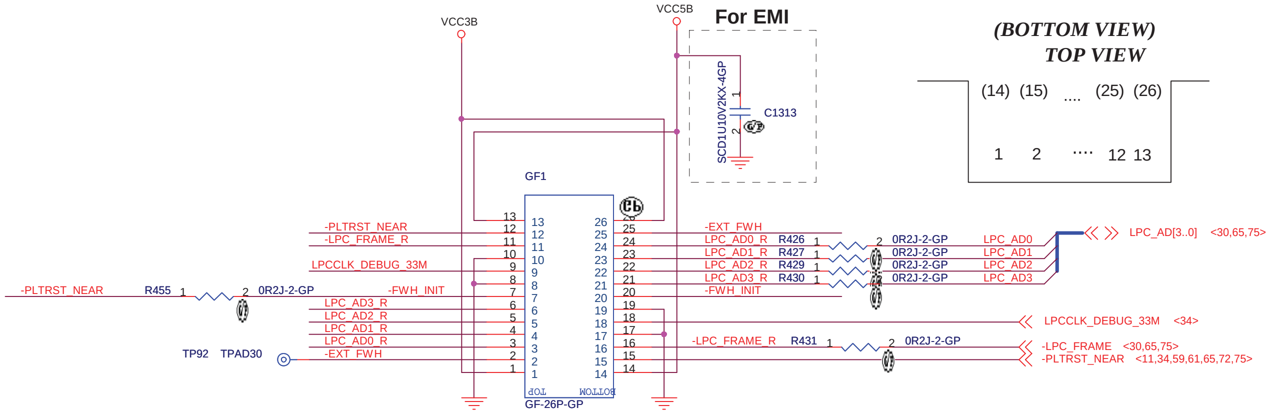
BLANK

BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	*BLANK*	
Size A	Document Number **Kendo-1-UMA**	Rev **SB**
Date: Friday, March 13, 2009	Sheet 62 of 104	

Golden Finger for Debug Board



Lenovo Debug Tool IF.

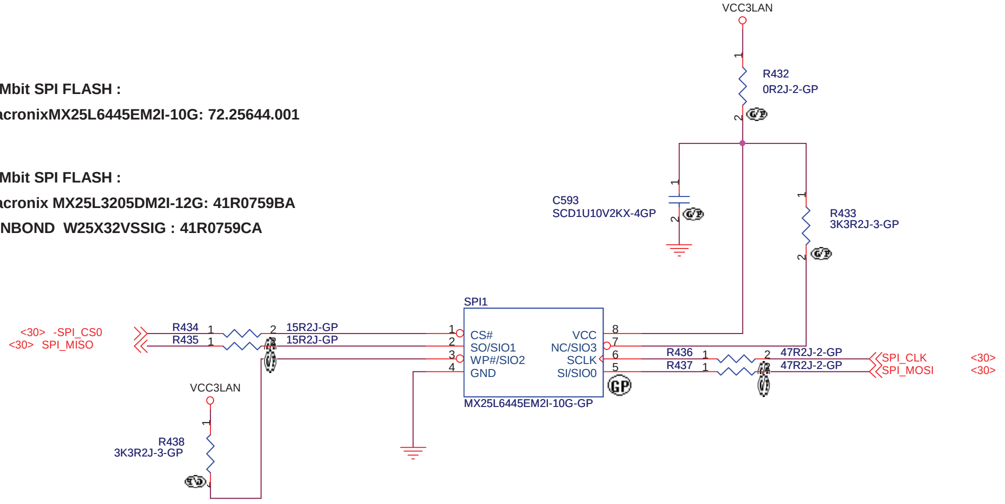
64Mbit SPI FLASH :

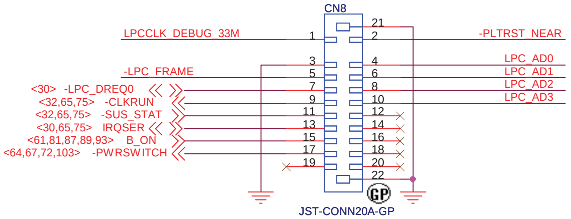
MacronixMX25L6445EM2I-10G: 72.25644.001

32Mbit SPI FLASH :

Macronix MX25L3205DM2I-12G: 41R0759BA

WINBOND W25X32VSSIG : 41R0759CA

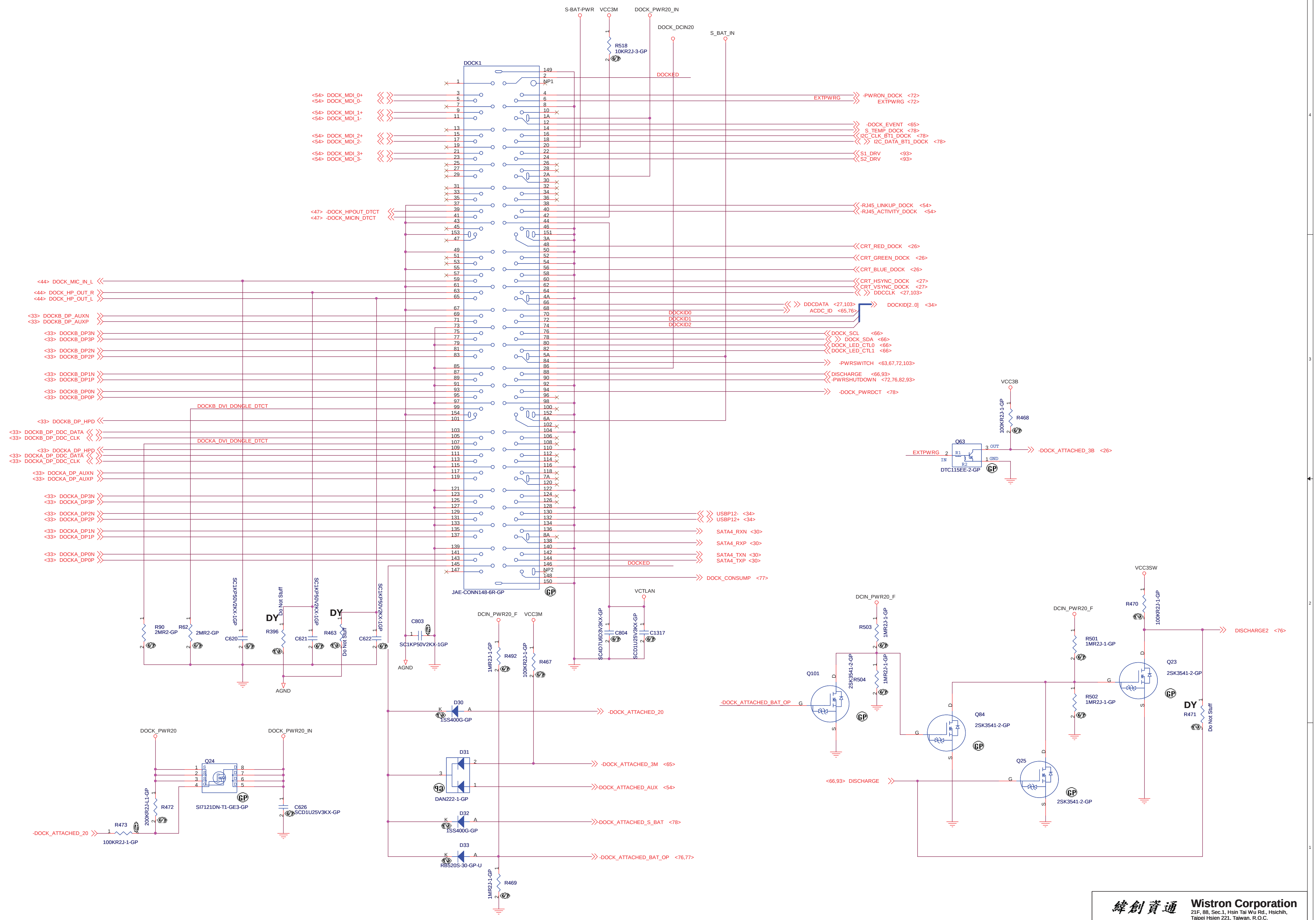




BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	SPI & LPC DEBUG CARD	
Size A3	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 63 of 104	

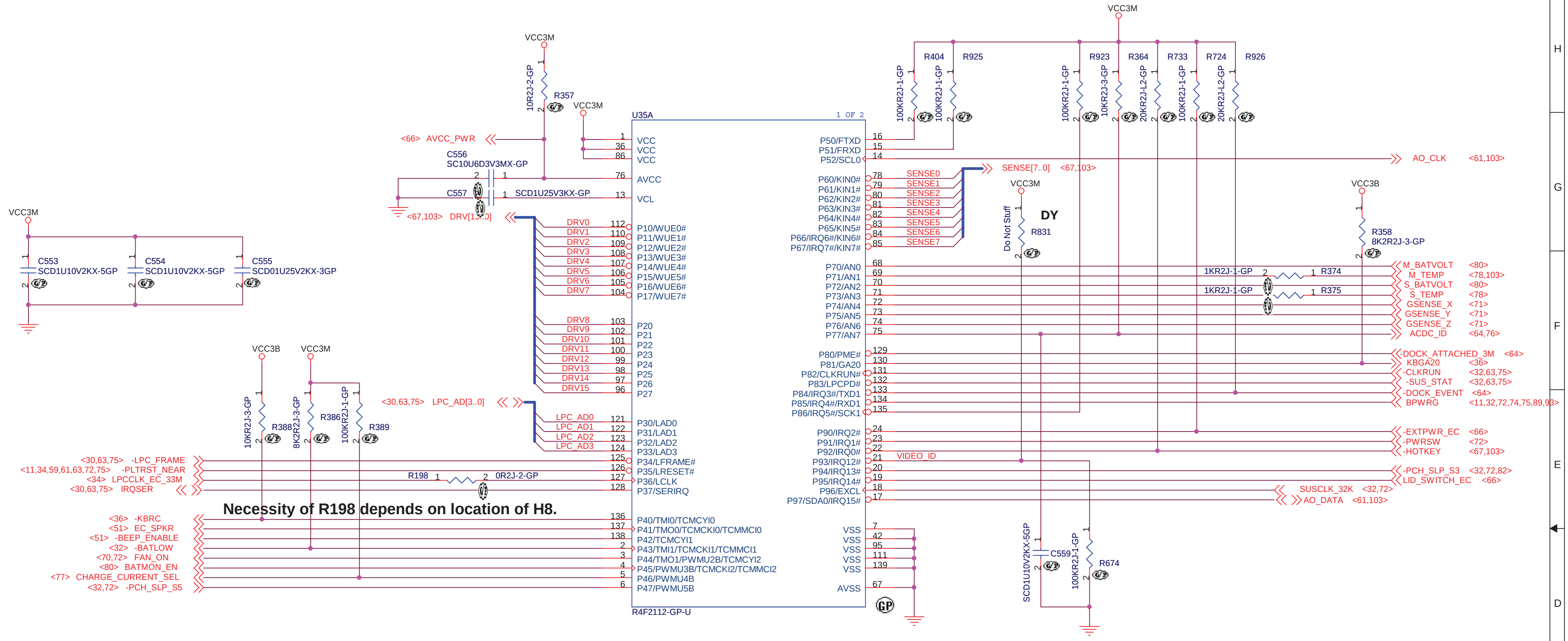


緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
DOCKING CONNECTOR

Size | Document Number **Kendo-1-UMA** | Rev SB

Date: Friday, March 13, 2009 | Sheet 64 of 104



Necessity of R198 depends on location of H8.

Table

GFX	EXT.	INT.
R831	ASM	NO_ASM
R674	NO_ASM	ASM

LOGIC

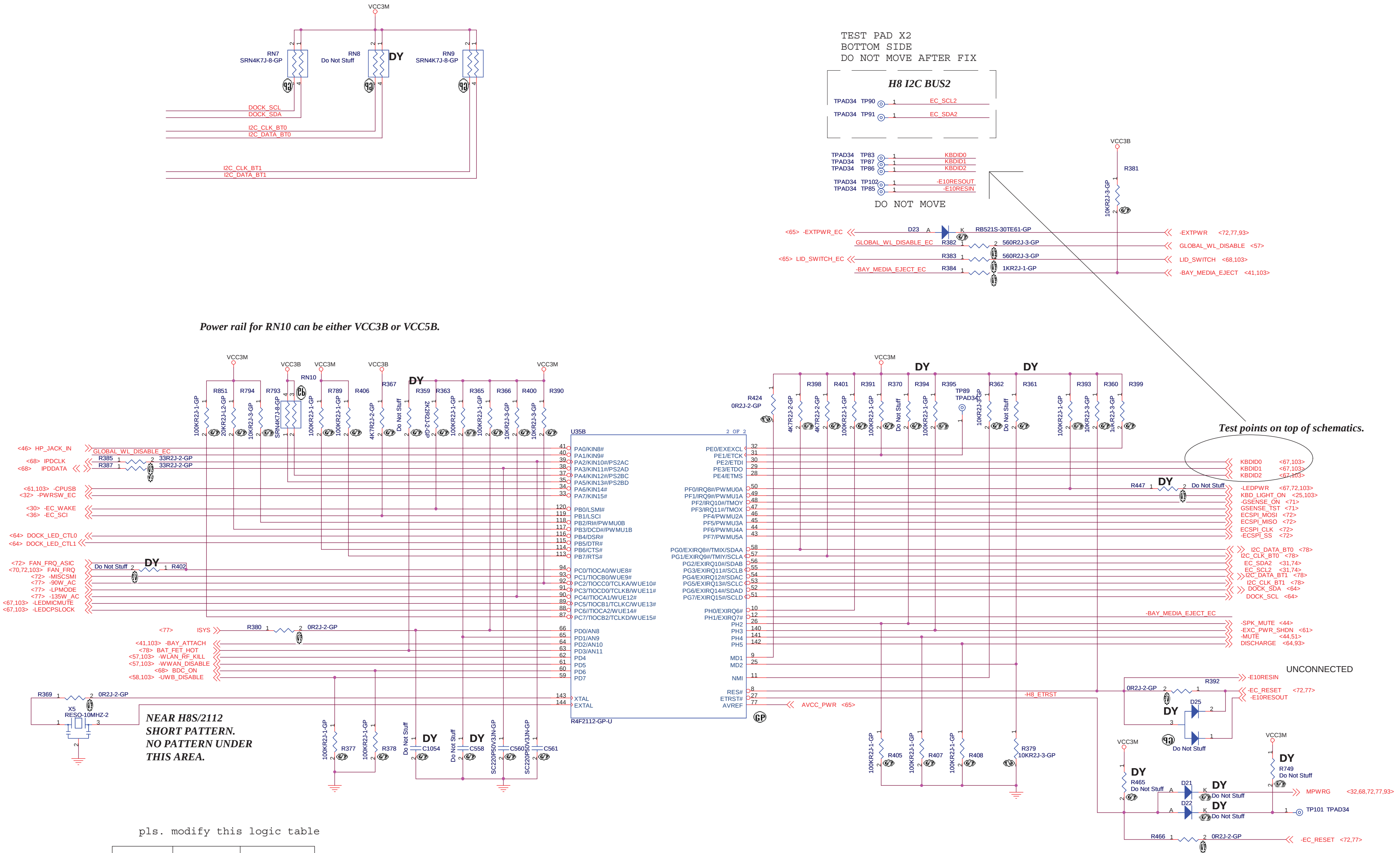
BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
H8S/2112 (1/2)

Size A3
Document Number Kendo-1-UMA
Rev SB

Date: Friday, March 13, 2009
Sheet 65 of 104



Power rail for RN10 can be either VCC3B or VCC5B.

Test points on top of schematics.

NEAR H8S/2112
SHORT PATTERN.
NO PATTERN UNDER
THIS AREA.

pls. modify this logic table

E10A Debug I/F	Enable	Disable
R368	NO ASM	ASM
R380	NO ASM	ASM
D20	ASM	NO ASM
D22	ASM	NO ASM
D21	ASM	NO ASM
R360	ASM	ASM
R361	ASM	NO ASM
R620	ASM	NO ASM
R372	ASM	NO ASM
R379	NO ASM	ASM

LOGIC

BOM1

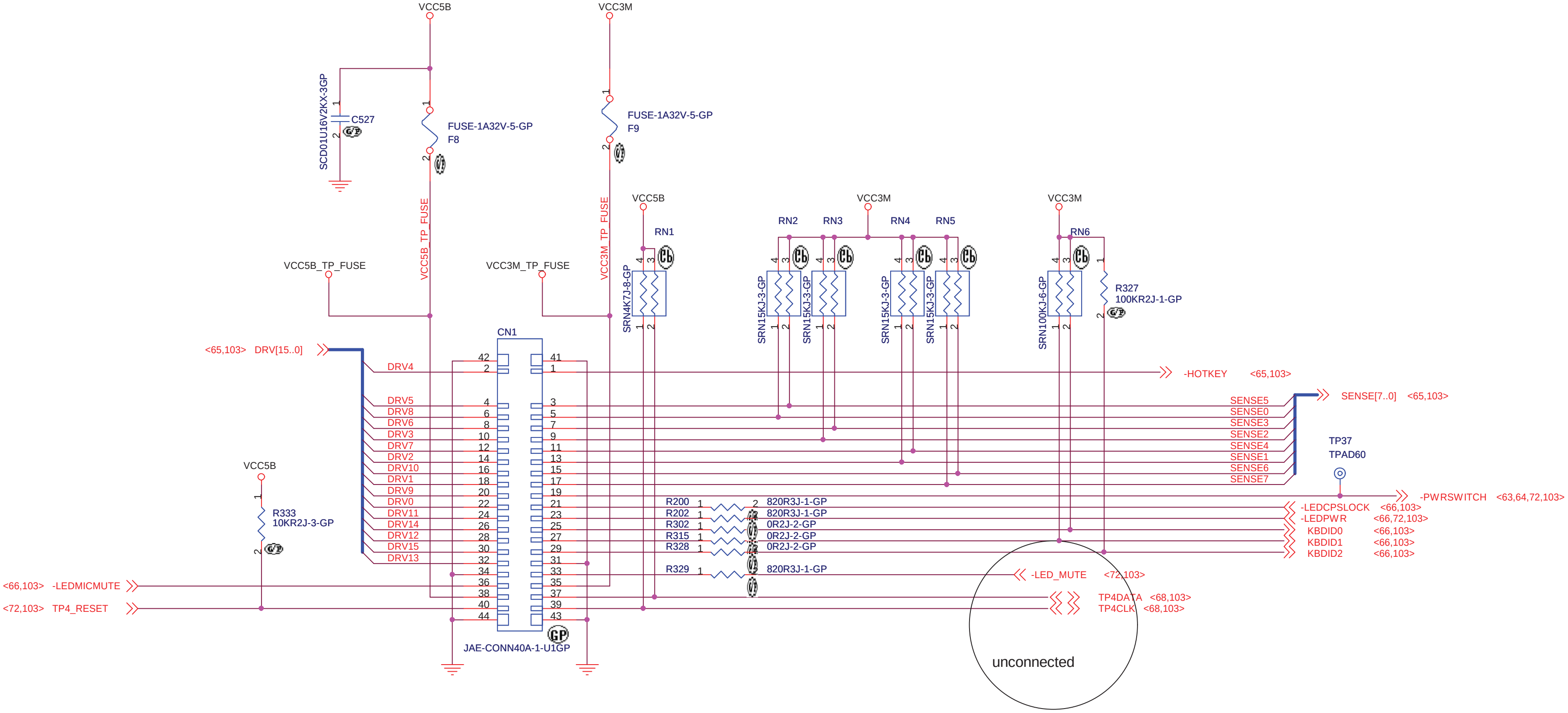
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

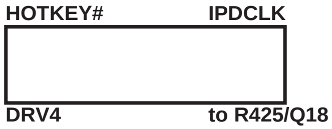
Title
H8S/2112(2/2)

Size A2
Document Number
Kendo-1-UMA
Rev SB

Date: Friday, March 13, 2009 Sheet 66 of 104

Keyboard Connector





Keyboard Connector Top View

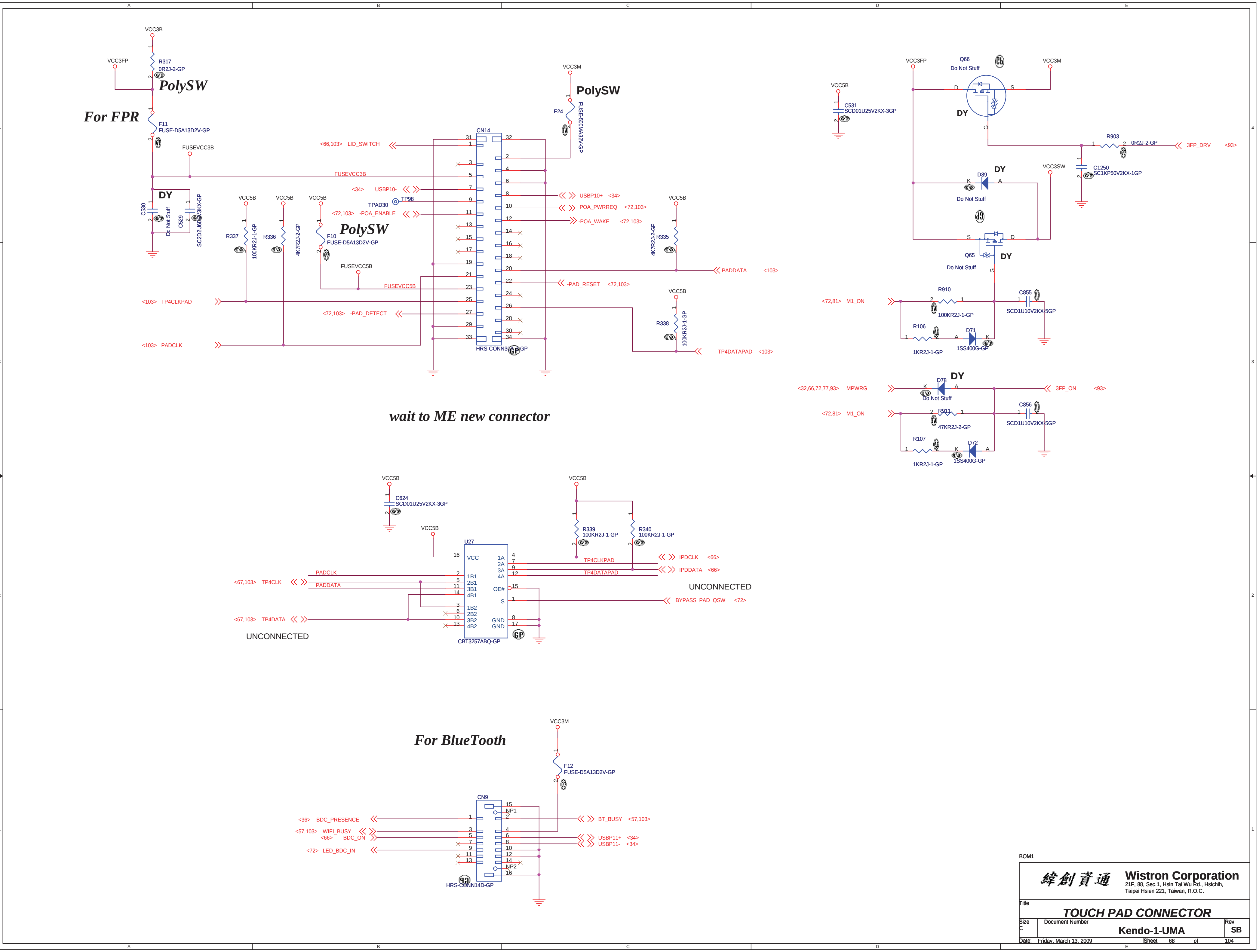
BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
KEYBOARD CONN

Size A3
Document Number **Kendo-1-UMA**
Rev **SB**

Date: Friday, March 13, 2009
Sheet 67 of 104



BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
TOUCH PAD CONNECTOR

Size C
Document Number **Kendo-1-UMA**
Rev SB

Date: Friday, March 13, 2009 Sheet 68 of 104

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BOM1

緯創資通

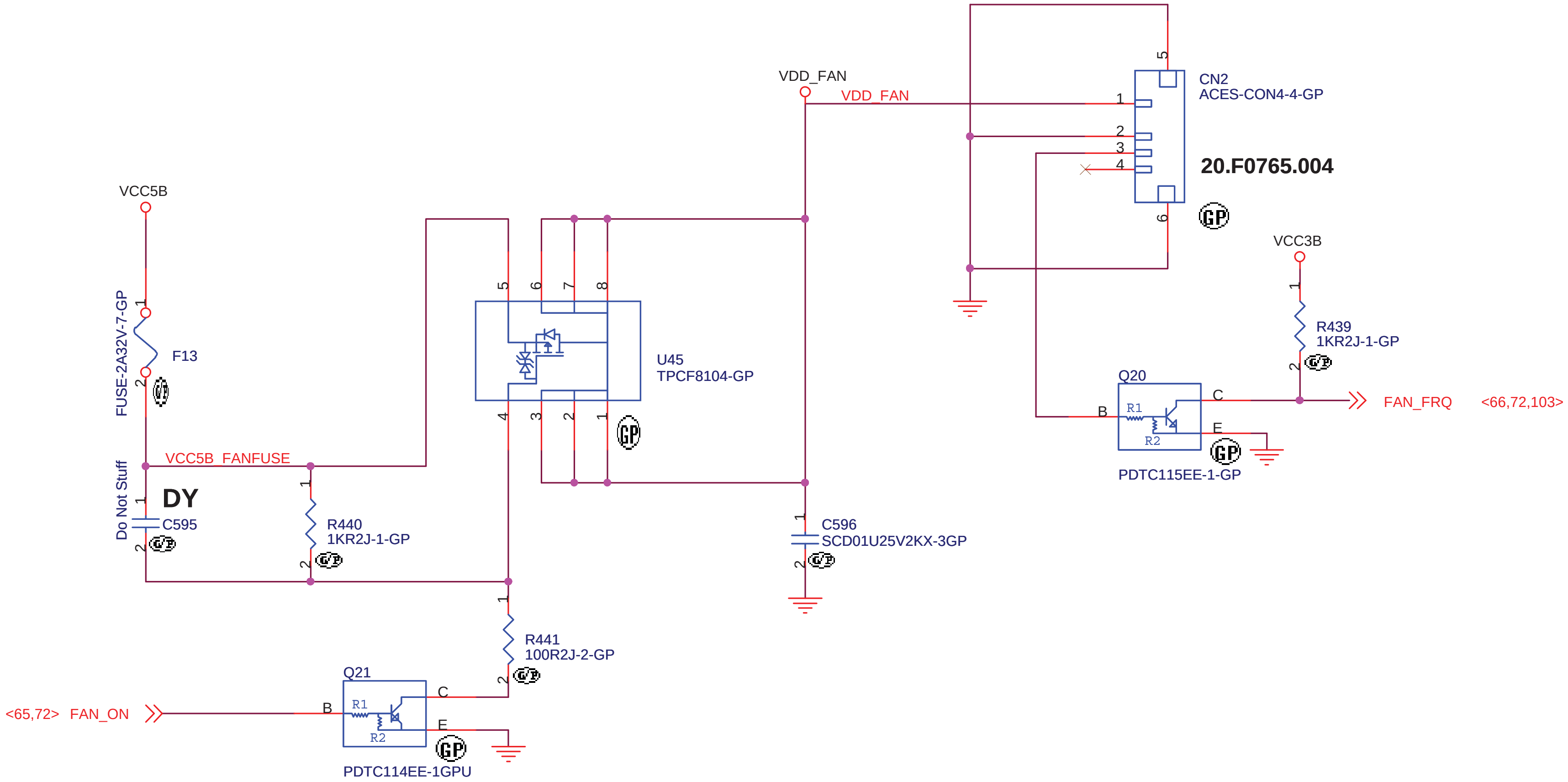
Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
BLANK

Size	Document Number	Rev
A	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 69 of 104



BOM1

緯創資通

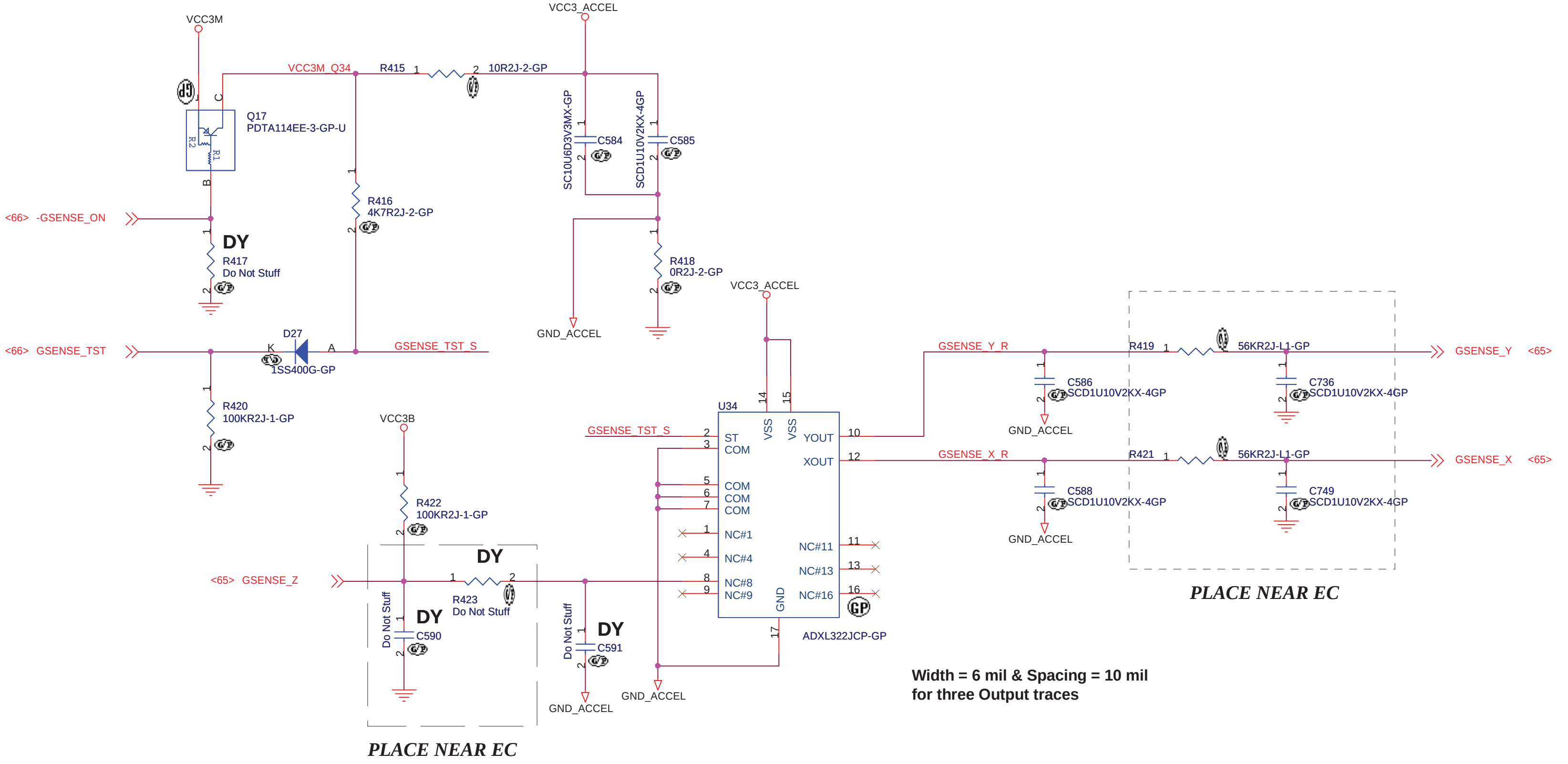
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

FAN CONTROL

Size	Document Number	Rev
A4	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 70 of 104



Width = 6 mil & Spacing = 10 mil
for three Output traces

	ADXL322/LIS244AL LIS34AL	NO ACC.
R417	NO-ASM	ASM
R420	ASM	ASM
U34	ASM	NO-ASM
Q17	ASM	NO-ASM
D27	ASM	NO-ASM
R416	ASM	NO-ASM
R415	10-OHM	NO-ASM
C585	ASM	NO-ASM
C584	ASM	NO-ASM
C586	ASM	NO-ASM
C588	ASM	NO-ASM
C587	56K	NO-ASM
C419	ASM	NO-ASM
R421	56K	NO-ASM
C589	ASM	NO-ASM
C591	NO-ASM	NO-ASM
R423	NO-ASM	NO-ASM
C590	NO-ASM	NO-ASM
R422	ASM	ASM

Layout Comment :

(1) Place C586, C588, Q17, R415, R417,
C584, C585, R420 close to U34.

(2) Avoid routing under DCDC switching area.

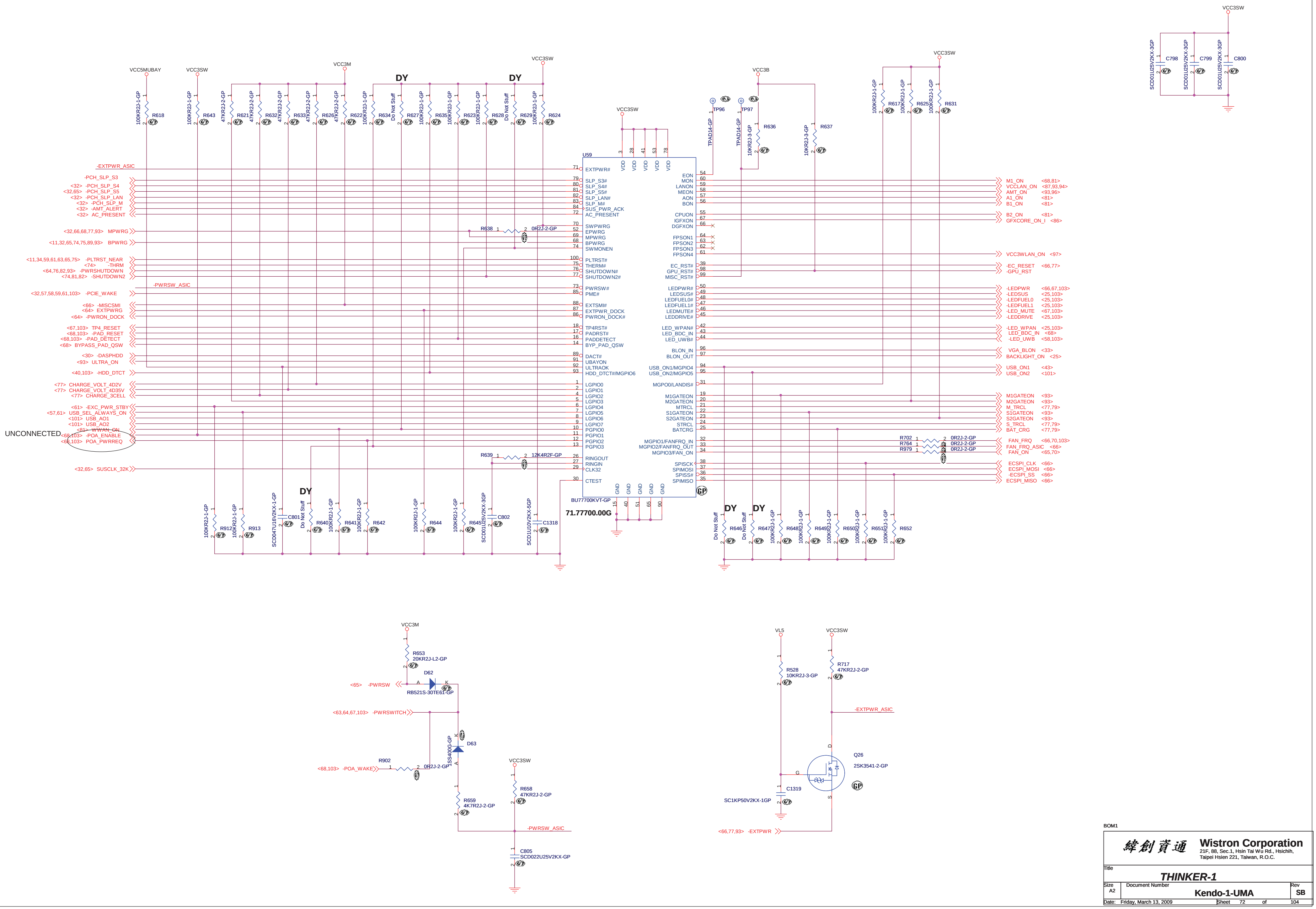
BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
G-SENSOR

Size A3 | Document Number **Kendo-1-UMA** | Rev **SB**

Date: Friday, March 13, 2009 | Sheet 71 of 104



緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
	THINKER-1	
Size A2	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 72 of 104	

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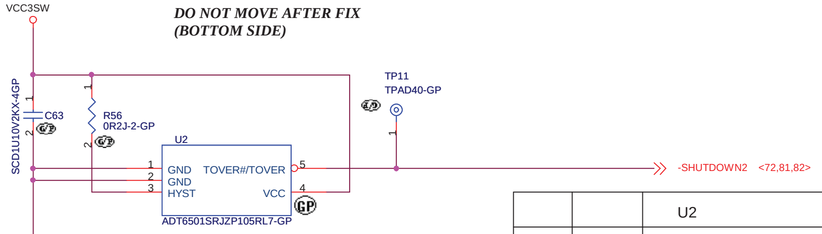
BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title ***BLANK***		
Size A	Document Number **Kendo-1-UMA**	Rev **SB**
Date: Friday, March 13, 2009	Sheet 73 of 104	

Thermal Sensor for CPU

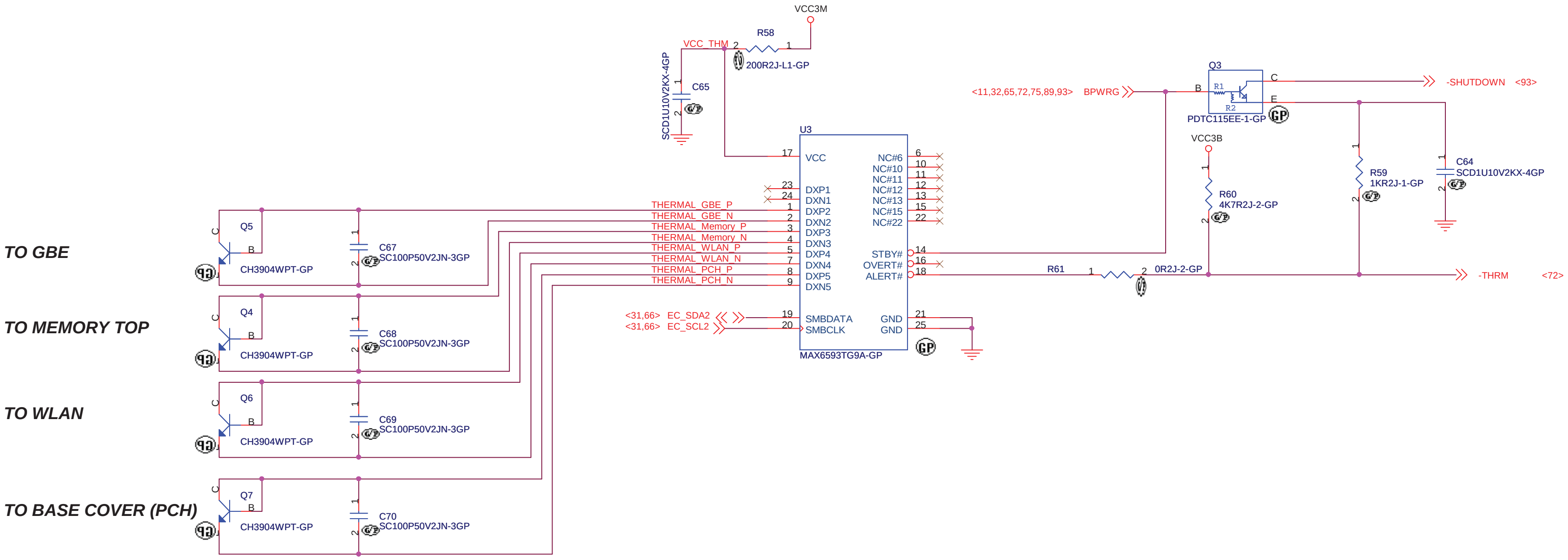
DO NOT MOVE AFTER FIX
(BOTTOM SIDE)



105 DEG.C THRESHHOLD

		U2	R56
1	NS	LM26	NO_ASM
2	MAXIM	MAX6519	NO_ASM
3	ADI	ADT6501SRJZP105RL7	ASM
4	MAXIM	MAX6501UKP105+1	ASM

THESE CAPS MUST BE PLACED AS
CLOSE AS POSSIBLE TO MAX6593



MAX6593
Remote 1: Discrete GFX Address 01h.(Discrete only)
Remote 2: GigaBitEthernet Address 02h.
Remote 3: SODIMM Top Address 03h.
Remote 4: Wireless LAN Slot Address 04h.
Remote 5: Base Cover (Under PCH) Address 05h.
Locall: SODIMM Bottom Address 07h.

PLACE UNDER PC-CARD SLOT

H8 I2C Bus 2 ADDRESS : 4DH

TEST PAD FOR BOARD MFG TEST

Layout Comment :

(1) Thermal sensor trace lines should not be overlapped with other high frequency trace lines in other layers.
(2) Also, it should not be overlapped with large amplitude trace lines either.

BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

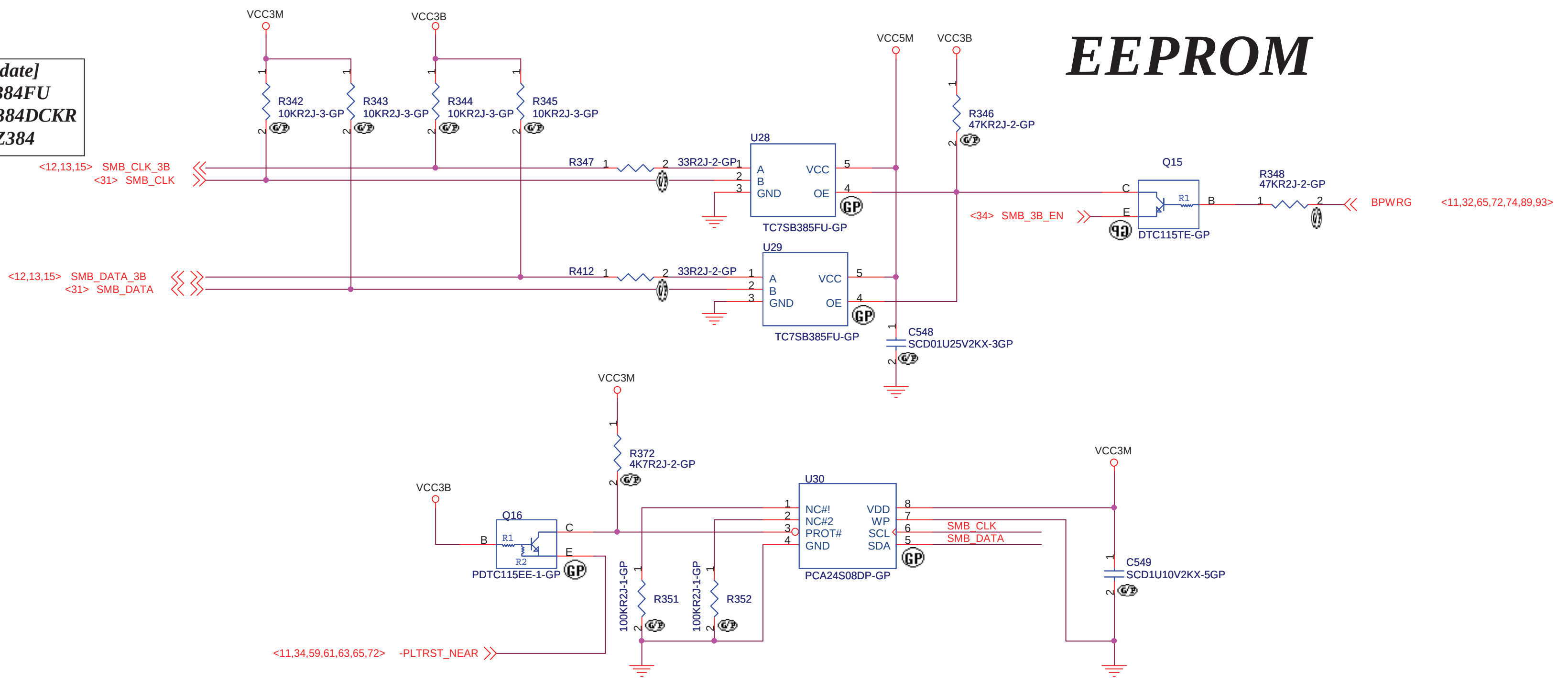
Title **THERMAL SENSOR**

Size Custom Document Number **Kendo-1-UMA** Rev **SB**

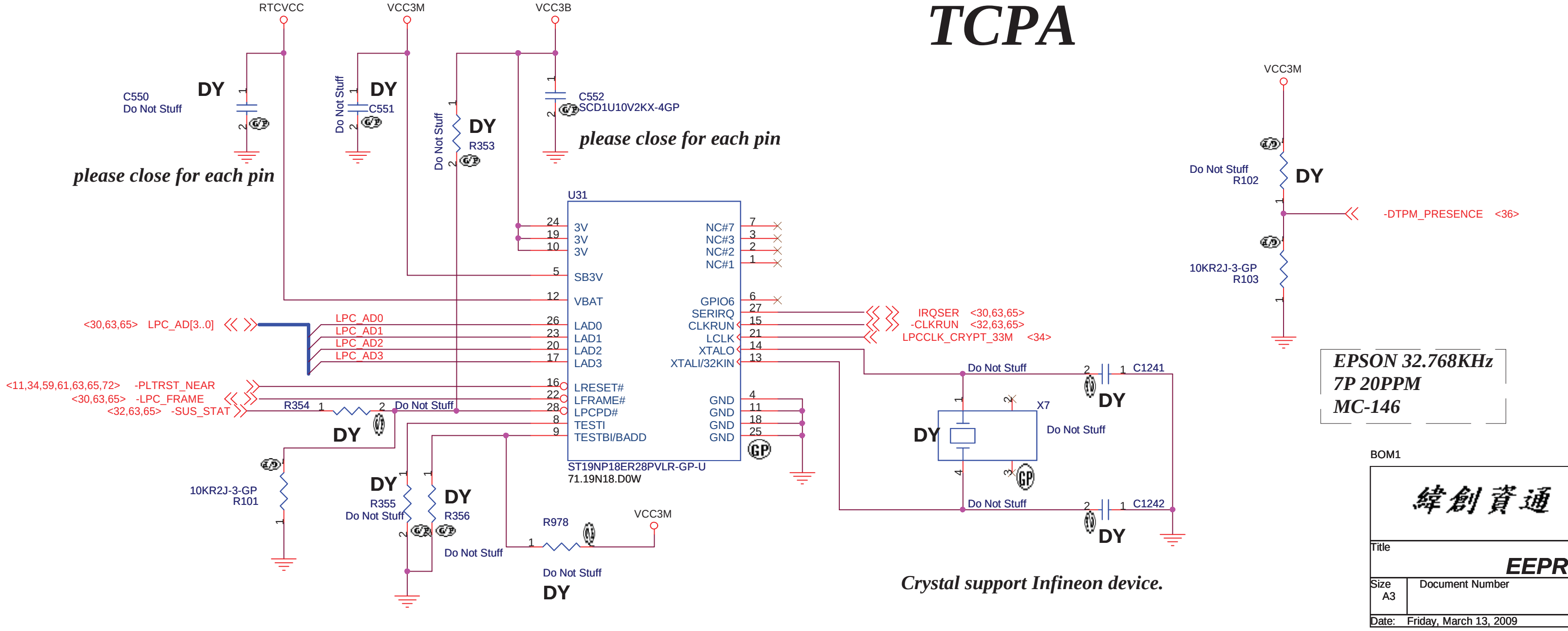
Date: Friday, March 13, 2009 Sheet 74 of 104

[Sourcing Candidate]
Toshiba TC7SB384FU
TI SN74CBT1G384DCKR
Fairchild NC7SZ384

EEPROM



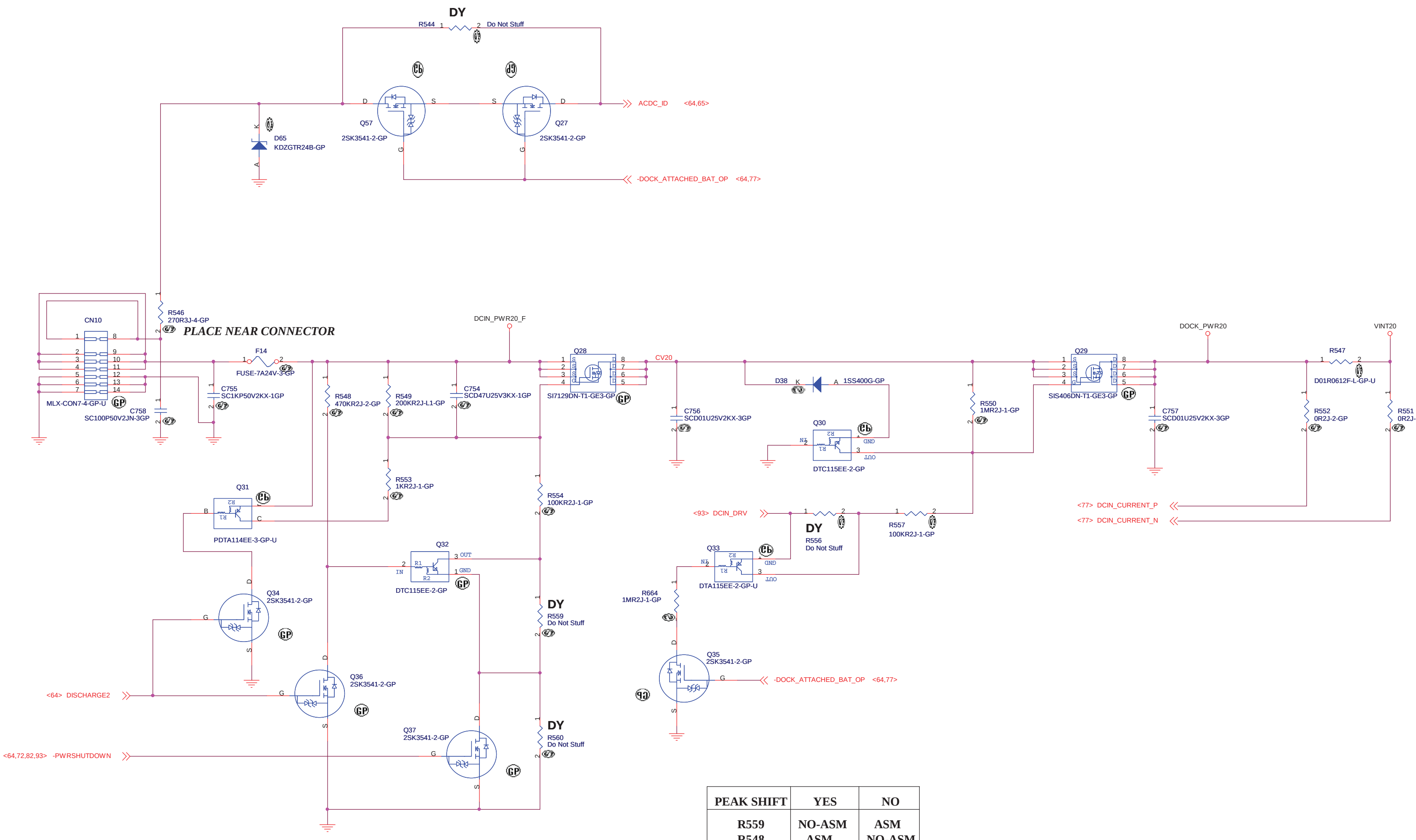
TCPA



BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	EEPROM/TCPA	
Size A3	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 75 of 104	



PEAK SHIFT	YES	NO
R559	NO-ASM	ASM
R548	ASM	NO-ASM
Q32	ASM	NO-ASM
Q36	ASM	NO-ASM

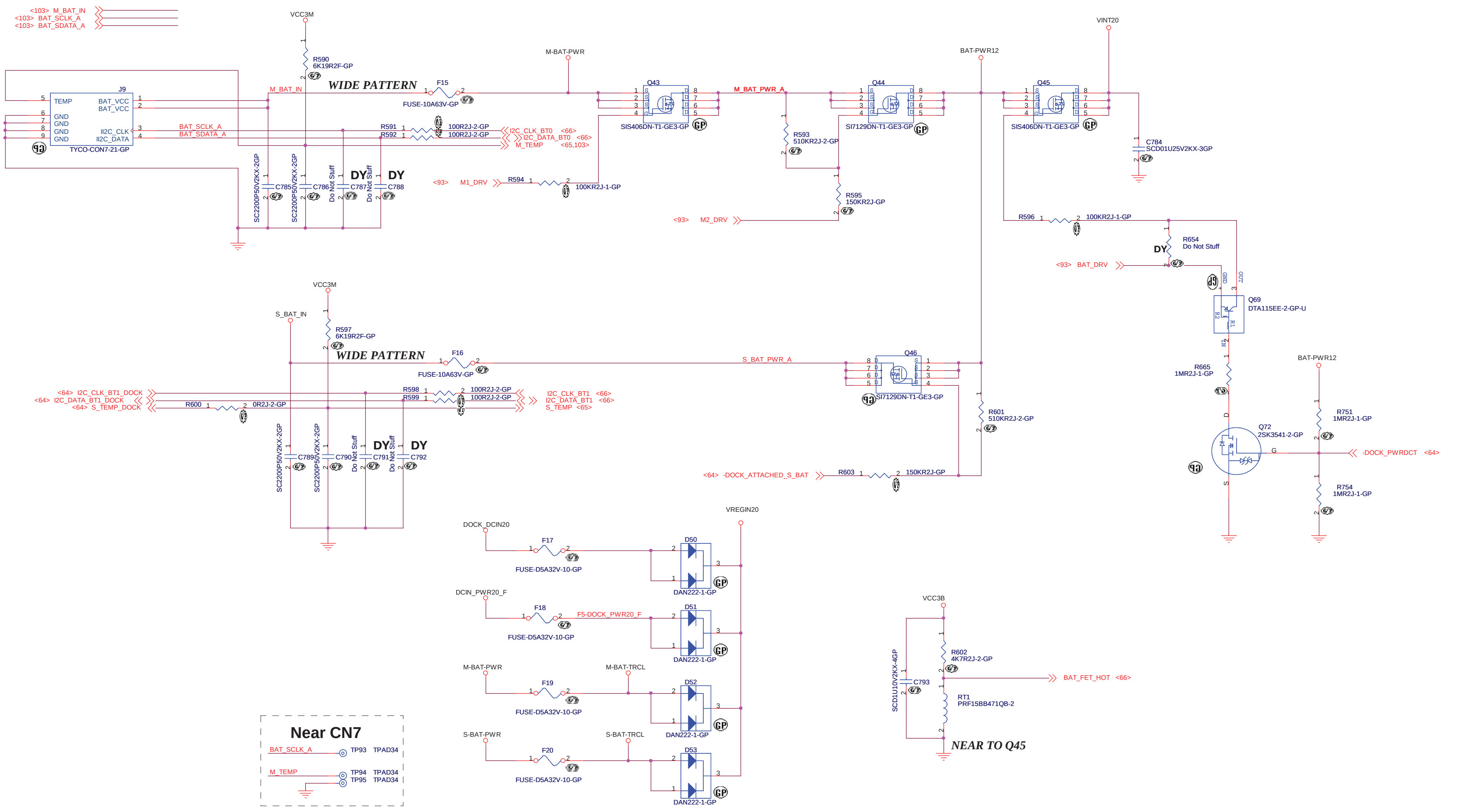
BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
DC-IN

Size A2 | Document Number **Kendo-1-UMA** | Rev **SB**

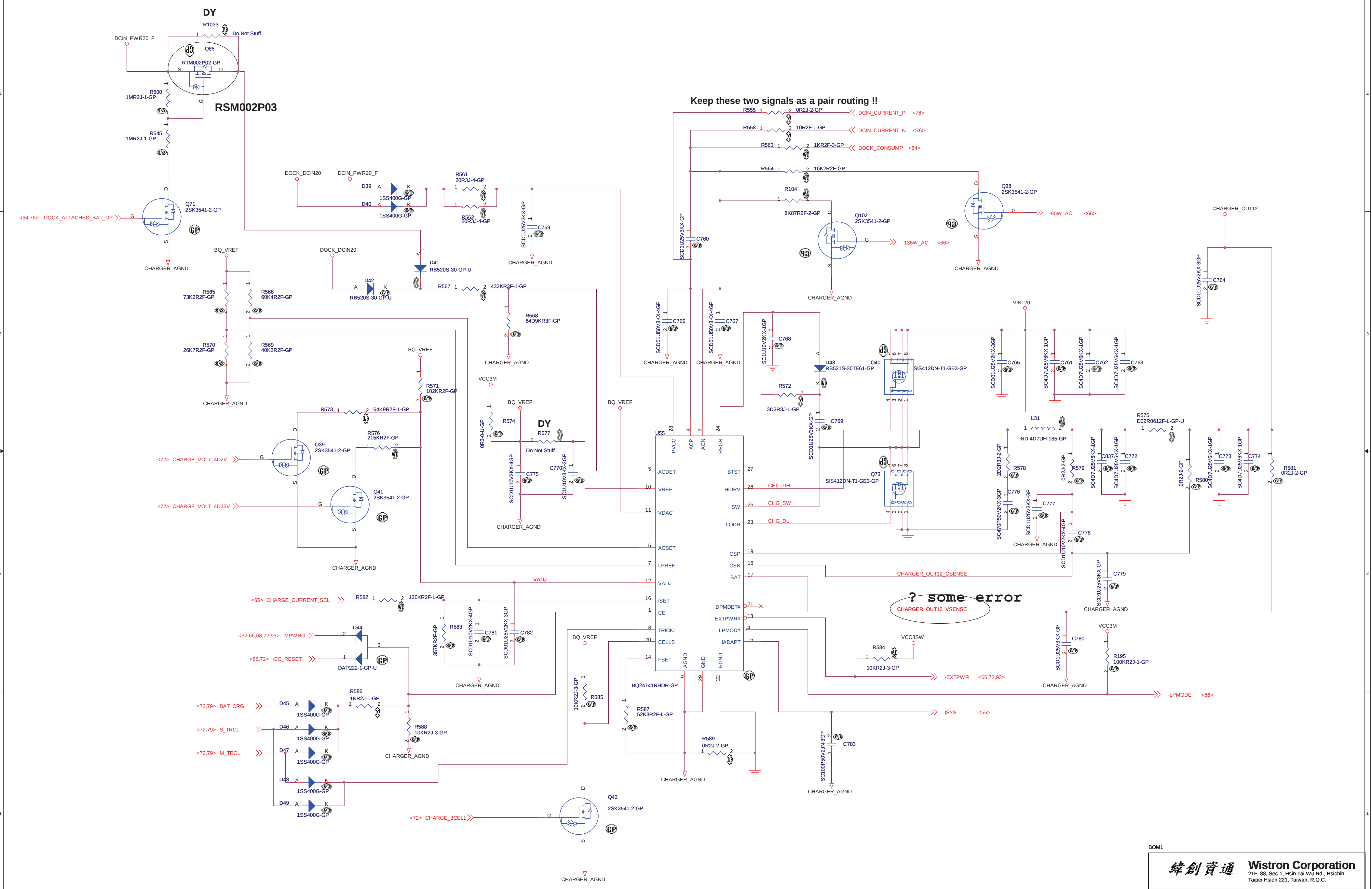
Date: Friday, March 13, 2009 | Sheet 76 of 104



BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
	BATTERY INPUT	
Size A2	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 77 of 104	



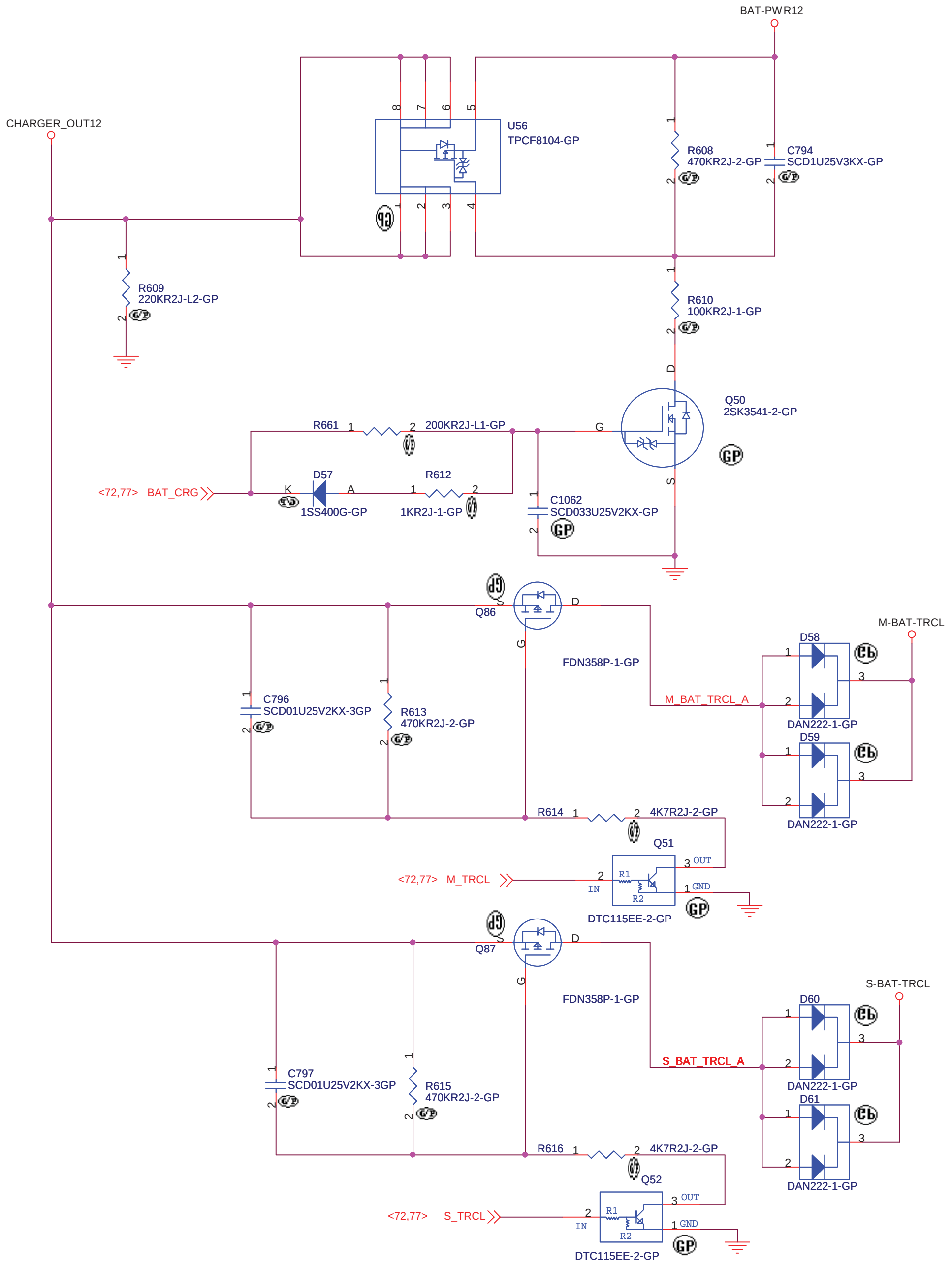
BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
BATTERY CHARGER (BQ24741)

Size A2 | Document Number Kendo-1-UMA | Rev SB

Date: Friday, March 13, 2009 | Sheet 78 of 104

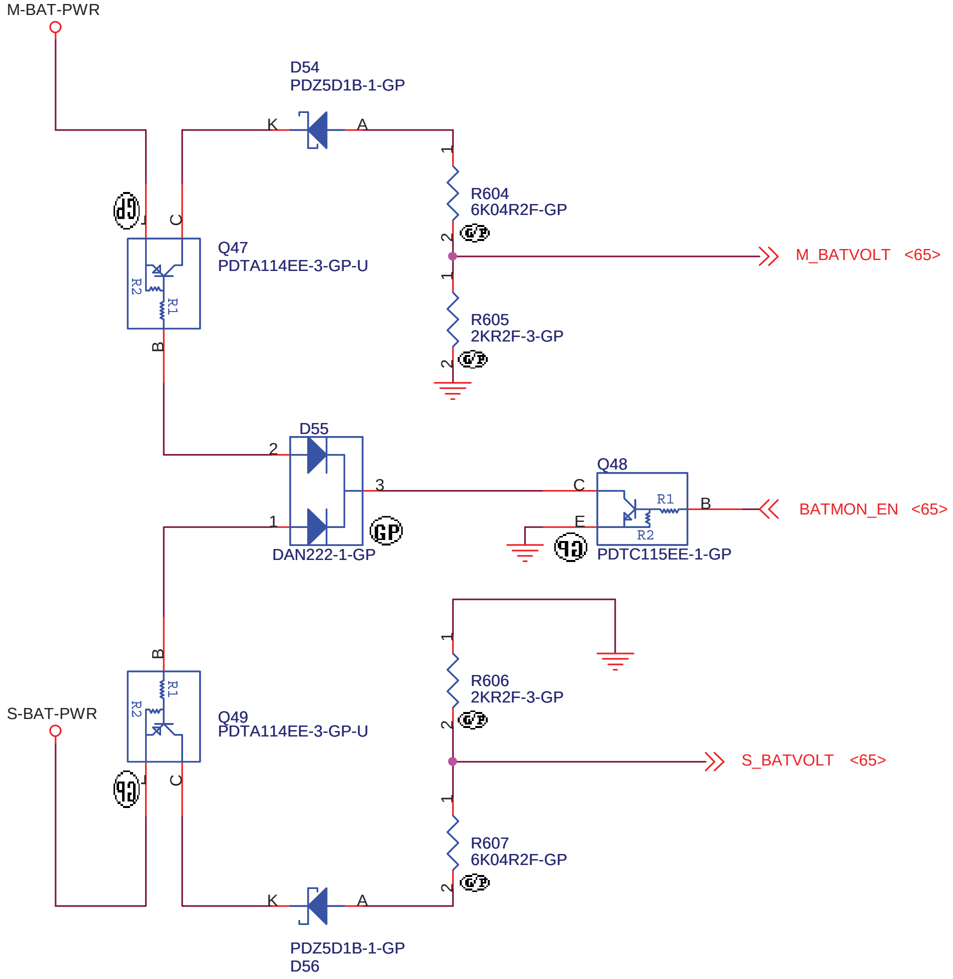


BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	**CHARGER SELECT**	
Size A3	Document Number **Kendo-1-UMA**	Rev **SB**
Date: Friday, March 13, 2009	Sheet 79 of 104	

VOUT = 0.249 (VBAT - 5)



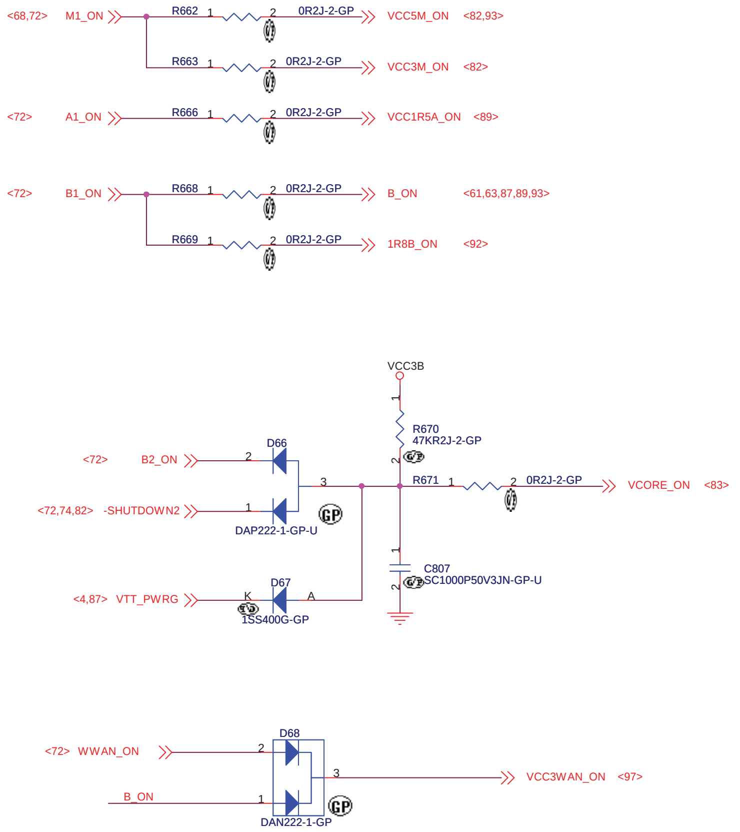
BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
BATTERY MONITOR

Size	Document Number	Rev
A4	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 80 of 104



Table

ALWAYS ON	**YES**	**NO**
D68	**ASM**	**NO_ASM**

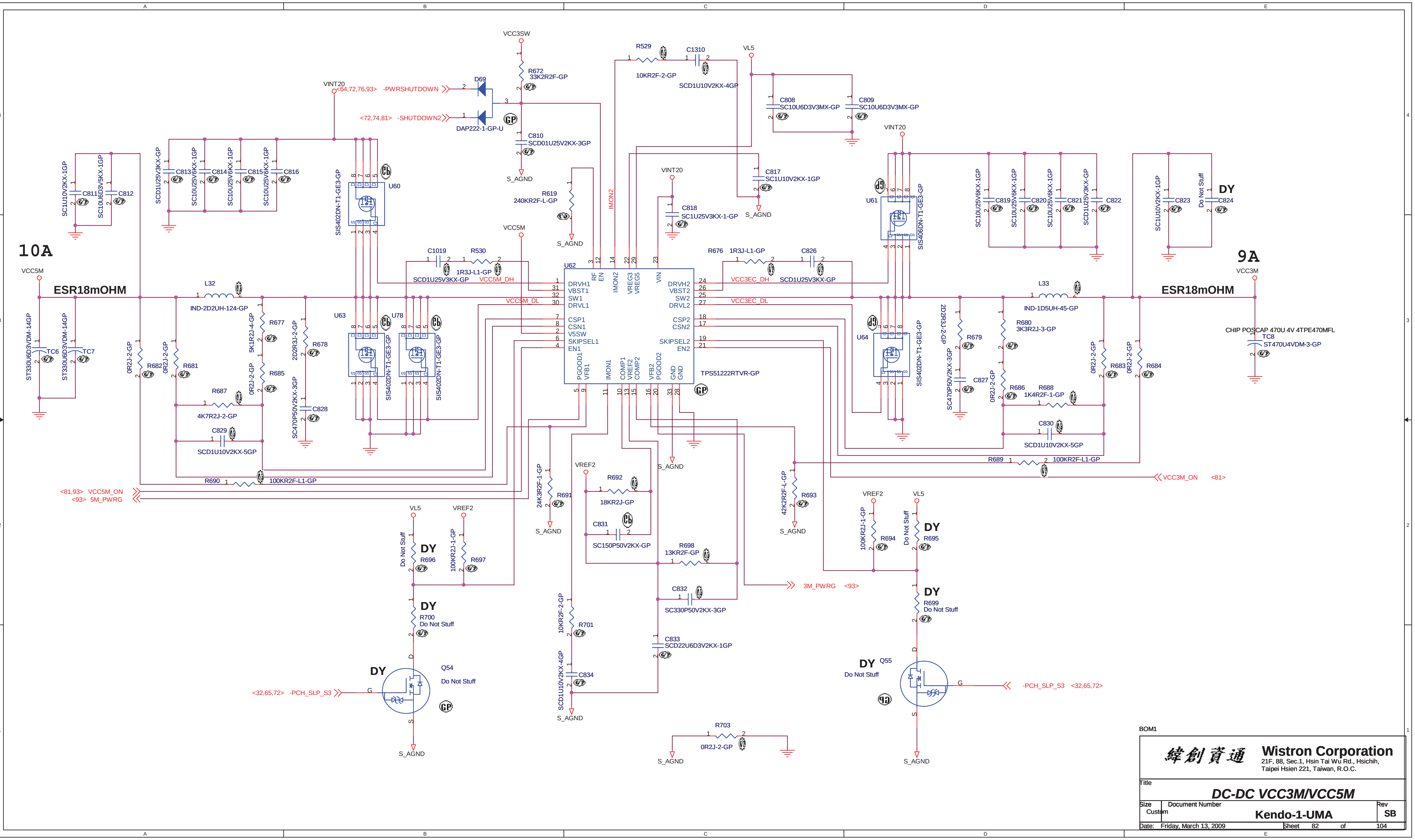
BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
POWER SEQUENCE

Size A3
Document Number **Kendo-1-UMA**
Rev **SB**

Date: Friday, March 13, 2009
Sheet 81 of 104



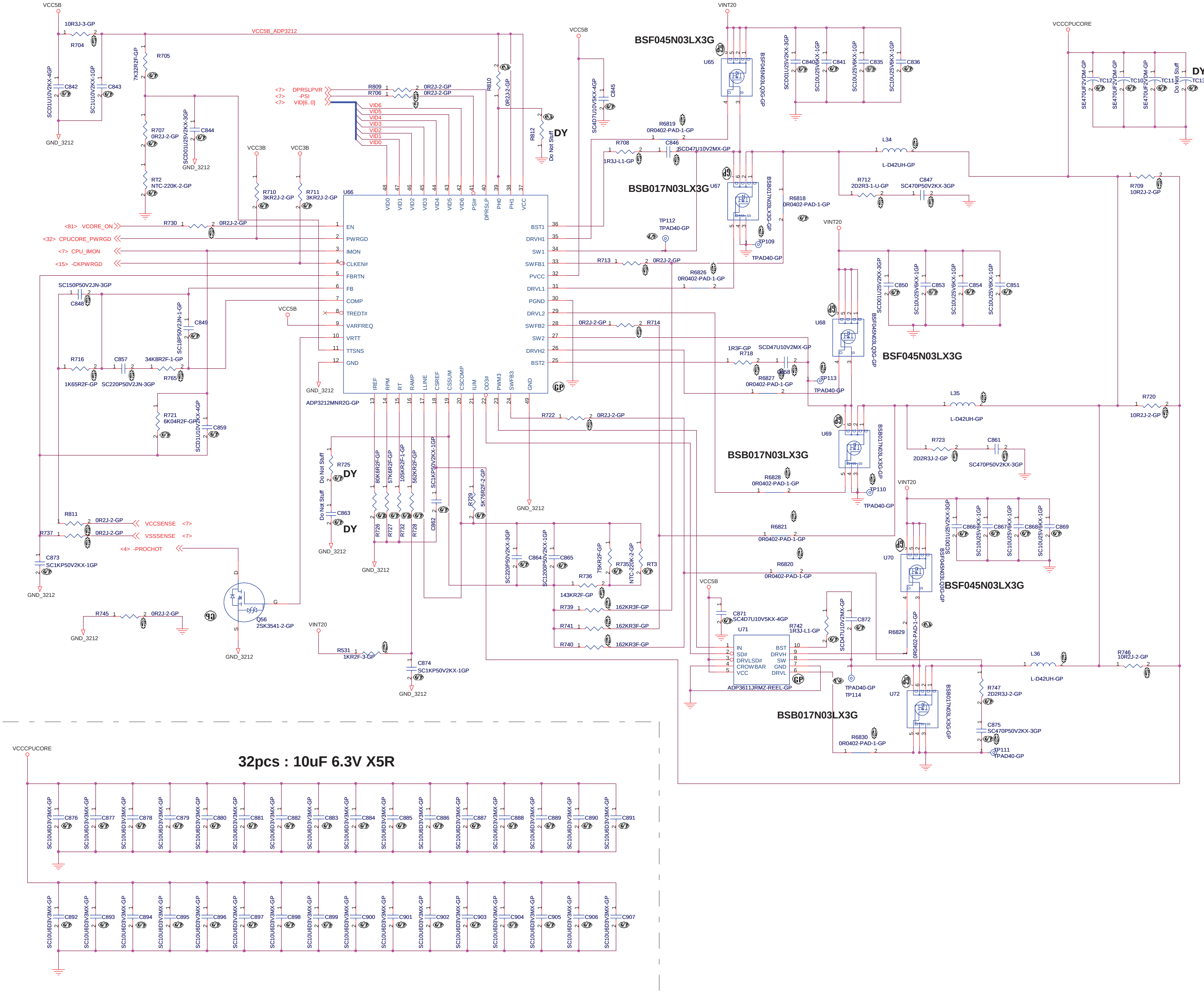
BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
DC-DC VCC3M/VCC5M

Size	Document Number	Rev
Custom	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 82 of 104



BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	**DC-DC VCCCPUCORE**	
Size A2	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 83 of 104	

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BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	***BLANK***	
Size A	Document Number **Kendo-1-UMA**	Rev **SB**
Date: Friday, March 13, 2009	Sheet 84 of 104	

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BOM1

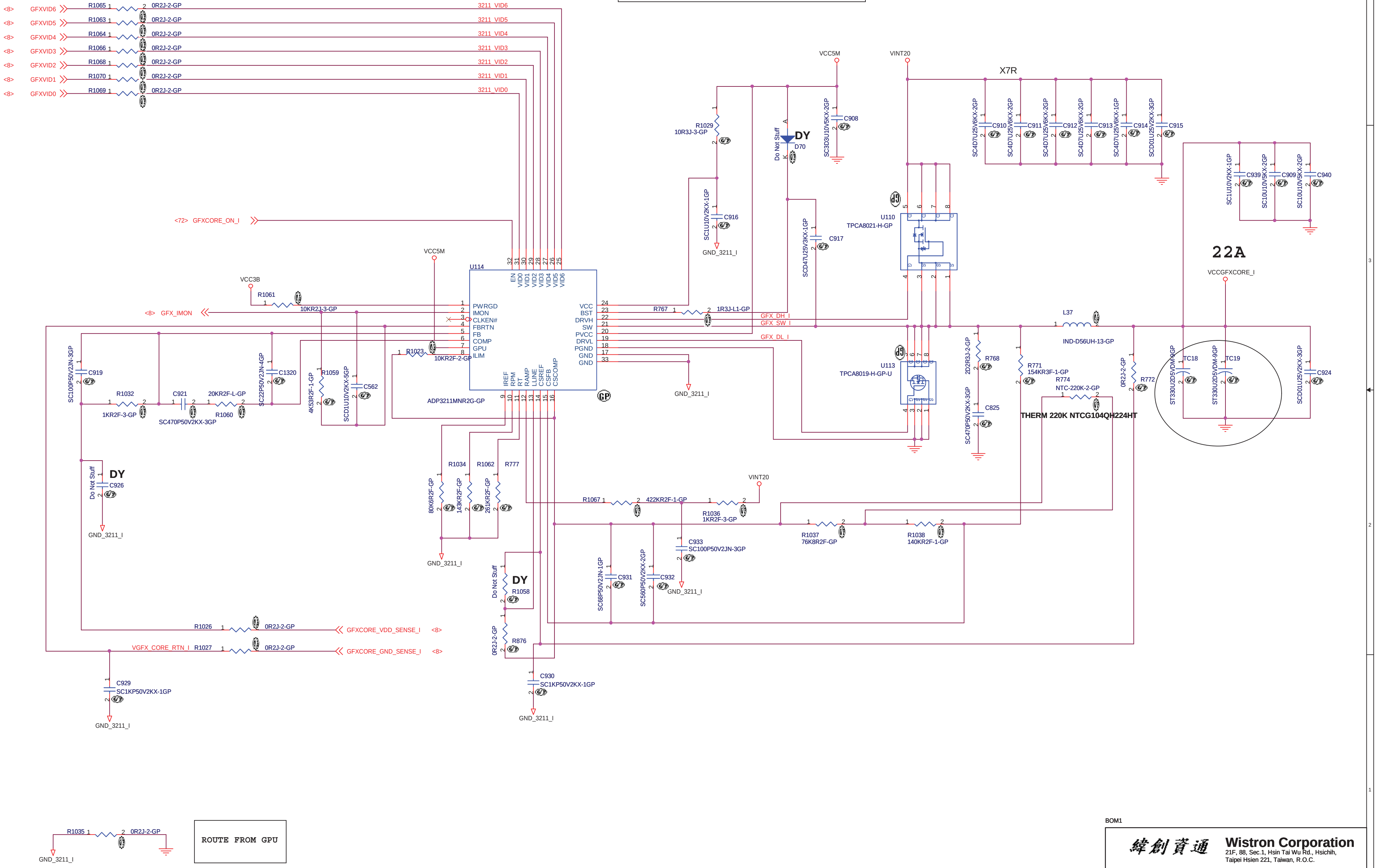
緯創資通 **Wistron Corporation**

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	*BLANK*	
Size A	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 85 of 104	

INTEG VCCGFXCORE VOLTAGE
1.NORMAL; 1.05V
2.GRAPHICS RENDER STANDBY MODE; 0.75V

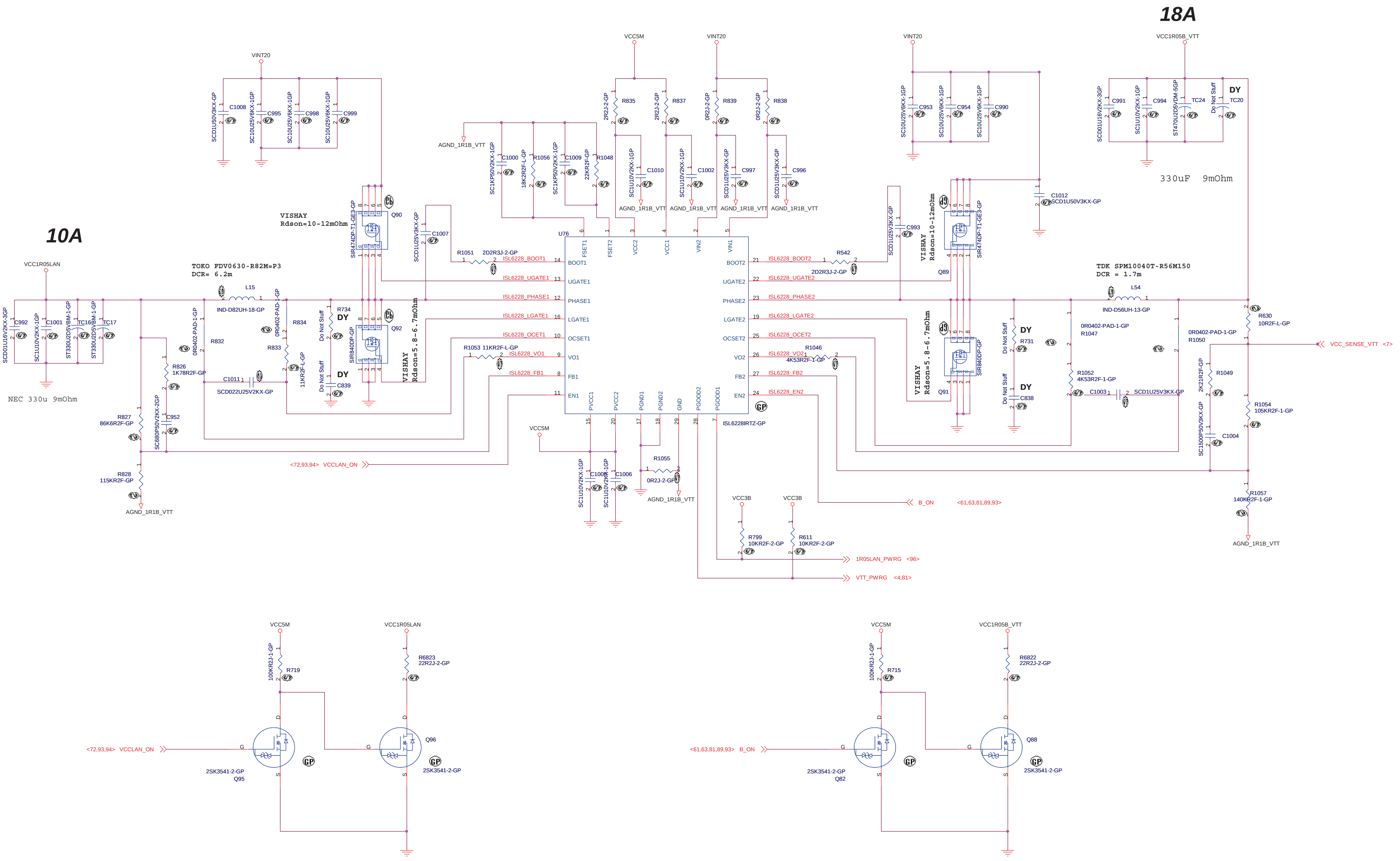
VID <4..0>
01000 = 1.05V



BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	DC-DC GFXCORE_I		
Size C	Document Number	Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 86	of 104	



BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	DC-DC VCC1R05B_VTT	
Size A2	Document Number Kendo-1-UMA	Rev SB
Date: Friday, March 13, 2009	Sheet 87 of 104	

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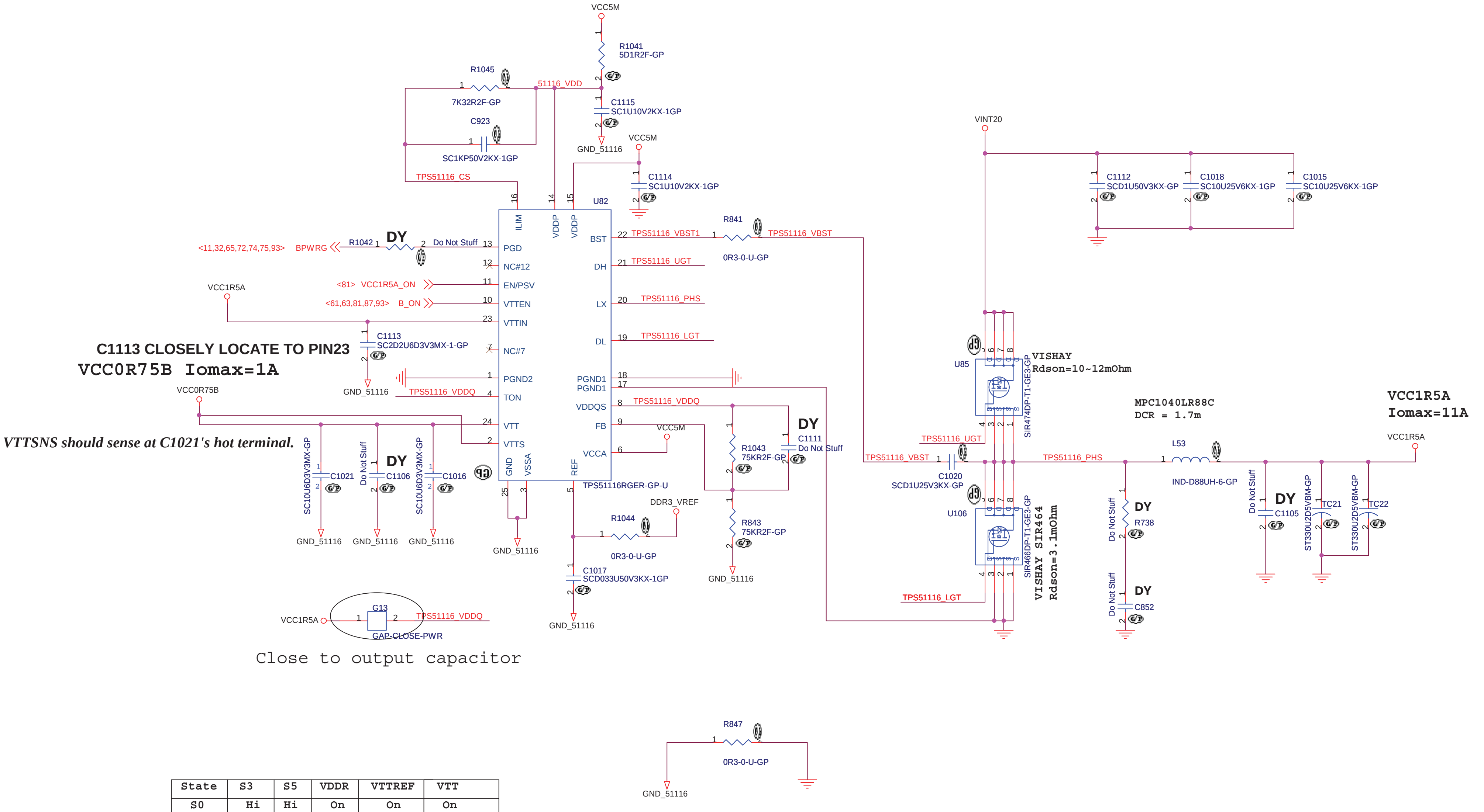
BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title
BLANK

Size	Document Number	Rev
A3	**Kendo-1-UMA**	**SB**

Date: Friday, March 13, 2009 Sheet 88 of 104



State	S3	S5	VDDR	VTTREF	VTT
S0	Hi	Hi	On	On	On
S3	Lo	Hi	On	On	Off(Hi-Z)
S4/S5	Lo	Lo	Off	Off	Off

BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title **DC-DC VCC1R5A & VCC0R75B**

Size	Document Number	Rev
A3	Kendo-1-UMA	SB

Date: Friday, March 13, 2009 Sheet 89 of 104

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BOM1

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
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Title	***BLANK***	
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Title
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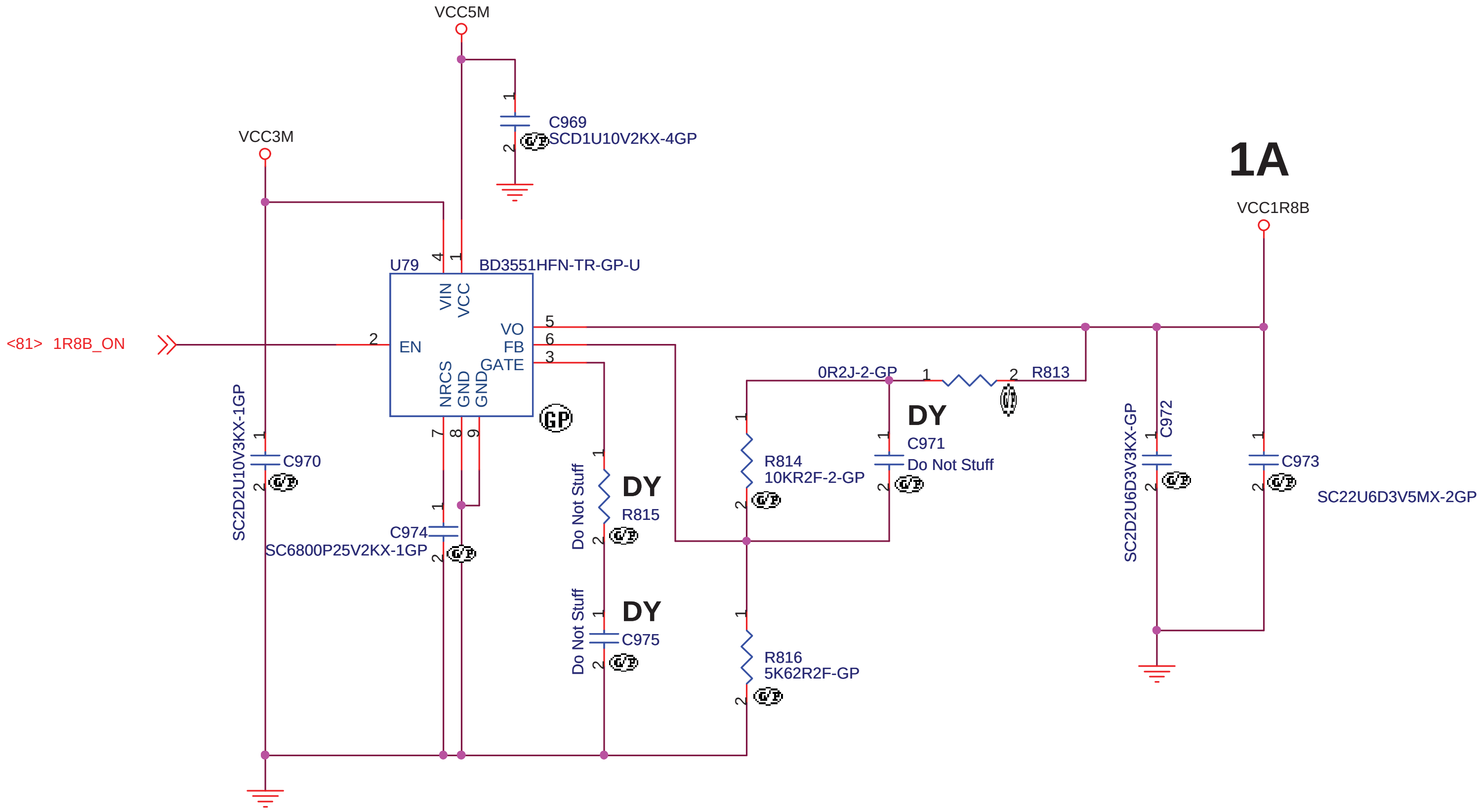
Size A
Document Number **Kendo-1-UMA**
Rev **SB**

Date: Friday, March 13, 2009



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Title

DC-DC VCC1R8B

Size A4

Document Number

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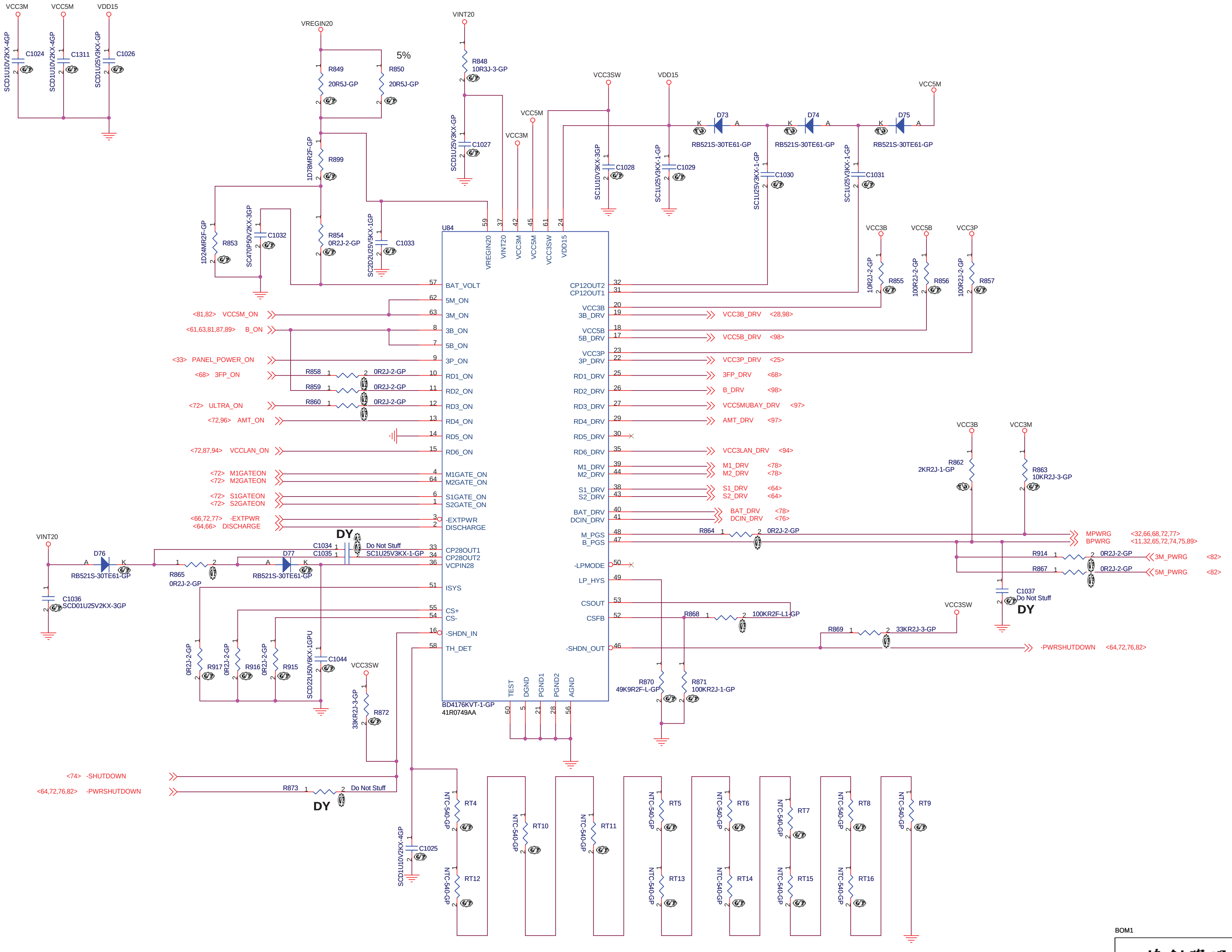

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Title
DC-DC RINKAN-2

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Custom	Kendo-1-UMA	SB

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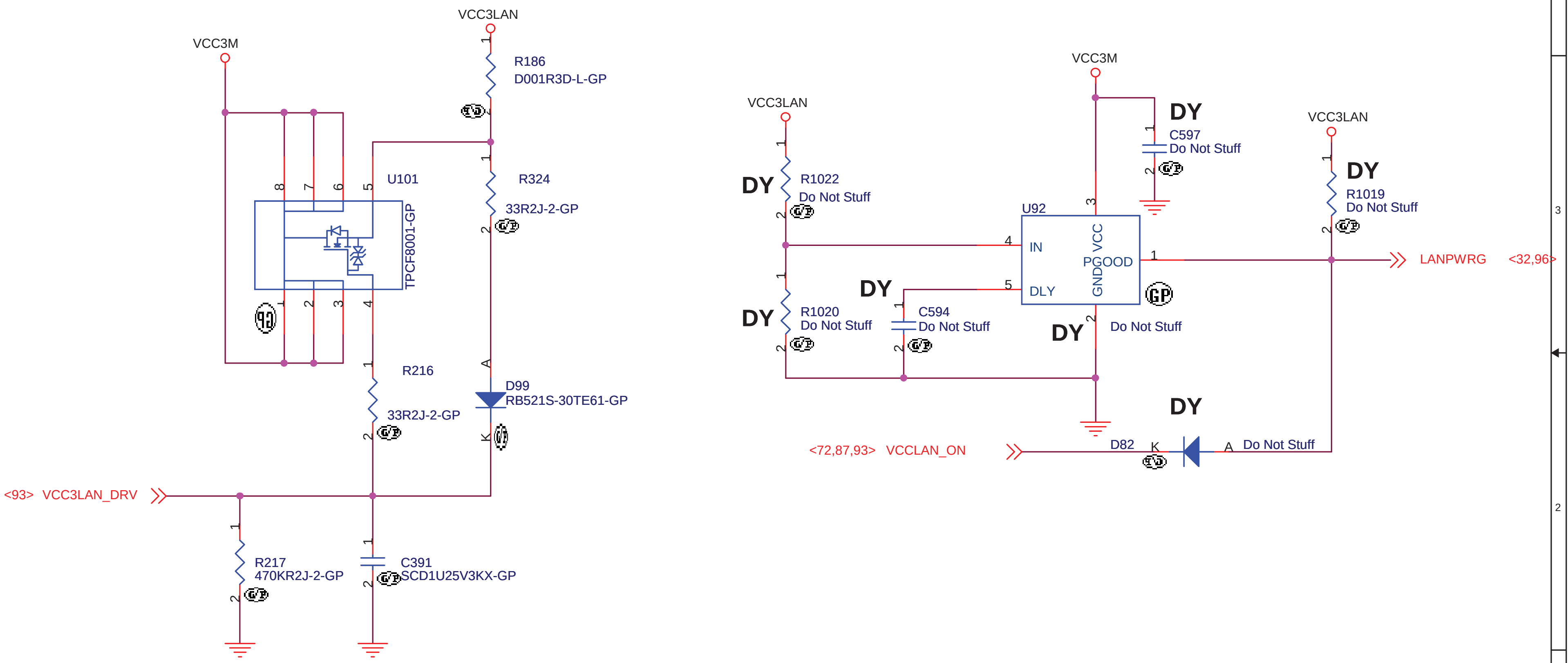

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Title
LOAD SW LAN & LANPWRG

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Title

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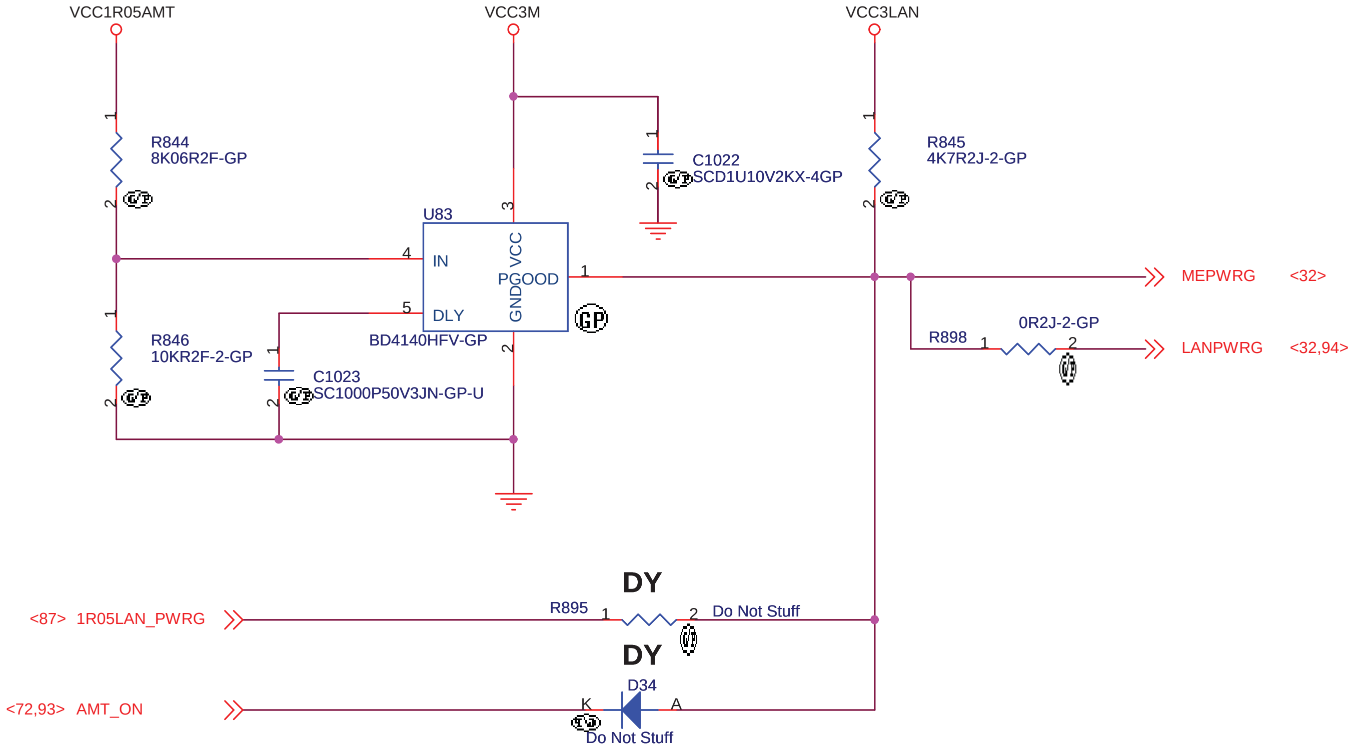
Document Number **Kendo-1-UMA**

Rev **SB**

Date: Friday, March 13, 2009



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Title
MEPWRG

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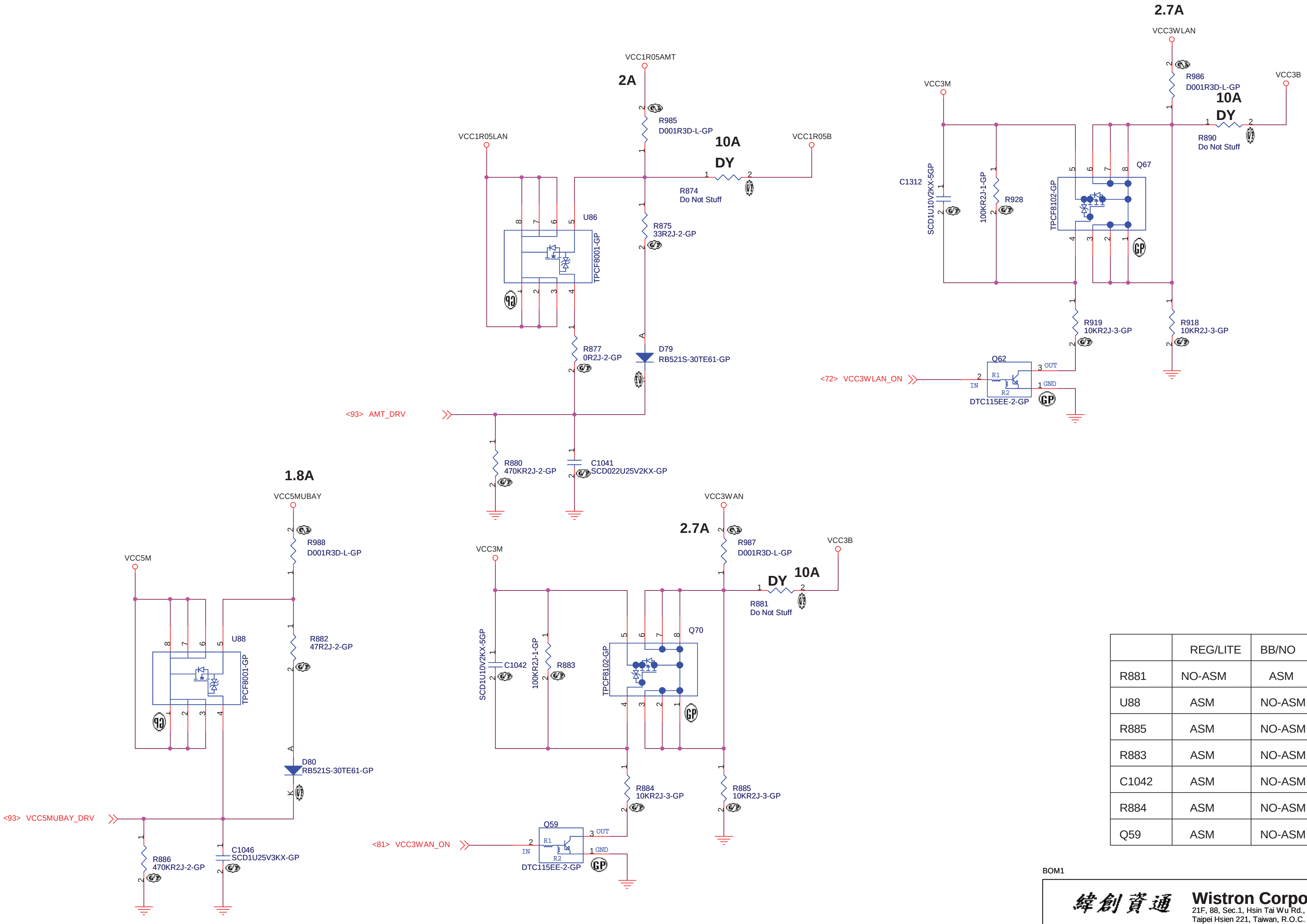

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R881	NO-ASM	ASM
U88	ASM	NO-ASM
R885	ASM	NO-ASM
R883	ASM	NO-ASM
C1042	ASM	NO-ASM
R884	ASM	NO-ASM
Q59	ASM	NO-ASM

BOM1

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Title
LOAD SW B

Size A3
Document Number **Kendo-1-UMA**
Rev **SB**

Date: Friday, March 13, 2009
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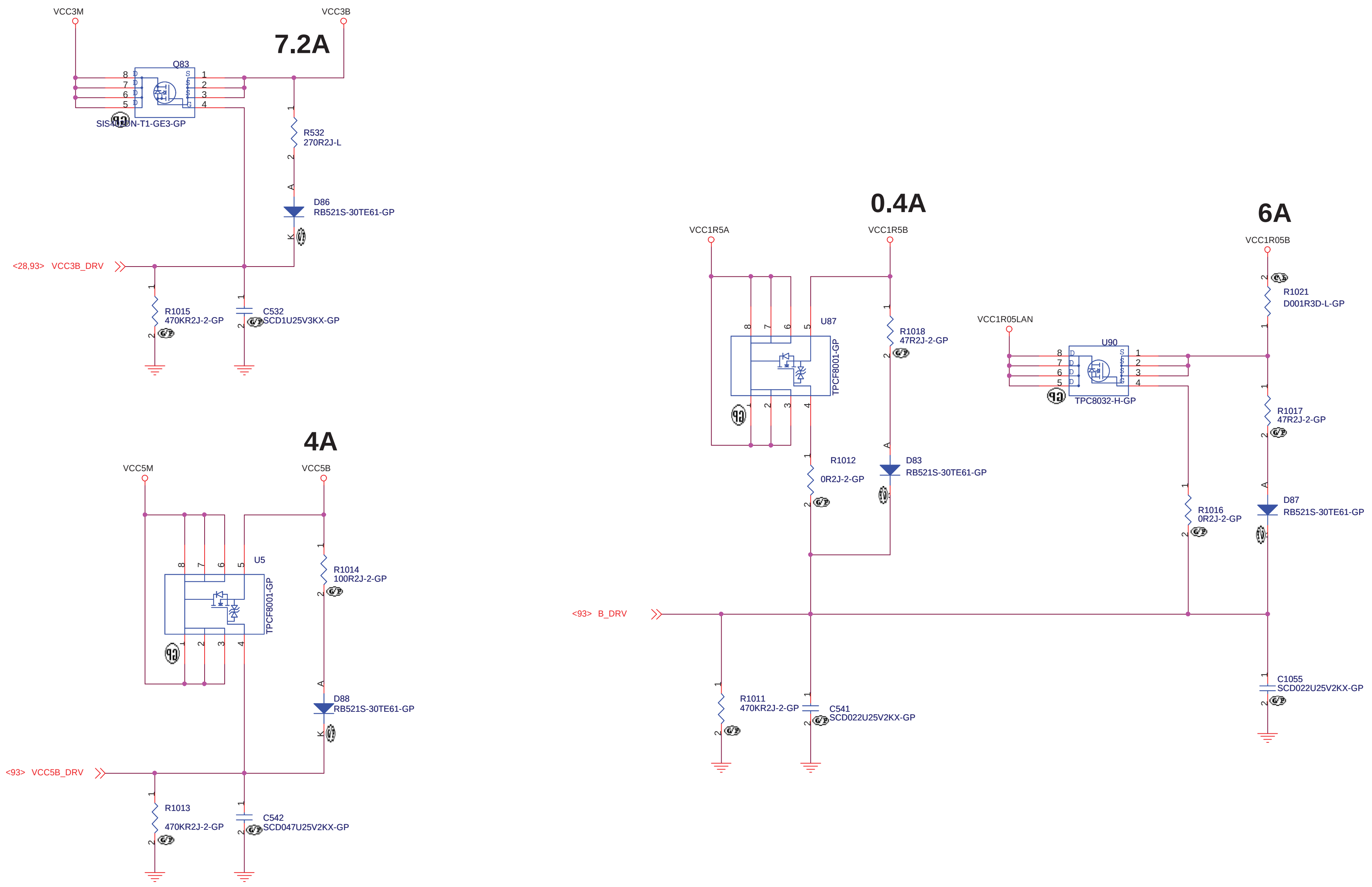

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Title	*LOAD SW1*	
Size A3	Document Number **Kendo-1-UMA**	Rev **SB**
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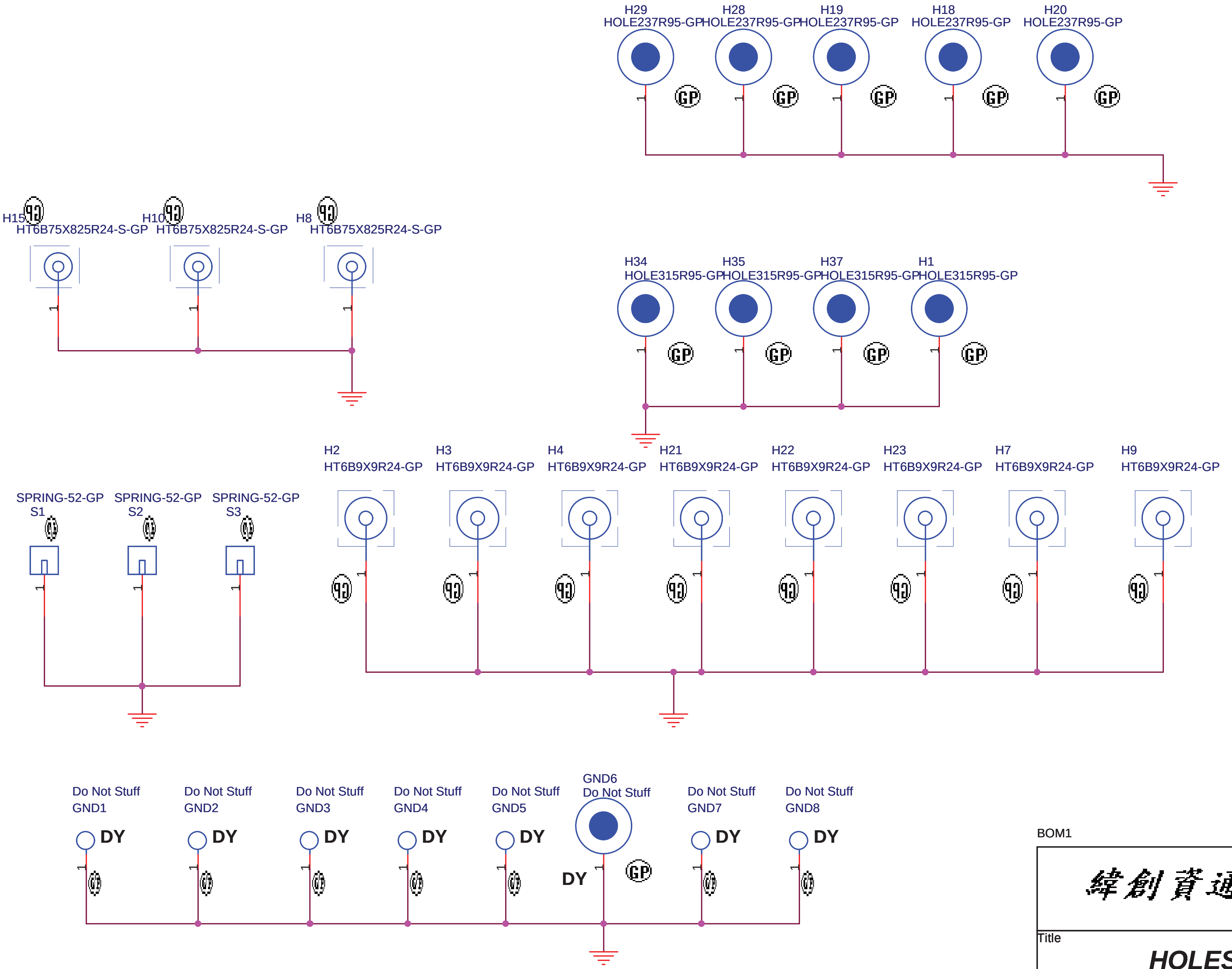
Size A	Document Number **Kendo-1-UMA**		Rev **SB**
Date:	Friday, March 13, 2009	Sheet 99 of 104	



BOM1

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Title
HOLES/GND/PADS

Size A4
Document Number **Kendo-1-UMA**
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Date: Friday, March 13, 2009
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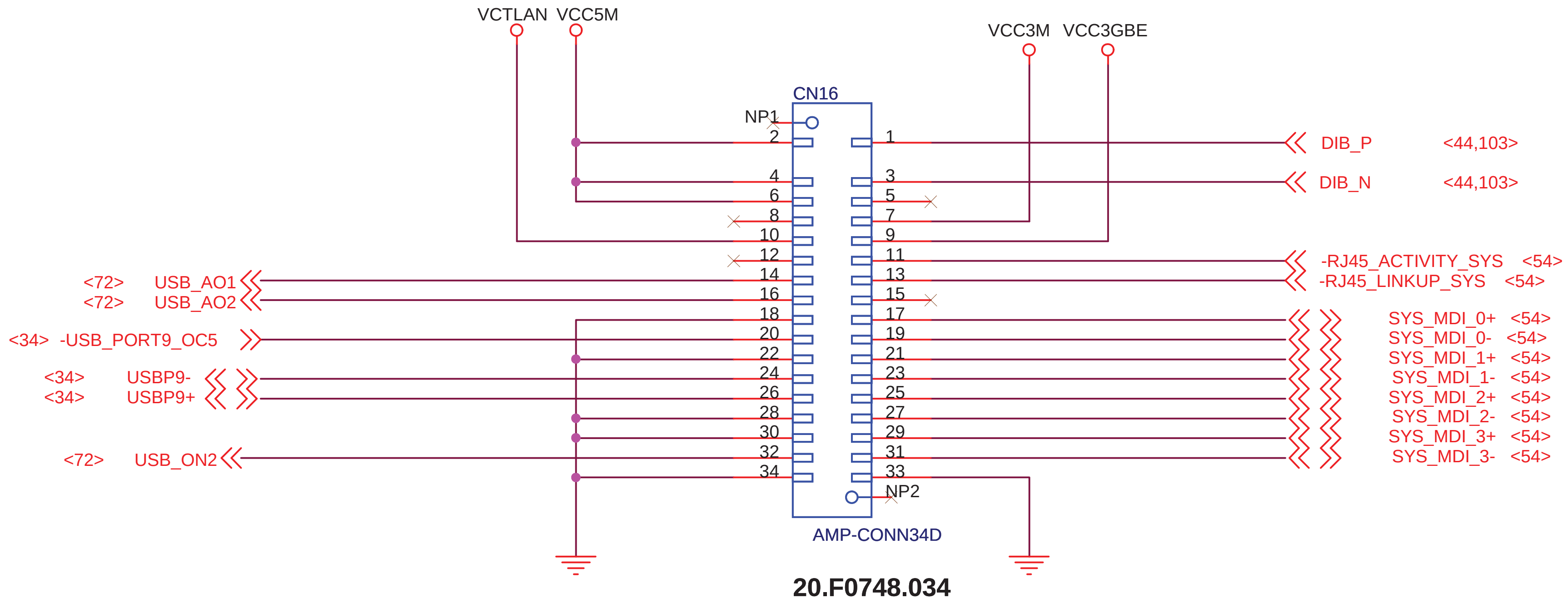

Pull-down resistor is necessary on USB_AO1/2
for PI3USB14LE on the USB BRD.

BOM1

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Title
USB BOARD CONNECTOR

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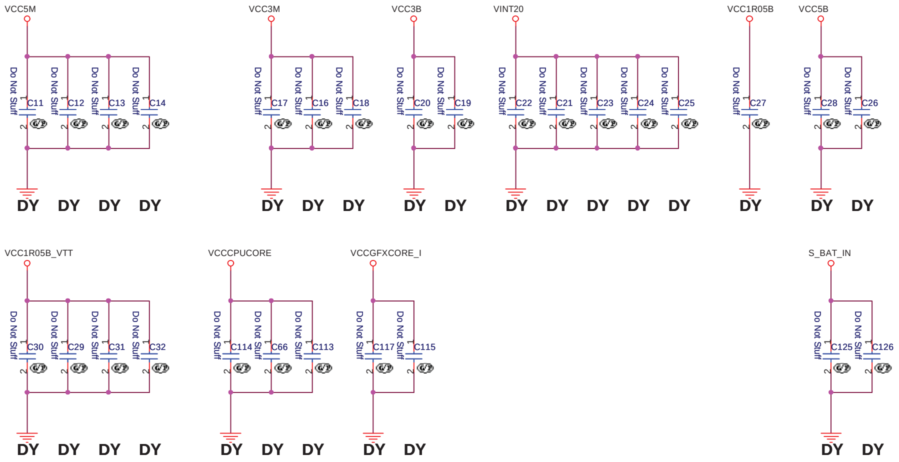
Long power trace EMI decoupling caps



BOM1

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Title	**EMI DECOUPLING**	
Size Custom	Document Number **Kendo-1-UMA**	Rev **SB**
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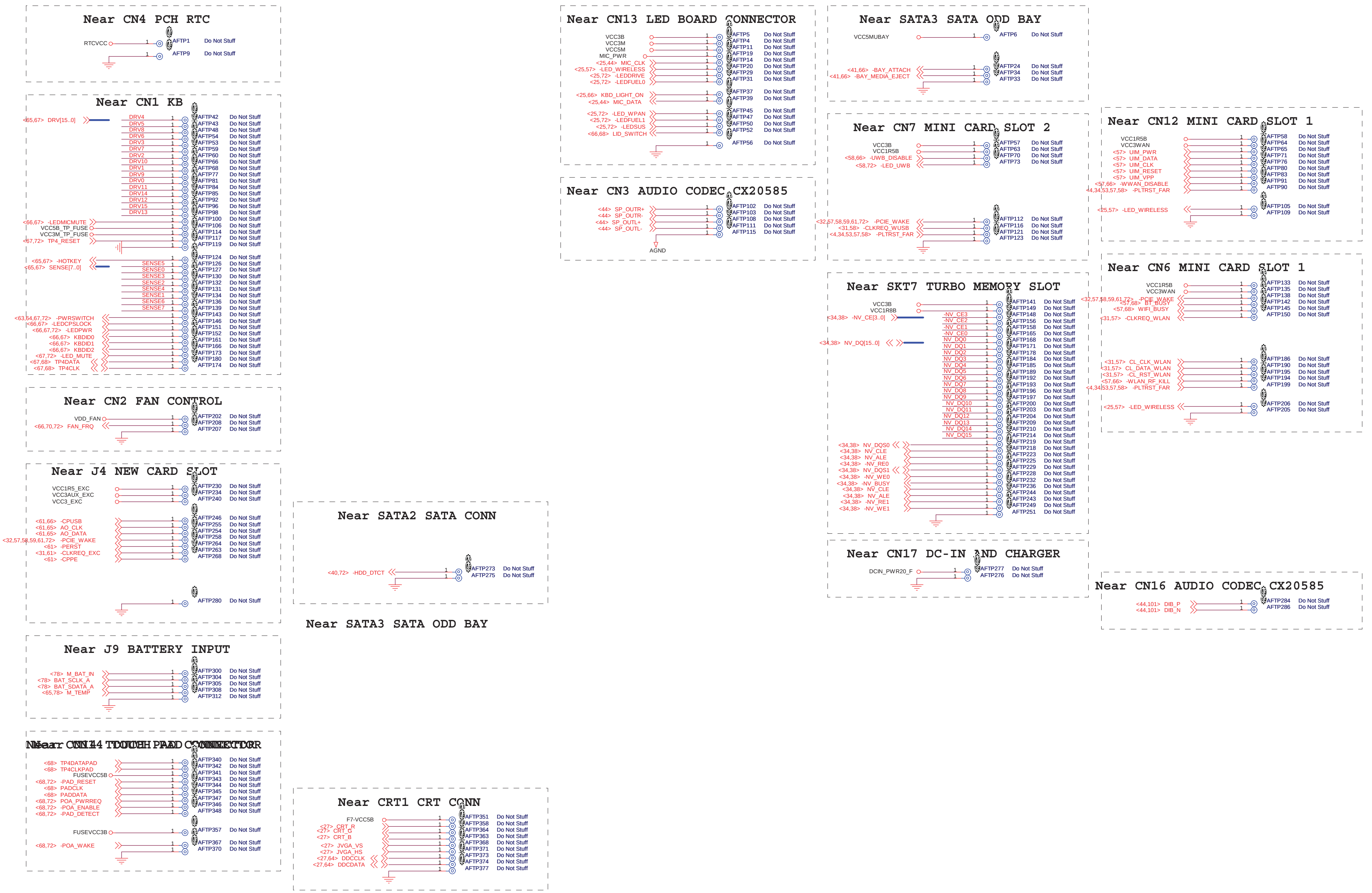

BOM1

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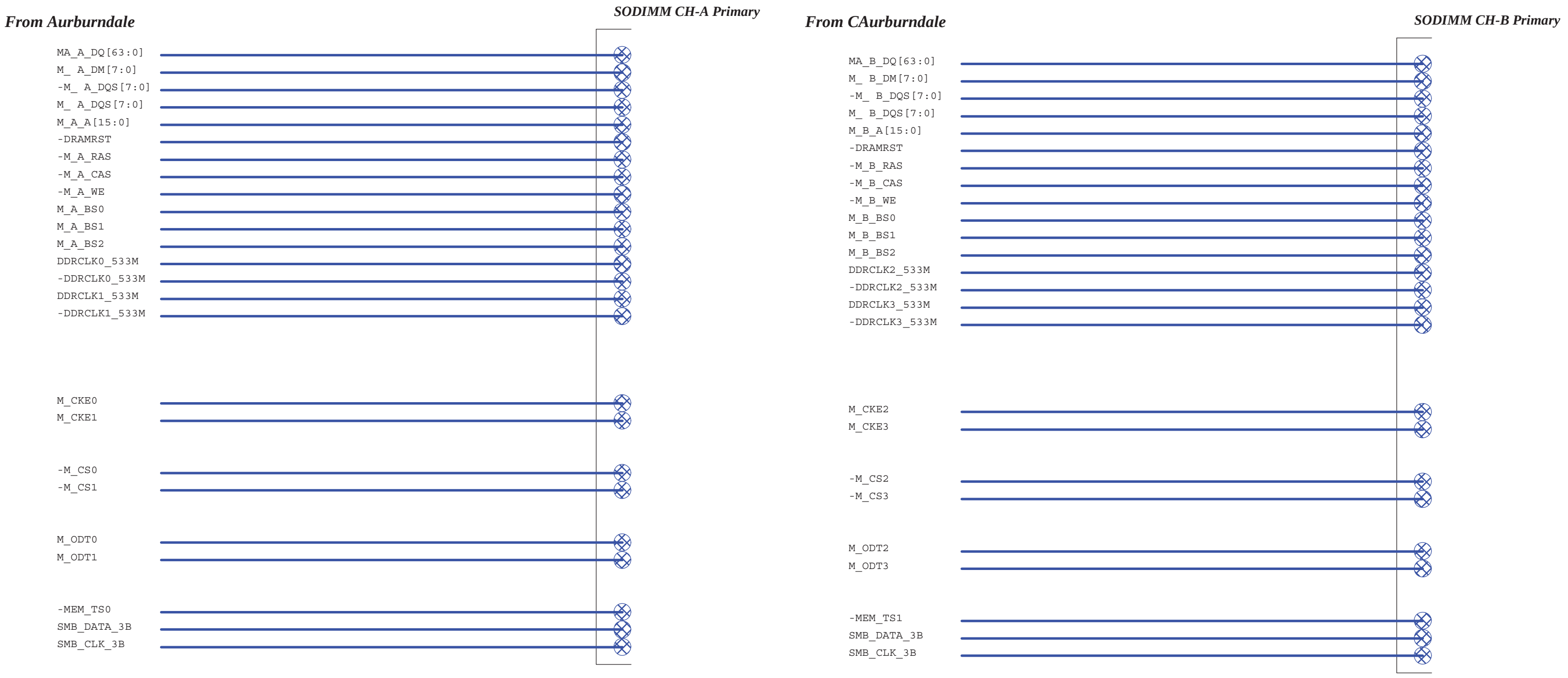
Taipei Hsien 221, Taiwan, R.O.C.

Title
TEST PAD

Size A2
Document Number Kendo-1
Rev SB

Date: Friday, March 13, 2009 Sheet 103 of 104



SODIMM IIC Address :
CH-A Primary : 50h
CH-B Primary : 51h

Pin1 on SODIM connector (VREF_DQ) only conects to DDR Voltage divider.
Clarksfield H17/J17 is left.

BOM1

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Title		
	SODIMM CONFIGRATION	
Size A2	Document Number Kendo-1-UMA	Rev SB
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